

# MOST

Media Oriented Systems Transport

Multimedia and Control  
Networking Technology

**MOST150 oPhy Sub-Spec Rev. 1.1**

**Addendum B150**

**07/2012**

**MOSTCO CONFIDENTIAL**

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## Document References

All documents which this MOST document has references to are listed here with the actual revision this document is referring to.

Number	Document	Revision
[1]	MOST150 oPhy Automotive Sub-Specification [MOST150]	Rev. 1.1

## Document History

First version 1.0-00

Change Ref.	Section	Changes
-	-	- First version, no changes

# 1 Introduction

This document is a supplement to the **MOST150 oPhy Automotive Sub-Specification** [1]. It specifies the usage of 2+0 Small Form Connector SMD package and connector interface as an option for MOST150.

## 2 Content of Addendum

### 2.1 Small Form Connector 2+0 SMD 7-Pin-Package

References for drawings related to the 2+0 Small Form Connector SMD 7-Pin-Package are listed in Table 2-1.

Drawing Code	File Name
MOST150 Small Form FOT SMD	MOST150_Small_Form_FO-Tranceiver_SMD_7Pin.TIF
	PDF Files are available on <a href="http://www.mostcooperation.com">www.mostcooperation.com</a> x indicates version number of drawing file

Table 2-1: Drawing Codes and File Names for the Small Form Connector 2+0 SMD 7-Pin-Package

The corresponding up-to-date drawings have to be applied.

#### 2.1.1 FOT Pin-out

An EOC (Tx) and OEC (Rx) shall be combined into 2 x 7-pin package being available as surface mount (SMD) package, see *Figure 2-1*. The pin-out is shown below. The printed circuit board footprint is shown in above mentioned drawings.

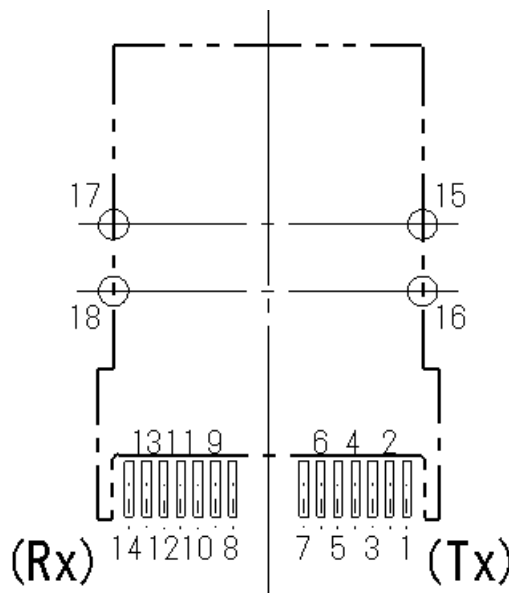


Figure 2-1: SMD 7-Pin-FOT Pin-out

## 2.1.2 OEC Signal Descriptions

The OEC illustrated in *Figure 2-1* must conform to the pin-out and signal functionality as described in Table 2-2.

Pin Name	Pin No.	Functional Description
STATUS	8	STATUS output. Logic Low when the OEC is in the On-State, Logic High when the OEC is in the Off-State
VCC_RX1	9	Power supply for the OEC.
GND_RX	10	Ground return for the OEC power supply.
RXN	11	OEC data output -. N terminal of the differential signal.
RXP	12	OEC data output +. P terminal of the differential signal.
RESERVED4	13	Connected according OEC datasheet.
VCC_RX2	14	Power supply for the OEC.

Table 2-2: Signal Description for the 7-Pin- OEC

## 2.1.3 EOC Signal Descriptions

The EOC illustrated in *Figure 2-1* must conform to the pin-out and signal functionality as described in Table 2-3.

Pin Name	Pin No.	Functional Description
/RST	1	Active-low logic input signal that disables optical output.
RESERVED3	2	Connected according OEC datasheet.
TXN	3	EOC data input -. N terminal of the differential signal.
TXP	4	EOC data input +. P terminal of the differential signal.
GND_TX	5	Ground return for the EOC power supply.
VCC_TX	6	Power supply for the EOC.
RESERVED1	7	Reserved for future use in MOST. On the PCB, connect to ground through 0-Ohm resistor.

Table 2-3: Signal Description for the 7-Pin-EOC

## 2.2 Connector Interface

Table 2-4 lists the connector interface of the Small Form Connector 2+0 SMALL.

"Nick Name"	Number of Optical Contacts	Number of Electrical Contacts		
		PIN = 0.63 mm	PIN = 1.5 mm	PIN = 2.8 mm
2+0 SMALL	2	-	-	-

Table 2-4: Small Form Connector 2+0 SMALL

Table 2-5 is a listing of all standardized connectors and indicates the drawing codes, the file names of all specified connector interfaces and the drawing date.

"Nick Name"	Drawing Code	TIFF File
2+0 SMALL	MOST-CON-2-0-SMALL	MOST-CON-2-0-SMALL-00.tif
		TIFF Files available on <a href="http://www.mostcooperation.com">www.mostcooperation.com</a>

Table 2-5: Drawing Codes and File Names of Connector Interfaces

The corresponding up-to-date drawings have to be applied.

## 3 Comment

### 3.1 Reason for Addendum

Package downsizing for MOST150 usage.

## Appendix A: List of Figures

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## 4 Appendix B: List of Tables

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