

MOST

Media Oriented Systems Transport

Multimedia and Control
Networking Technology

Compliance Verification Procedure – Physical Layer

ERRATA SHEET

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Contents

1	INTRODUCTION	7
2	ROADMAP PHYSICAL COMPLIANCE TEST	7
3	ERRATA	8

Bibliography

Number	Document
[1]	MOST Specification Framework
[2]	MOST Specification
[3]	MOST High Protocol Specification
[8]	MOST FunctionCatalog
[9]	MOST Specification Of Physical Layer
[10]	MOST Compliance Test of Physical Layer
[11]	MOST Compliance Requirements
[12]	MOST Core Compliance Test Specification

Document History

Changes

Change Ref.	Section	Changes
1V1-01	3	Section added.
1V1-02	3	Consideration of different FS: GEN 7 added.
1V1-02	3	Consideration of Phys. Spec Addendum B: GEN 8 added.
1V1-03	3	Checking connector tolerances: GEN 9 added.
1V1-03	3	Clarification Compliance testing with different sample frequencies FS: GEN 7a added.

1 Introduction

This document is a supplement to the MOST COMPLIANCE VERIFICATION PROCEDURE - PHYSICAL LAYER, Version 1.0-00 [12].

2 Roadmap Physical Compliance Test

The following tables represent the preliminary restrictions for physical compliance testing defined in The document MOST_COMPLIANCE VERIFICATION PROCEDURE_PHYSICAL LAYER 1V0-00.

Limited physical layer testing

Input Parameter for SP3	Criterion	Practicable
P _{opt} variable	(Critical) unlocks	Yes
	Coding errors	New ET needed
APWD variable	(Critical) unlocks	New stimulus needed
	Coding errors	New Stimulus & new ET needed

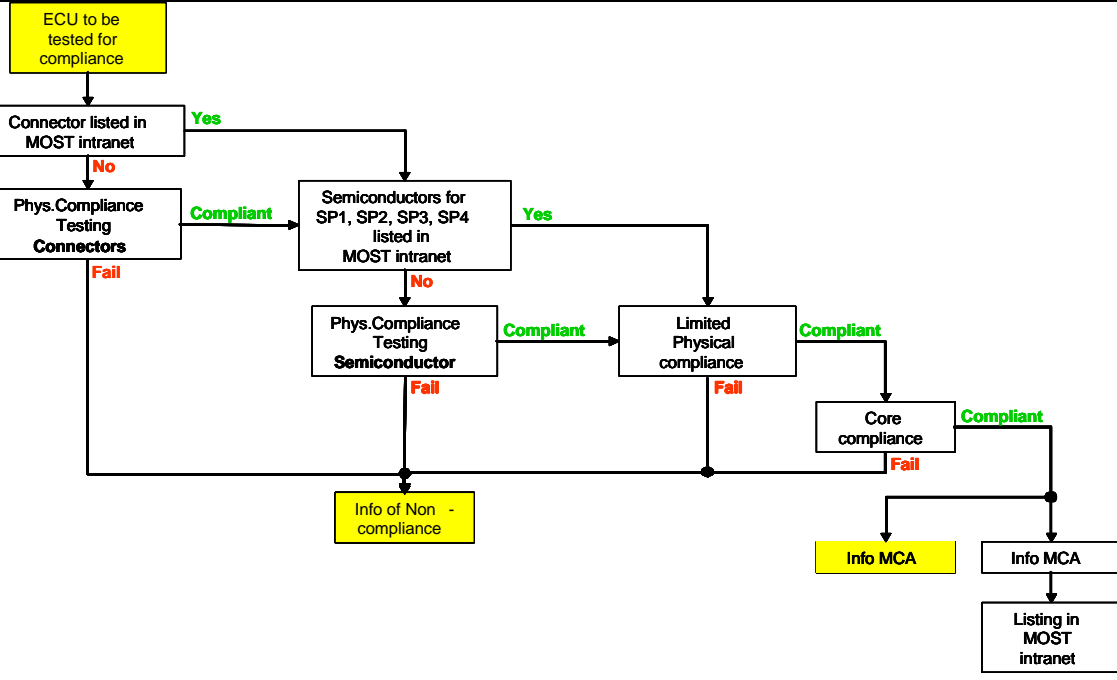
Full physical layer testing - Receiver

Input Parameter	Criterion	practicable today
P _{opt} variable	Status signal	Yes
APWD variable	Status signal	New Stimulus needed

3 Errata

Legend: → means “will be substituted by”

<p><u>Definition of “family”</u> to minimize test effort for a couple of products with many similarities. A family member is a variation of parameter invariant to MOST Compliance Verification</p> <p>e.g.</p> <ul style="list-style-type: none"> - pigtail with different fibre length in range of already tested fibre lengths - pigtail with different shielding - pigtail with different pin orientation (90° / 180°) - and supersets <p>In case a change supersedes the already tested range it has to be considered as “level 3” change.</p> <p>Note: Change of FOT implies a new family.</p>	GEN 1
<p>Treatment of Pigtail with different (electrical) connector interface:</p> <p>In case the construction does not change the principal optical path, only the mechanical/optical interface has to be tested.</p> <p>Consequently this will be seen as level 3 change.</p>	GEN 2
<p><u>Guideline for supplier in case of product changes</u> (provided by MCA).</p> <p>Changes, that may impact</p> <ul style="list-style-type: none"> - the optical path or - mechanical/optical interface <p>and which have to be considered as “level 3” changes:</p> <ul style="list-style-type: none"> - Coupling concept - Fixation of the ferrule - Fibre type - Extension of temperature range - Change of component manufacturer (e.g. FOT) - Material in the optical path - Receiver and transmitter chips / changes of FOT - Measurement Verification not performed in case of connector interface changes - (Refer also recommended Guideline AEC-Q100, p. 19, col E5, Electrical Distribution , http://www.aecouncil.com) <p>NOTE: Of course, this guideline is valid for changes during product development process, too.</p>	GEN 3
<p>Example:</p> <p>Pigtail-Family1 (with FOT1) and Pigtail- Family2 (with FOT2) have been both tested for Full Physical Compliance.</p> <p>A device using Pigtail-Family1 (with FOT1) has been tested for Limited Physical Compliance.</p> <p>Layout or circuitry or application or power supply changed?</p> <ul style="list-style-type: none"> - Yes: level 3 change - No: level 2 change 	GEN 4
<p>Substitutions of Early implementation components in a product will be treated according to Guideline of Changes (refer GEN 3)</p>	GEN 5

 <pre> graph TD Start[ECU to be tested for compliance] --> Q1{Connector listed in MOST intranet} Q1 -- Yes --> Q2{Semiconductors for SP1, SP2, SP3, SP4 listed in MOST intranet} Q1 -- No --> P1[Phys. Compliance Testing Connectors] P1 -- Compliant --> Q2 P1 -- Fail --> InfoNC[Info of Non-compliance] Q2 -- Yes --> Q3{Limited Physical compliance} Q2 -- No --> P2[Phys. Compliance Testing Semiconductor] P2 -- Compliant --> Q3 P2 -- Fail --> InfoNC Q3 -- Compliant --> C1[Core compliance] Q3 -- Fail --> InfoNC C1 -- Compliant --> Q4{Listing in MOST intranet} C1 -- Fail --> InfoMCA1[Info MCA] Q4 --> InfoMCA2[Info MCA] </pre> <p>This flow is valid for Compliance testing of a device.</p>	GEN 6
<p>The procedure for Compliance testing with different sample frequencies FS will be as follows:</p> <ul style="list-style-type: none"> • Limited Physical Compliance: Each supported FS has to be tested, i.e. a device supporting FS 44,1 kHz and FS 48 kHz has to be tested for both frequencies. The supported frequencies will be marked in the MCPL. • Full Physical Compliance: <ul style="list-style-type: none"> ○ For FOTs each supported FS has to be tested, i.e. a FOT supporting FS 44,1 kHz and FS 48 kHz has to be tested for both frequencies. The supported frequencies will be marked in the MCPL. ○ As Pigtails will not be affected by the sample frequency FS the test with one FS will be sufficient. 	GEN 7
<p>Clarification Compliance testing with different sample frequencies FS: “For electronic control units (ECU) the limited physical compliance will be done at the specified frame sampling rate. If the FOT is not tested for Full Physical Layer Compliance at this frame sampling rate, at least the data sheet of the FOT must cover this sampling rate. If the sampling rate of the ECU is not covered by the data sheet of the FOT, the ECU is not compliant because the components are operated outside of the FOT specification.</p> <p>The compliance testing of pigtails is not affected by the sampling rate, because for the Full Physical Layer test of pigtail subset, one frame sampling rate is sufficient.”</p>	GEN 7a
<p>With release of MOST Physical Layer Rev. 1.1 Addendum B the overshoot behaviour has to be adapted and considered accordingly in Compliance Testing.</p>	GEN 8
<p>Checking connector tolerances:</p> <p>“To verify the mechanical interface according to the Compliance Verification Procedure three measurement values of the obligatory drawings will be taken and described in the test report accordingly. The corresponding measurement uncertainty should be better than 0.1mm.”</p>	GEN 9

Notes: