

MOST

Media Oriented Systems Transport

Multimedia and Control
Networking Technology

MOST Core Compliance Test Specification

Rev 1V3.1

09/2010

Version 1.3.1-00



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Document References

All documents which this MOST document have references to are listed here with the actual revision this document is referring to.

Test Cases according to MOST Specification Rev. 2.4:

Number	Document	Revision
[1]	MOST Compliance Requirements Specification	2V1
[2]	MOST Dynamic Specification	1V2
[3]	MOST Specification	2V4
[4]	MOST FunctionBlock "NetBlock"	2V4
[5]	MOST FunctionBlock "NetworkMaster"	2V3.3
[6]	MOST FunctionBlock "Enhanced Testability"	2V5
[7]	MOST FunctionBlock "General FBlock"	2V3.3

Test Cases according to MOST Specification Rev. 2.5:

Number	Document	Revision
[1]	MOST Compliance Requirements Specification	2V1
[2]	MOST Dynamic Specification	1V3
[3]	MOST Specification	2V5
[4]	MOST FunctionBlock "NetBlock"	2V5
[5]	MOST FunctionBlock "NetworkMaster"	2V5.1
[6]	MOST FunctionBlock "Enhanced Testability"	2V6.1
[7]	MOST FunctionBlock "General FBlock"	2V5.1

Document History

Changes MOST Core Compliance Test Specification 1V3 to MOST Core Compliance Test Specification 1V3.1

Change Ref.	Section	Changes
1V3.1_001	all	Fusion of Core Compliance Test Specifications 1V2 and 1V3 to MOST Core Compliance Test Specification 1V3.1
1V3.1_002	Para 1	This MOST Core Compliance Test Specification covers MOST Spec 2V4 and 2V5
1V3.1_003	Manufacturer information list	Input from ERRATA: LowLevel retries and MidLevel retries: Use these values in general for the tester.
1V3.1_004	2.1.0-1 2.1.0-2	Input from ERRATA: Note: All commands from DUT (OPTYPE 0x0 .. 0x8) will be ignored for all Fblocks. Note: For Netblock and NMW Fblock, OPTYPES 0x09 ... 0xF not tested. Reaction on "ACK-OPTypes" may also be Error (instead of ErrorACK).
1V3.1_005	2.1.1-1	Input from ERRATA: Add block after "DUT in SleepMode": "Wait 10 sec".
1V3.1_006	2.1.2-4	Input from ERRATA: a) Adapt value of interest: T_boundary, SBC register → change of NetInterface state b) Adapt test description: The DUT has to change to NormalOperation within t_Boundary → The DUT has to change to NormalOperation.
1V3.1_007	2.1.3-1 2.1.1-4 2.1.3-4	Input from ERRATA: Replace t_manufacturer_0x00 by t_DeadLockMid
1V3.1_008	2.4.1-1	Input from ERRATA: Note: The wake-up attempt must consist of: (1) MOST signal on (2) Waiting for t_Config_min (3) MOST signal on (4) Wait for t_Restart_min → Note: The wake-up attempt must consist of: (1) MOST signal on (2) Waiting for t_Config_min (3) MOST signal off (4) Wait for t_Restart_min
1V3.1_009	2.4.1-2	Input from ERRATA: Add in note: In order to assure that wakeup duration has expired the tester is allowed to wait additionally for t_DeadLockMid.
1V3.1_010	2.4.1-9	Input from ERRATA: t_WaitAfterNCE(max) will not be checked, use t_DeadLockShort instead. <i>Update: use t_DeadLockMid (to be harmonized with Core Compliance Test Spec 3V0)</i>
1V3.1_011	2.4.2-3	Input from ERRATA: „DUT not ok (1): DUT disturbs communication before U_Low is reached“ → “DUT not ok (1): DUT disturbs communication before U_Low is reached or before MOSTsignal is switched off”

Change Ref.	Section	Changes
1V3.1_012	2.5-3	<p>Input from ERRATA:</p> <ul style="list-style-type: none"> a) Tester1 has to behave like normal MOST device regarding RBD. → t_diag_master of MOST tester = t_diag_master_min b) Add note at Statement „Close ring...“: If DUT is slave device, it is allowed to switch off MOST signal once for a short period of time (t_Restart) due to reconfiguration during “Diag_M2”. If it fails to stop generating MOST signal the second time → DUT not ok (1)
1V3.1_013	2.5-8	<p>Input from ERRATA:</p> <p>Tester1 has to behave like normal MOST device regarding RBD. → t_diag_master of MOST tester = t_diag_master_min</p> <p>(revised by change ref. 1V3.1_052)</p>
1V3.1_014	2.6.2-3b	<p>Input from ERRATA:</p> <p>Inquiry t_WaitForAnswer (min) + T_DelayCfgRequest1(min) <= t_test <= t_WaitForAnswer (max) + T_DelayCfgRequest1(max) → Inquiry T_DelayCfgRequest1(min) <= t_test <= t_WaitForAnswer (max) + T_DelayCfgRequest1(max)</p> <p>Inquiry t_WaitForAnswer (min) + T_DelayCfgRequest2(min) <= t_test <= t_WaitForAnswer (max) + T_DelayCfgRequest2(max) → Inquiry t_DelayCfgRequest2(min) <= t_test <= t_WaitForAnswer (max) + T_DelayCfgRequest2(max)</p> <p>Admit 20 resp. 19 loops (retries). Reason: MOST Spec not completely accurate in this definition (MOST Spec Rev. 2.5, section 3.8).</p> <p>Procedure (superseding (8)): Perform Wakeup → DUT checks Tester1 within t_DeadLockMid? → → Perform Wakeup → Wait manufacturer dependent t_WaitForApplication 2. Tester 1: Open bypass → DUT checks Tester1 within t_DeadLockMid? → Reason: In multi-node devices, at the startup there may happen very much traffic with many Fblocks. This may lead to infringement of time intervals due to OEM Low level retry requirements.</p> <p>Experimental Setup: a) tester setup #1 → tester setup #2 b) tester #2 behaves like normal MOST device. Reason: In case of MOST signal wake-up, tester #2 is needed to perform the wake-up.</p>
1V3.1_015	2.6.2-4b	<p>Input from ERRATA:</p> <p>Add note at block “Tester1: Send CentralRegistry.Get to DUT”: - Wait for manufacturer dependent time until all DUT Fblocks have registered.</p>

Change Ref.	Section	Changes
1V3.1_016	2.6.4-9	<p>Input from ERRATA: Exception: DUT is allowed to send "Deallocate.All!" → Exception: DUT is allowed to send "Deallocate.All" and Debug Messages (to 0xFF0)</p> <p>Note: Tester (NWM) must not send FblockIDs.Get to DUT. → Note: Tester (NWM) must not send any message to the DUT.</p>
1V3.1_017	2.6.4-10	<p>Input from ERRATA: Add note: Test not performed for Netblock and ET. NWM may respond with InstID 0x00 according to MOST Spec.</p> <p>Adapt inquiry in flowchart: Received FktIDs.Status or FktIDs.Error from DUT? → Received FktIDs.Status from DUT?</p>
1V3.1_018	2.8.1-1	<p>Input from ERRATA: Test not applicable for MOST 2V4 devices</p>
1V3.1_019	2.8.3-1b	<p>Input from ERRATA: Refer to left branch. There is the following procedure: 1 send Configuration.Status(NotOk) to DUT 2 Request Notification Matrix from DUT → 1 send Configuration.Status(NotOk) to DUT 2 wait for t_DeadLockShort 3 send Configuration.Status(OK) to DUT 4 Request Notification Matrix from DUT"</p>
1V3.1_020	2.8.3-2	<p>Input from ERRATA: DUT responds with empty list? → DUT responds with empty list or any NotificationCheck error?</p> <p>Delete note in flowchart: "Note: Test has to be performed with every single Fblock of the DUT."</p> <p>Add note at inquiry "DUT responds with list, containing all functions that support notification?": Note: If the Fblock does not contain any functions that support notification, the DUT is allowed to respond with empty list or any NotificationCheck error.</p>
1V3.1_021	2.8.4-2	<p>Input from ERRATA: Add to note: In case the MessageBufSize is dynamic (indicated by value 0) the length to be sent by the tester is 44 bytes (4 segments).</p>
1V3.1_022	3.0-1	<p>Input from ERRATA: Adapt comment 2 to DUT NOT OK(2): If DUT reports sink / source relevant FktIDs, the manufacturer has to indicate that the relevant Fblock contains sink /source (and vice versa) → If DUT reports sink / source relevant FktIDs, the manufacturer has to indicate that the relevant Fblock contains sink /source (and vice versa) with the following exception: In case functions are only implemented in principle and source resp. sink count is zero the result is set correspondingly DUT ok (1): Fblock neither contains sink nor source according to manufacturer info or DUT ok (2): Fblock contains source according to manufacturer info or DUT ok (4): Fblock contains sink according to manufacturer info</p>

Change Ref.	Section	Changes
1V3.1_023	3.1-3 3.1-4 3.1-5 3.1-6	Input from ERRATA: In cases where the bandwidth of sink cannot be determined because of the data type, the test case is skipped for this sink.
1V3.1_024	3.1-3 3.1-4	Input from ERRATA: Adapt Note: Test to be performed with every single SinkNumber, supported by the DUT. → Test to be performed with every single SinkNumber, supported by the DUT but Disconnect before in order to avoid resource overflow.
1V3.1_025	3.2-5	Input from ERRATA: Monitoring of ResourceAllocate and ResourceDeallocate will be omitted.
1V3.1_026	3.2-6	Input from ERRATA: Add in box "Free enough bandwidth ...": Wait t_DeadlockShort
1V3.1_027	3.2-13	Input from ERRATA: 1. TEST_GSO_SourceConnect → TEST_GSO_SourceDisconnect 2. SourceConnect.StartResult → SourceDisconnect.StartResult 3. SourceConnect.Result → SourceDisconnect.Result 4. Ignore parameter channelist and SrcDelay.
1V3.1_028	3.2-3 3.2-6 3.2-8	Input from ERRATA: Add in note that ResourceAllocates resp. ResourceDeAllocates can occur several times. The evaluation/check must regard all Resource(De)Allocates, not just the first one.
1V3.1_029	3.2-4 3.2-7 3.2-9	Input from ERRATA: Monitoring of ResourceAllocate and ResourceDeallocate will be omitted.
1V3.1_030	2.1.1-4	Test case splitted in two versions (MOST Spec 2V4 and 2V5) to consider AbilityToWake and PermissionToWake.
1V3.1_031	2.1.3-1	Test case splitted in two versions (MOST Spec 2V4 and 2V5) to consider AbilityToWake and PermissionToWake.
1V3.1_032	2.1.3-4	Test case splitted in two versions (MOST Spec 2V4 and 2V5) to consider AbilityToWake and PermissionToWake.
1V3.1_033	2.4.1-1	Test case splitted in two versions (MOST Spec 2V4 and 2V5) to consider AbilityToWake and PermissionToWake.
1V3.1_034	2.4.1-2	Test case splitted in two versions (MOST Spec 2V4 and 2V5) to consider AbilityToWake and PermissionToWake.
1V3.1_035	Para 3.1	Distinguish MOST2V4:AbilityToWake and MOST2V5:PermissionToWake
1V3.1_036	2.8.4-7	Input from ERRATA: Adapt note: "The size of the message must not exceed the max. supported buffer size of the DUT." → "In case of fix buffer size, the size of the message must not exceed the max. supported buffer size of the DUT." Adapt note: In case of dynamic buffer size, Segmentation Error(0x02) is also allowed. In this case the test must be aborted to avoid subsequent errors. → In case of dynamic buffer size, Segmentation Error(0x02) or (0x04) are also allowed. In this case the test must be aborted to avoid subsequent errors.
1V3.1_037	2.3.2-3	Input from ERRATA: Insert before Send ShutDown.Start (Execute) 1. "Send ShutDown.StartAck(0x01, Query)" 2. "wait for t_Suspend"

Change Ref.	Section	Changes
1V3.1_038	2.7-1	Flowchart updated: "Send message to..." → " Send GroupAddress.Get to ..." (D100-3)
1V3.1_039	2.6.2-4b	Flowchart updated: Add after inquiry "Configuration.Status(Ok/New) received within t_DeadLockShort?" in path "no" block "send CentralRegistry.Get to DUT" and check for consistency and continue the subsequent checks (D100-2)
1V3.1_040	3.1-5	Deleted part "Mute.Get" as there is no requirement in MOST Specification that "muting" and "disconnect" correspond (D100-3)
1V3.1_041	2.3.3-5	Revised flowchart regarding to timer t_WaitAfterOvertemperatureShutdown (D102-1)
1V3.1_042	General Notes; (detect "Sleep Mode")	Mechanism how to detect sleep mode revised. (D102-2)
1V3.1_043	2.4.2-1	Update Note: "A test message has to be sent at every 0.5V step." → "A test message has to be sent at every step." (D104-1)
1V3.1_044	2.4.2-3	Delete Note: "Tolerances of manufacturer have to be considered. If U_Low has hysteresis, the higher value has to be used!" (D104-2)
1V3.1_045	Document References	MOST Compliance Requirements Specification: 2V0 → 2V1 MOST FunctionBlock "NetworkMaster": 2V3.3; 2V5 → 2V3.3; 2V5.1 MOST FunctionBlock "Enhanced Testability": 2V5 → 2V5; 2V6.1 MOST FunctionBlock "General FBlock": 2V3.3; 2V5 → 2V3.3; 2V5.1 Split into two separate lists (2V4 and 2V5)
1V3.1_046	Introduction	Deleted: "This Core Compliance Test Specification is based on MOST Specification 2V4 and MOST Specification 2V5. It requires FBlock ET 2.5."
1V3.1_047	para 2.1.2	This version of MOST Core Compliance Test Specification requires implementation of FBlock ET V2.5 --> This version of MOST Core Compliance Test Specification requires implementation of FBlock ET 2V5 or 2V6.1
1V3.1_048	Changes of DUT	Use wording from MOST Core Compliance Test Specification 3V0
1V3.1_049	Product variants	Use wording from MOST Core Compliance Test Specification 3V0
1V3.1_050	2.6.1-1	Add note to block "Get address from DUT": FBlockIDs.Get could be used to get the address of the DUT. --> FBlockIDs.Get (received from DUT) could be used to get the address of the DUT. (D106-1)
1V3.1_051	3.1-8	Delete test case TEST_GSI_Mute
1V3.1_052	2.5-8	<ol style="list-style-type: none"> 1. Experimental setup: Tester 1 in master mode. 2. Revision of change ref. 1V3.1_013; Change note of test description to: "Tester 1 has to behave like normal MOST device regarding to RBD procedure for TimingSlave (t_Diag_Slave of tester1 = t_Diag_Slave_Max). 3. Add note to flowchart: "Tester 1 has to perform RBD procedure that is specified for TimingSlave."

Abbreviations

CR	Central Registry
DR	Decentral Registry
DUT	Device Under Test
I/F	Interface
MOST	Media Oriented Systems Transport
NCE	Network Change Event
NO	NormalOperation
NWM	Network Master
PM	Power Master
RE	Routing Engine
TM	Timing Master

Overview of Core Compliance Tests (sorted by test number)

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1 Introduction

The MOST (Media Oriented Systems Transport) system has been developed for communication systems with the need of high bandwidths like e.g. radio, phone, sound systems, CD, and voice control. Its architecture is based on a ring communication. To ensure a reliable operation of the whole network under all conditions, every single device that is connected to the ring has to fulfill the specification requirements. Devices of different manufacturers have to ensure correct operation in the overall system environment.

In order to verify the behavior of the devices, they have to pass different kinds of tests that check every single core function regarding the network management of the MOST system.

The objective of this document is the definition of all core functions and all tests required in order to prove compliance of a device with the MOST specification.

This Core Compliance Test Specification is based on MOST Specification 2V4 and MOST Specification 2V5.

2 General Items

2.1 Definitions

2.1.1 DUT

The DUT is the entire device under test, including one external MOST interface. A DUT has at least one MOST node (but it could contain more than one). All tests and timings, specified by the MOST Core Compliance Tests, are always related to the external MOST interfaces.

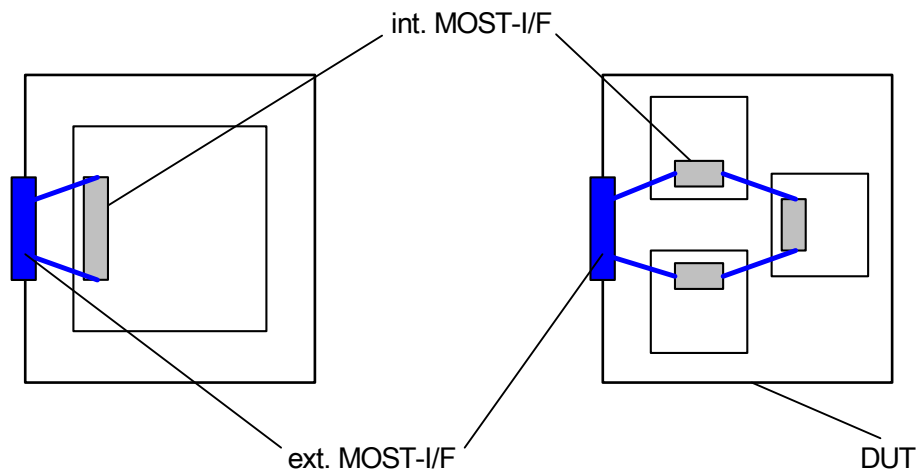


Figure 2-1 DUT with one internal MOST device and a DUT with three internal MOST devices.

If a device contains more than one node (secondary nodes), some tests could end with different results, i.e. one for every node. For example, the “Segmented Message Rejection test” could end with two different results as one node supports segmented messages and the other one does not support segmented messages.

2.1.2 FBlock "EnhancedTestability"

This FBlock is used to trigger sequences which have to be tested in the MOST Compliance Test but which are normally triggered by a project specific, sometimes complicated, mechanism. Due to the nature of this FBlock neither notification nor processing messages will be implemented. The FBlock must be initialized every time the NetOn state is reached. The FBlock is only available during NetOn. All properties are reset to their default state when entering NetOn, if not mentioned otherwise. The functions in this FBlock describe a general interface for starting functionality partly implemented in the application, partly in the Network Services. If an application callback returns wrong or unexpected values the FBlock sends a “device malfunction” error message (code 0x0B).

In case the FBlock ET returns wrong figures, e.g. wrong voltage levels, the corresponding test result will be indicated as "Not ok". Due to effort reasons, compliance testing relies on FBlock ET, the test houses has to inform the supplier correspondingly.

This version of MOST Core Compliance Test Specification requires implementation of FBlock ET 2V5 or 2V6.1

2.1.3 Changes of DUT

MOST Compliance Specification defines three classes of changes of a compliant device relative to the potential impact of change and amount of re-qualification required.

For Core Compliance Tests, the following rules are defined how to treat changes of DUT:

As the manufacturer is responsible for the compliance of a device with the standard, he is also responsible for re-testing in case of changes.

Beyond, the system integrator may request additional compliance verification.

- Re-Test is mandatory in case of implementation of new MOST Specification version which leads to substantial changes in the Core Compliance Test Specification.
- In case of changes the following recommendation for re-test is given:
 - Architectural functional changes
 - Extensions or reduction of functions that are in the scope of compliance verification.
 - Changes of dynamic behaviour which affect timer boundaries and constraints of MOST Specification

2.1.4 Product variants of DUT / family

To minimize test effort for product variants with many similarities, "family" is defined. A family member is a variation of parameters invariant to MOST Compliance Verification.

Such variants can be considered as equivalent "level 1" or "level 2" changes (refer to MOST Compliance Requirements Specification, para 3.9.2 "Guidelines for handling of product changes").

In case a change supersedes the already tested range it has to be considered as "level 3" change (refer to MOST Compliance Requirements Specification, para 3.9.2 "Guidelines for handling of product changes") and consequently cannot be considered as a family member.

Families for core are based on the same hardware. Sufficient coverage of variants must be tested so that each FBlock has been involved. The most complex variants should be preferred for testing.

2.1.5 Multi Node Devices

A DUT with more than one MOST node is defined as Multi Node Device.

MOST25

If a Multi Node Device consists of one primary and (at least) one secondary node, only the primary node will be tested.

If a Multi Node Device consists of several independent nodes, each node will be treated individually:

1 TM and (at least) 1 slave node:	M: All tests for TM S: All test for slave but not "All devices except TM"
1 Master (NWM) node and (at least) 1 slave node:	M: All tests for NWM S: All test for slave but not "All devices except NWM"
1 Master (PM) node and (at least) 1 slave node:	M: All tests for PM S: All test for slave but not "All devices except PM"
1 Master (TM/NWM) node and (at least) 1 slave node:	M: All tests for NWM/TM S: All test for slave but not "All devices except TM" but not "All devices except NWM"
1 Master (TM/PM/NWM) node and (at least) 1 slave node:	M: All tests for PM /NWM/TM S: All test for slave but not "All devices except PM" but not "All devices except TM" but not "All devices except NWM"
Several slave nodes:	S: All test for slave

The DUT passes the test successfully if all testable nodes of the DUT has passed the test successfully.

3 Procedures of MOST Core Compliance Tests

The MOST Core Compliance Tests are based on the Core Functions, identified at paragraph "2 Identification of MOST Core Functions".

3.1 General Notes

Wake-up

In some cases, the DUT needs some pre-conditions (e.g. active CAN interface) for wake-up. The pre-conditions depend on the manufacturer. These pre-conditions have to be provided to the DUT before start of testing.

MOST2V4:

If the DUT has to perform a wake-up by itself (e.g. via AutoWakeup), the "AbilityToWake" has to be set to "TRUE".

MOST2V5:

If the DUT has to perform a wake-up by itself (e.g. via AutoWakeup), the "PermissionToWake" has to be set to "TRUE".

If the AutoWakeup is triggered via FBlock ET, the DUT does not enter the normal SleepMode before performing the wake-up. The DUT enters "NetInterface PowerOff". This mode cannot be detected by monitoring the power consumption of the DUT. With entering state "NetInterface PowerOff", the DUT switches off the MOST signal at its output.

Timer definitions

Names of Timer, used by this Core Compliance Test Specification are based on the MOST Specification. Compared to the MOST Specification, an underscore is used to separate the timer name.

Example:

Timer of MOST Specification	t _{Config}
Corresponding timer of Core Compliance Test Specification	t_Config t_Config_min t_Config_max

Manufacturer timings

In some cases, the timing of a DUT depends on the manufacturer. In this case, the timing can be read out from the FBlock ET (ManufacturerTimings). One timer value is accessible:

- Time between (external) wakeup and MOST signal active.
In the test procedures, this timer is called *t_manufacturer_0x00*.

Deadlock prevention

To prevent a deadlock during testing, three timeouts have been defined:

- t_DeadLockShort = 1s
- t_DeadLockMid = 20s
- t_DeadLockLong = 5 min

These timeouts are not defined by MOST Specification. They are only relevant for compliance testing. If a DUT fails a test due to timeout of a deadlock timer, the test could be performed again (after consultation with manufacturer) with extended timeout for deadlock timer. Extended timeouts of deadlock timer have to be indicated in the test results.

Uninitialized node address

This test specification uses the variable "uninitialized_node_address" to identify the address of an uninitialized node. Use always the value that is specified in the MOST Specification, the DUT is based on.

Address of tester

Default address of the tester is default logical address corresponding to the ring position. If DUT uses an address normally used by a tester (e.g. in case of supplier, using static addresses in dynamic address range), the tester has to use another address from free address range).

DUT Manufacturer Information List

This list contains all information that have to be provided by the device manufacturer for compliance testing. It also includes remarks and references to corresponding Core Compliance Test Cases.

MCTHs can use information from this list to support the manufacturers providing test relevant information.

Information, normally stored in FB ET, are not included in this table. Description of this information can be derived directly from FBlock ET.

Item / Property	Note / Remark	Reference to Core Compliance Test Case
Device supports SleepMode:	<p>Yes: The device has to reduce its power consumption below threshold before timeout expires</p> <p>No: The reduction of power consumption is not detectable</p> <p>Manufacturer has to provide:</p> <ul style="list-style-type: none">- Does DUT support SleepMode- Power consumption in SleepMode- Timeout for SleepMode (which period of time has to be exceed from "MOST signal off at Rx" until DUT enters SleepMode resp. reduces power consumption)	<p>para 3.1</p> <p>2.3.2-3 (ShutDown.Start(Execute) test</p>

Item / Property	Note / Remark	Reference to Core Compliance Test Case
MOST2V4: DUT has implemented the "AbilityToWake"	Yes: The DUT supports AbilityToWake flag of NetBlock and is able to suppress any wake-up. No: The DUT does not have a valid implementation. (e.g. AbilityToWake.SetGet(Off) responds <>Status (Off))	para 3.1 (DUT: SleepMode)
Wakeup preconditions	Preconditions that have to be applied to the DUT for wake-up (e.g. active CAN interface) Additionally information whether DUT needs special conditions during operation (e.g. KL15) to stay in normal operation.	para 3.1
DUT uses static node address in dynamic address range	If the DUT uses a static node address, that is in the specified dynamic address range, the node address has to be provided	para 3.1 2.6.4-4 (Address Re-Initialization test)
Free address range	Logical node address that can be used by the tester during testing.	2.6.4-3 (NWM Address Storage test)
Free FBlock range	FBlocks that can be used by the tester during testing	
Wakeup method: DUT is wakeable via MOST signal	DUT supports optical wakeup according to MOST Specification (wakeup via MOST signal)	2.1.1-1 (Signal On Test) 2.1.1-5 (Bypass test) 2.2.1-5 (Critical unlock test) 2.5-7 (Wakeup after RBD test)
Wakeup method: DUT is electrical wakeable	DUT supports electrical wakeup. How to generate electrical wakeup (e.g. signal form and duration)	2.2.1-5 (Critical unlock test) 2.5-7 (Wakeup after RBD test)
Hysteresis (U_Normal <-> U_Critical)	Minimum hysteresis between voltage levels U_Normal and U_Critical	2.4.2-1 (Critical Voltage test)
Tolerances of voltage levels	Within which tolerances the DUT detects voltage levels U_Critical, U_Normal and U_Low.	2.4.2-3 (Low Voltage test)
LowLevel retries and MidLevel retries	LowLevel retries and MidLevel retries should not be considered. Number and Timing of LowLevel and MidLevel retries has to be provided by manufacturer. Use these values in general for the tester.	2.6.2-3b (Device Ignore test (b))
Maximum delay, the DUT could register own FBlocks into its CR	Period of time, the DUT needs to register own FBlocks into its CR after Configuration.Status(Ok)	2.6.2-4b (Device Integration test (b))
DUT supports segmented messages	DUT is able to send and receive segmented messages	2.8.4-2 (Segmented Message Sending test) 2.8.4-3 (Segmented Message Error test) 2.8.4-7 (Message Segmentation Buffer test) 2.8.4-8 (Parallel Segmented Message Reception test)
Multi Node Device	If DUT contains more than one node, the following information should be provided: - How many nodes does the DUT contain - Topology of DUT (position of PM, TM, NWM and/or Secondary Node, if applicable)	

Item / Property	Note / Remark	Reference to Core Compliance Test Case
DUT frequency	If DUT is not TM, the tester has to provide MOST signal with the correct frequency (44.1kHz or 48.0 kHz).	
Delay between connection to power and detection wakeup	Potentially, the tester has to wait for a short period of time between connecting the DUT to power and generating MOST signal to wake-up the DUT. Otherwise, the DUT fails to detect wake-up event.	
List of FBlocks, containing sink and/or source functionality	The list has to consider all FBlocks, reported by FBlockID.Status	3.0-1 (TEST_GSI_GSO_Identification)
Sink devices: List of all supported sink numbers with DataType and DataDescription		3.1-1 (TEST_GSI_SinkInfo) 3.1-3 (TEST_GSI_Connect) 3.1-4 (TEST_GSI_Connect_Repeat) 3.1-5 (TEST_GSI_DisConnect) 3.1-6 (TEST_GSI_DisConnect_Repeat) 3.1.8 (TEST_GSI_Mute)
Source devices: List of all supported source numbers with DataType and DataDescription		3.2-1 (TEST_GSO_SourceInfo) 3.2-3 (TEST_GSO_Allocate) 3.2-4 (TEST_GSO_Allocate_Reperat) 3.2-5 (TEST_GSO_Allocate_NoChAvail) 3.2-6 (TEST_GSO_Allocate_ReqChAvail) 3.2-7 (TEST_GSO_Allocate_WrongSourceNr) 3.2-10 (TEST_GSO_SourceConnect) 3.2-11 (TEST_GSO_SourceConnect_Repeat) 3.2-14 (TEST_GSO_SourceActivity)
Source devices: ChannelList		3.2-3 (TEST_GSO_Allocate)
Source devices: DUT supports allocate mechsnm		3.2-3 (TEST_GSO_Allocate) 3.2-4 (TEST_GSO_Allocate_Reperat) 3.2-5 (TEST_GSO_Allocate_NoChAvail) 3.2-6 (TEST_GSO_Allocate_ReqChAvail) 3.2-7 (TEST_GSO_Allocate_WrongSourceNr) 3.2-8 (TEST_GSO_DeAllocate) 3.2-9 (TEST_GSO_DeAllocate_Repeat)
Source device: DUT supports source connect mechanism		3.2-10 (TEST_GSO_SourceConnect) 3.2-11 (TEST_GSO_SourceConnect_Repeat) 3.2-12 (TEST_GSO_SourceDis-connect) 3.2-13 (TEST_GSO_SourceDis-connect_Repeat)
DUT supports SourceActivity		3.2-14 (TEST_GSO_SourceActivity)

Item / Property	Note / Remark	Reference to Core Compliance Test Case
NormalOperation: Delay until properties are available after Configuratin.Status(Ok)		2.8.3-1a (Notification Matrix Storage test (NWM)) 2.8.3-1b (Notification Matrix Storage test (Slave)) 2.8.3-2 (NotificationCheck test) 2.8.3-7 (Notification Matrix Double Entry test) 2.8.3-10 (Notification Error test)

Further definitions

States of DUT

DUT: NormalOperation	
Def.: NetBlock must be available "DUT: Normaloperation" equal to "DeviceNormalOperation" of MOST Spec 2V5	
Effectuate state of DUT	Detect state of DUT
<p>a) DUT is slave device: Tester has to behave according to MOST Specification and start the system normally:</p> <ul style="list-style-type: none"> - Wait for DUT opens its bypass (MPR = MPR_nominal) - Sending Configuration.Status(NotOk) - perform system scan (including retries in case of invalid address of DUT) - Sending Configuration.Status(Ok) <p>b) DUT is NWM device: Tester has to behave like a normal slave device. It has to process and respond all requests from DUT to enable DUT entering Configuration.Status(Ok). The tester has to respond network scan by DUT with empty FBlock list. (alternatively, the tester could register some FBlocks). Additionally, the tester has to wait for relevant node opens its bypass (MPR = MPR_nominal) in case the DUT is a Multi Node Device.</p>	<p>a) DUT is slave device: DUT has to respond to FBlockIDs.Get</p> <p>b) DUT is NWM device: DUT has to respond to FBlockIDs.Get</p>

DUT: SleepMode	
Effectuate state of DUT	Detect state of DUT
<p>MOST2V4:</p> <p>a) DUT is power master: - Set AbilityToWake = "False" - Switch off MOST signal</p> <p>b) DUT is power slave: - Set AbilityToWake = "False" - Switch off MOST signal</p> <p>Note: For devices that do not implement AbilityToWake-Function (despite Coordination Area), the following holds:</p> <p>I. The answer to AbilityToWake.SetGet need not be evaluated.</p> <p>II. a) The supplier must specify how to suppress a wake-up condition for shutdown procedure. b) If such a suppression is not applicable in the scope of compliance testing, the precondition SleepMode cannot be established and the test case will be passed as "DUT ok". This has to be remarked in the test report correspondingly.</p>	<p>The manufacturer has to provide:</p> <ul style="list-style-type: none"> - whether DUT supports SleepMode - Timeout for SleepMode (which period of time has to be exceeded from "MOST signal off at Rx" until DUT enters SleepMode) - Threshold of current for SleepMode detection <p>a) DUT is power master: If DUT supports SleepMode: Monitoring power consumption. The current has to reach or drop below value, specified by manufacturer. If DUT generates MOST signal at Tx although timeout expires, the test will be stopped ("DUT not ok: No SleepMode possible"). The timer for timeout will be started as soon as DUT switches off MOST signal at Tx for the first time. If current does not reach or drop below specified threshold within timeout (specified by manufacturer), the test will be stopped ("DUT not ok: No SleepMode possible").</p> <p>If DUT does not support SleepMode: As soon as the timeout (specified by manufacturer) expires and DUT does not generate MOST signal at Tx, it will be assumed that DUT has entered SleepMode, independent from power consumption. If DUT generates MOST signal at Tx although timeout expires, the test will be stopped ("DUT not ok: No SleepMode possible").</p> <p>b) DUT is power slave: refer to "DUT is power master"</p>
<p>MOST2V5:</p> <p>a) DUT is power master: - Set PermissionToWake = "False" - Switch off MOST signal</p> <p>b) DUT is power slave: - Set PermissionToWake = "False" - Switch off MOST signal</p>	

DUT: NetInterface PowerOff	
Effectuate state of DUT	Detect state of DUT
<ul style="list-style-type: none"> - Trigger AutoWakeUp if required and applicable - Switch off tester MOST signal 	DUT does not generate MOST signal

DUT: RBD finished	
Effectuate state of DUT	Detect state of DUT
<p>Only start of RBD can be triggered.</p> <ul style="list-style-type: none"> - Trigger RBD via FB ET (AutoWakeUp) <p>Note: Some devices does not finish RBD automatically. They need a trigger event to finish RBD. This trigger has to be generated by FB ET (by means of timer). The test equipment does not provide interface for that trigger.</p>	<p>DUT switches off MOST signal (after RBD).</p>

Procedures

Procedure	Procedure of tester
Perform Wake-up	<p>a) Tester is TM:</p> <ol style="list-style-type: none"> 1. Generate MOST signal 2. wait for MOST signal from DUT (timeout $t_{DeadLockShort}$) 3. wait for stable lock (timeout $t_{DeadLockMid}$) <p>b) Tester is not TM:</p> <ol style="list-style-type: none"> 1. Generate wake-up event (e.g. optical slave wakeup including $t_{Restart}$) 2. Wait for MOST signal at input 3. Generate MOST signal 4. Wait for stable lock 5. Wait for SBC > 5 <p>Note: If DUT already generates MOST signal, it will be sufficient that tester switches on MOST signal immediately.</p>
Perform ShutDown	<p>a) DUT is power master:</p> <ul style="list-style-type: none"> - Trigger ShutDown by means of FB ET - If MOST signal is off, DUT has performed ShutDown <p>If $t_{DeadLockMid}$ expires after triggering ShutDown and DUT continues generating MOST signal, the tester switches off the MOST signal by itself.</p> <p>If DUT does not support FB ET: Tester switches off the MOST signal by itself as ShutDown cannot be triggered via FB ET.</p> <p>b) DUT is power slave:</p> <ul style="list-style-type: none"> - Switch off MOST signal <p>NOTE: If DUT is PM, preconditions have to be switched off that could prevent DUT from performing ShutDown.</p>

Procedure	Procedure of tester
Generate Unlock	To generate an unlock event of predictable duration two requirements must be met: First the preamble at the beginning of at least every third MOST frame has to be made invalid or delayed during the period of unlock. Second a PLL unlock must be avoided.
Trigger RBD	During NormalOperation, the function "AutoWakeup" (0x201) of FB ET has to be called with parameter "Diagnosis".
MOST signal on	Tester switches MOST signal on
MOST signal off	Tester switches MOST signal off
NCE with unlock	The NCE has to be generated between TM and DUT.
NCE without unlock	The NCE has to be generated between DUT and TM
MOST2V5: Check CapabilityToWake of DUT	Use NetBlock.DeviceInfo.Get(CapabilityToWake) of DUT

3.2 Overview of the experimental set-ups

Most of the MOST Core Compliance Tests require the same experimental set-up. So every test contains a reference to the according experimental set-ups that are described below.

To avoid overtemperature situations of DUT during testing, the manufacturer recommendation regarding to cooling have to be met.

3.2.1 Experimental set-up #1

A Spy-Device is connected to the MOST ring to log the whole communication.
The DUT is directly connected to the tester. The power consumption of the DUT can be monitored.

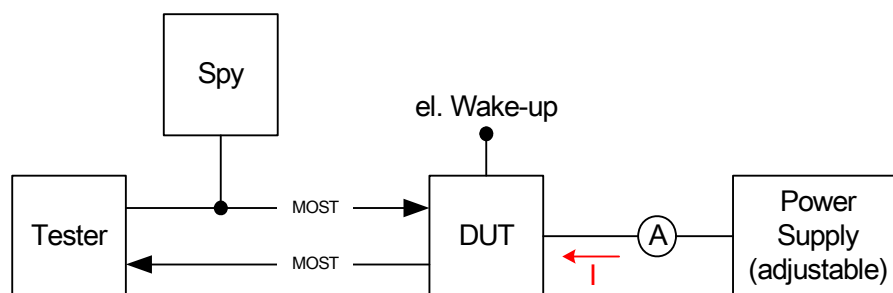


Figure 3-1 Experimental setup #1.

3.2.2 Experimental set-up #2

The DUT is connected to a switch. This switch has two positions. In position (a), the DUT is connected to the tester 1. In position (b), the ring consists of tester 1, the DUT and tester 2. By default, the switch is in position (a).

The power consumption can be monitored. A Spy-Device is connected to the MOST ring to log the whole communication.

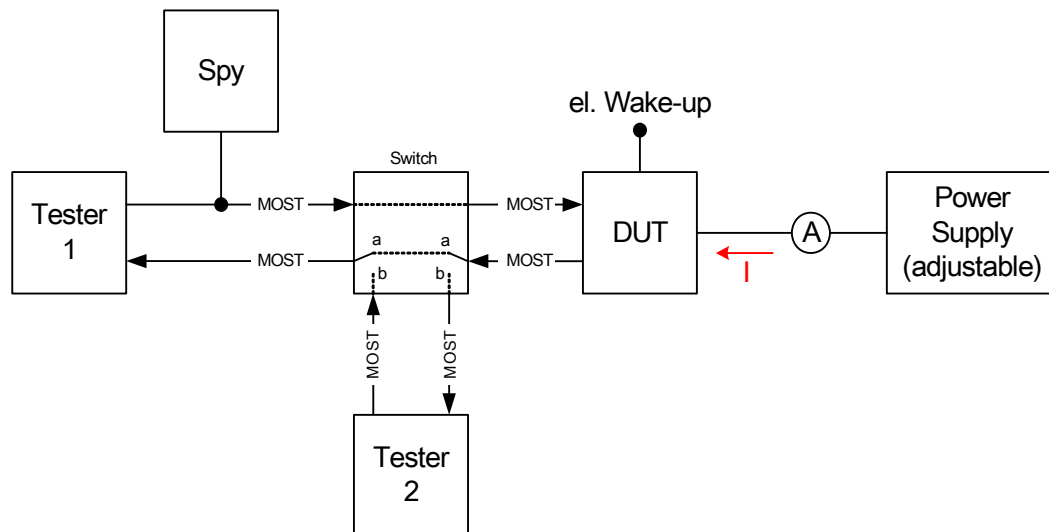


Figure 3-2 Experimental setup #2.

3.3 Static FBlock behaviour

3.3.1.1 Generic FBlock Property test (2.1.0-1)

Name of test	Generic FBlock Property test 2.1.0-1
Reference to MOST Specification	Rev. 2.4: 2.3.2.5 OPType Rev. 2.5: 2.3.2.5 OPType
Value of Interest	
Start Conditions	DUT: NormalOperation
Test description	
Experimental set-up	# 1 - Tester in slave mode or master mode (depends on DUT)
Device type	All devices MOST Specification Rev. 2.5: [x] MOST25 [x] MOST50 oPHY [x] MOST50 ePHY
Multi Node Device	All devices
Note	<p>Test has to be performed with the following Fblocks of the DUT:</p> <ul style="list-style-type: none"> - NWM - NetBlock - FBlocks containing sink functions (identified by test TEST_GSI_GSO_Identification) - FBlocks containing source functions (identified by test TEST_GSI_GSO_Identification) <p>FBlockID defined by DUT (e.g. NetBlock) InstID: All InstIDs as reported by DUT (FBlockID.Status) FktID: All FktIDs reported by DUT (FktID.Status) and also listed in relevant FBlock Library (NWM, GeneralFBlock) of MOSTCo as well. Additionally, All FCat Fkts. will be checked of NetBlock.</p> <p>Test covers all OP-Types (0 to F). Responses will be only evaluated from actually tested FblockID and FktID. At the receipt of the response it will be checked whether the InstID is correspondent. (In case of discrepancy: DUT not ok). Each other message of the DUT will be ignored during the test. The FBlock Library gives evidence whether it is a property respectively a method.</p> <p>Note: Today a device may not implement all functions and OPTypes of the FBlock Library. If implemented, the functions / OPTypes must behave according to the FBlock Library.</p> <p>Note: There will be no range checks.</p> <p>Note: Each OPType test presumes that the previous test has completed the communication process, e.g. if after StartResult the Result is not received, then the process has to be finished by sending Abort. In case Abort fails, the DUT can be reset to finish the test step and continue with the next step.</p> <p>Note: In case the OPType test allows "None" as "permitted answer", the observation time period will be 2x t_Property for properties respectively 2 x t_ProcessingDefault1_max for methods.</p> <p>Note: For NetBlock, Error 0x03 (Fkt. not available) is also permitted as answer Note: All commands from DUT (OPType 0x0 .. 0x8) will be ignored for all FBlocks. Note: For Netblock and NMW FBlock, OPTypes 0x09 ... 0xF not tested.</p>
Results	<p>DUT ok: The DUT has passed the test. DUT not ok: The DUT fails to pass the test.</p>

OPType	Send to DUT	Permitted answer from DUT
0 (Set)	Set (without parameter)	- Status - None (within 2x t_property) - All Errors except 0x01,0x02,0x03,0x0A,0x0C
1 (Get)	Get(without parameter)	- Status - All Errors except 0x01,0x02,0x03,0x0A,0x0C
2 (SetGet)	SetGet(without parameter)	- Status - All Errors except 0x01,0x02,0x03,0x0A,0x0C
3 (Increment)	Increment(1)	- Status - All Errors except 0x01,0x02,0x03,0x0A,0x0C
4 (Decrement)	Decrement(1)	- Status - All Errors except 0x01,0x02,0x03,0x0A,0x0C
5 (GetInterface)	GetInterface	- Interface - All Errors except 0x01,0x02,0x03,0x0A,0x0C
6 (M: StartResultAck)	StartResultAck (SenderHandle=0x12,0x34)	- ErrorAck(SenderHandle=0x12,0x34, ErrorCode 0x04) - Error(ErrorCode 0x04)
7 (M: AbortAck)	AbortAck (SenderHandle=0x12,0x34)	- ErrorAck(SenderHandle=0x12,0x34, ErrorCode 0x04) - Error(ErrorCode 0x04)
8 (M: StartAck)	StartAck (SenderHandle=0x12,0x34)	- ErrorAck(SenderHandle=0x12,0x34, ErrorCode 0x04) - Error(ErrorCode 0x04)
9 (M: ErrorAck)	ErrorAck	None
A (M: ProcessingAck)	ProcessingAck	None
B (M: Processing)	Processing	None
C (Status)	Status	None
D (M: ResultAck)	ResultAck	None
E (Interface)	Interface	None
F (Error)	Error	None

3.3.1.2 Generic FBlock Method test (2.1.0-2)

Name of test	Generic FBlock Method test 2.1.0-2
Reference to MOST Specification	Rev. 2.4: 2.3.2.5 OPTYPE Rev. 2.5: 2.3.2.5 OPTYPE
Value of Interest	
Start Conditions	DUT: NormalOperation
Test description	
Experimental set-up	# 1 - Tester in slave mode or master mode (depends on DUT)
Device type	All devices [x] MOST25 [x] MOST50 oPHY [x] MOST50 ePHY
Multi Node Device	All devices
Note	<p>Test has to be performed with the following Fblocks of the DUT:</p> <ul style="list-style-type: none"> - NWM - NetBlock - Fblocks containing sink functions (identified by test TEST_GSI_GSO_Identification) - Fblocks containing source functions (identified by test TEST_GSI_GSO_Identification) <p>FBlockID defined by DUT (e.g. NetBlock) InstID: All InstIDs as reported by DUT (FBlockID.Status) FktID: All FktIDs reported by DUT (FktID.Status) and also listed in relevant FBlock Library (NWM, GeneralFBlock) of MOSTCo as well. Additionally, All FCat Fkts. will be checked of NetBlock.</p> <p>Test covers all OP-Types (0 to F). Responses will be only evaluated from actually tested FblockID and FktID. At the receipt of the response it will be checked whether the InstID is correspondent. (In case of discrepancy: DUT not ok). Each other message of the DUT will be ignored during the test. The FBlock Library gives evidence whether it is a property respectively a method.</p> <p>Note: Today a device may not implement all functions and OPTypes of the FBlock Library. If implemented, the functions / OPTypes must behave according to the FBlock Library.</p> <p>Note: There will be no range checks.</p> <p>Note: Each OPTYPE test presumes that the previous test has completed the communication process, e.g. if after StartResult the Result is not received, then the process has to be finished by sending Abort. In case Abort fails, the DUT can be reset to finish the test step and continue with the next step.</p> <p>Note: In case the OPTYPE test allows "None" as "permitted answer", the observation time period will be 2x t_Property for properties respectively 2 x t_ProcessingDefault1_max for methods.</p> <p>Note: In case the OPTYPE test allows "Processing" as "permitted answer": As response „Processing" might be received before Result or an Error, max. 50 Processing will be accepted by the tester, then "Abort" will be sent.</p> <p>Note: For NetBlock, Error 0x03 (Fkt. not available) is also permitted as answer.</p> <p>Note: All commands from DUT (OPTYPE 0x0 .. 0x8) will be ignored for all FBlocks. Note: For Netblock and NMW FBlock, OPTypes 0x09 ... 0xF not tested.</p>
Results	<p>DUT ok: The DUT has passed the test.</p> <p>DUT not ok: The DUT fails to pass the test.</p>

OPType	Send to DUT	Permitted answer from DUT
0 (Start)	Start	<ul style="list-style-type: none"> - None - Result - Processing - All Errors except 0x01,0x02,0x03,0x0A,0x0C
1 (Abort)	Abort	<ul style="list-style-type: none"> - None - All Errors except 0x01,0x02,0x03,0x0A,0x0C
2 (StartResult)	StartResult	<ul style="list-style-type: none"> - Processing - Result - All Errors except 0x01,0x02,0x03,0x0A,0x0C
3 (P: Increment)	Increment	<ul style="list-style-type: none"> - Error 0x04
4 (P: Decrement)	Decrement	<ul style="list-style-type: none"> - Error 0x04
5 (GetInterface)	GetInterface	<ul style="list-style-type: none"> - Interface - All Errors except 0x01,0x02,0x03,0x0A,0x0C
6 (StartResultAck)	StartResultAck (SenderHandle=0x12,0x34)	<ul style="list-style-type: none"> - ProcessingAck(SenderHandle=0x12,0x34) - ResultAck(SenderHandle=0x12,0x34) - all ErrorAcks (SenderHandle=0x12,0x34) except 0x01,0x02,0x03,0x0A,0x0C - all Errors except 0x01,0x02,0x03,0x0A,0x0C
7 (AbortAck)	AbortAck (SenderHandle=0x12,0x34)	<ul style="list-style-type: none"> - None - all ErrorAcks (SenderHandle=0x12,0x34) except 0x01,0x02,0x03,0x0A,0x0C - all Errors except 0x01,0x02,0x03,0x0A,0x0C
8 (StartAck)	StartAck (SenderHandle=0x12,0x34)	<ul style="list-style-type: none"> - None - ResultAck(SenderHandle=0x12,0x34) - ProcessingAck(SenderHandle=0x12,0x34) - alle ErrorAcks (SenderHandle=0x12,0x34) except 0x01,0x02,0x03,0x0A,0x0C - alle Errors except 0x01,0x02,0x03,0x0A,0x0C
9 (ErrorAck)	ErrorAck	None
A (ProcessingAck)	ProcessingAck	None
B (Processing)	Processing	None
C (Result)	Result	None
D (ResultAck)	ResultAck	None
E (Interface)	Interface	None
F (Error)	Error	None

3.4 Wake-up

3.4.1 Wake-up – General

3.4.1.1 Signal On Test (2.1.1-1)

Name of test	Signal On Test 2.1.1-1
Reference to MOST Specification	Rev. 2.4: 3.2.4.1 Waking of the Network Rev. 2.5: 3.2.4.1 Waking of the Network
Value of Interest	t_WakeUp
Start Conditions	DUT in SleepMode (detectable by monitoring of power consumption)
Test description	A wake-up event is applied to the DUT. The DUT has to generate the MOST signal within t_WakeUp_max
Experimental set-up	#1 - Tester in master mode or slave mode; depends on DUT
Device type	All devices that are wakeable via MOST signal [x] MOST25 [x] MOST50 oPHY [] MOST50 ePHY
Multi Node Device	All devices that are wakeable via MOST signal
Note	Test only applicable if DUT is wakeable via MOST signal (provided by manufacturer). Autowakeup (FB ET) not considered by this test.
Results	DUT ok: The DUT has passed the test DUT not ok: The timer t_WakeUp does not meet the specification

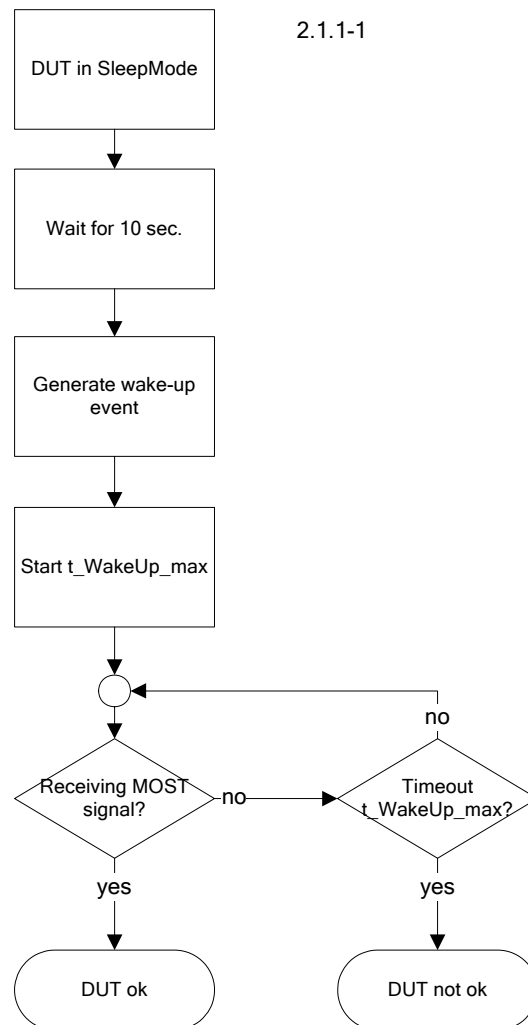


Figure 3-3 Flow of Signal On Test.

3.4.1.2 Bypass test (2.1.1-5)

Name of test	Bypass test 2.1.1-5
Reference to MOST Specification	Rev. 2.4: 3.2.2.2 NetInterfaceInit Rev. 2.5: 3.2.2.2 NetInterfaceInit
Value of Interest	t_Lock + t_WaitNodes Bypass
Start Conditions	DUT in SleepMode (detectable by monitoring of power consumption).
Test description	MOST signal is applied to the input of the DUT. The time between MOST signal on and MPR = MPR_Nominal must not exceed t_Lock_max + t_WaitNodes_max.
Experimental set-up	# 1 - Tester in master mode or slave mode; depends on DUT
Device type	All devices that are wakeable via MOST signal [x] MOST25 [x] MOST50 oPHY [] MOST50 ePHY
Multi Node Device	All devices that are wakeable via MOST signal
Note	The test can be applied for master and for slave devices. Test only applicable for devices, wakeable by MOST signal.
Results	DUT ok: The DUT has passed the test. DUT not ok: The DUT fails to open the bypass right on time.

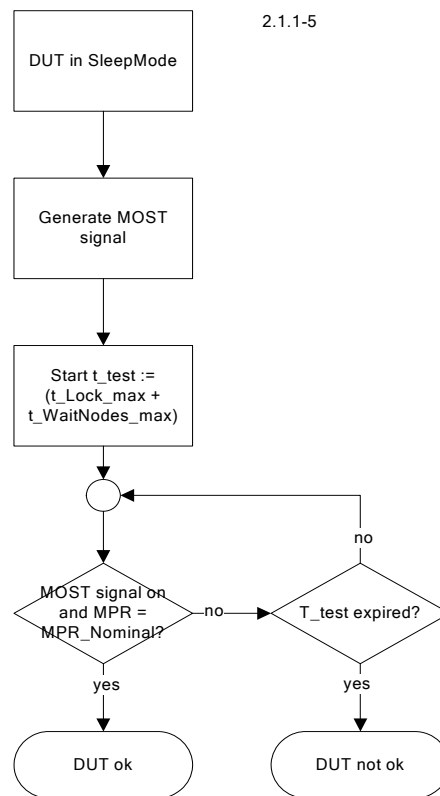


Figure 3-4 Flow of Bypass test.

3.4.1.3 Signal off test (2.1.2-6)

Name of test	Signal off test 2.1.2-6
Reference to MOST Specification	Rev. 2.4: 3.9 Timing Definitions Rev. 2.5: 3.8 Timing Definitions
Value of Interest	t_ShutDown
Start Conditions	DUT in NormalOperation
Test description	The tester switches off the MOST signal. The DUT has to stop generating the MOST signal within t_ShutDown_max.
Experimental set-up	# 1 - Tester in master mode or slave mode (depends on DUT).
Device type	All devices [x] MOST25 [x] MOST50 oPHY [] MOST50 ePHY
Multi Node Device	All devices
Note	
Results	DUT ok: The DUT has passed the test. DUT not ok: The DUT failed to switch off the MOST signal right on time.

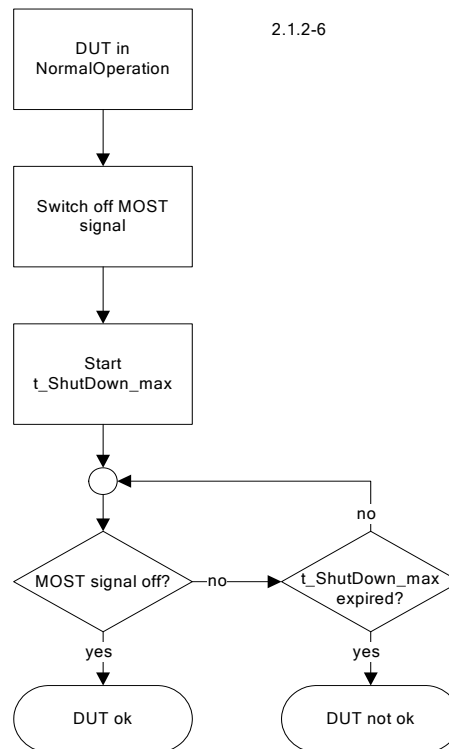


Figure 3-5 Flow of Signal off test.

3.4.2 Wake-up – Timing Master

3.4.2.1 SBC register test (2.1.1-2)

Name of test	SBC register test 2.1.1-2
Reference to MOST Specification	Rev. 2.4: 3.2.2.2 NetInterfaceInit Rev. 2.5: 3.2.2.2 NetInterfaceInit
Value of Interest	SBC register t_Config
Start Conditions	DUT: NetInterfacePowerOff
Test description	A wake-up event is triggered. The output of the tester is disabled. If the tester detects a stable lock, the SBC register has to show the value 0x04. Then the ring will be closed (by activating of the tester's output). The SBC register has to show a value > 0x05.
Experimental set-up	#1 - Tester in SlaveMode
Device type	TM [x] MOST25 [] MOST50 oPHY [] MOST50 ePHY
Multi Node Device	All devices containing TM
Note	The whole test has to be finished within t_Config. Otherwise the DUT will perform a ShutDown.
Results	DUT ok: The DUT has passed the test DUT not ok (1): Timeout t_Config without lock or SBC not set to 0x04 after lock DUT not ok (2): Timeout t_Config without SBC > 0x05

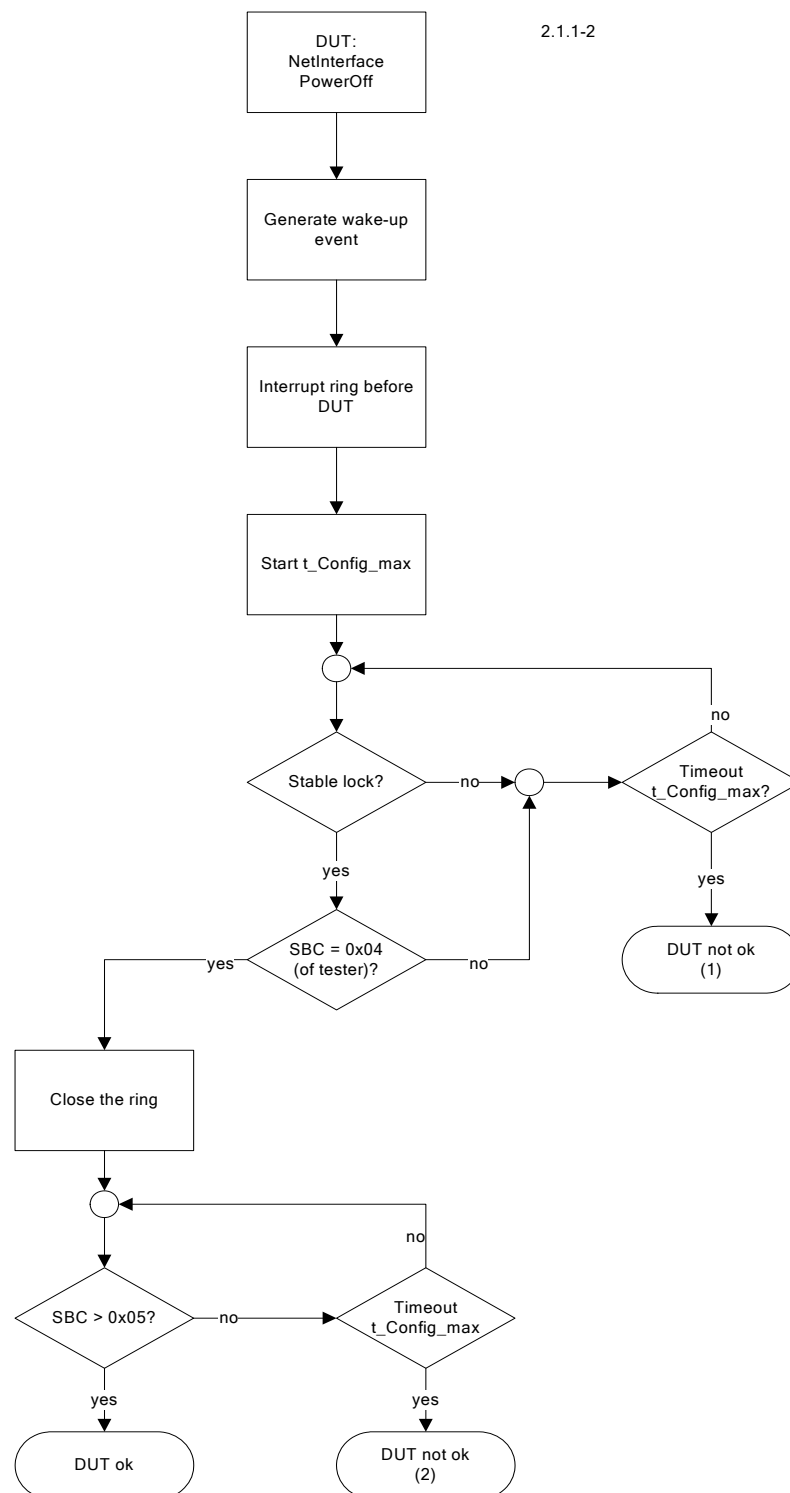


Figure 3-6 Flow of SBC register test.

3.4.2.2 Wake-up / Shutdown test (2.1.1-4) MOST2V4

Name of test	Wake-up / Shutdown test (MOST2V4) 2.1.1-4
Reference to MOST Specification	Rev. 2.4: 3.2.2.2 NetInterfaceInit
Value of Interest	t_Config
Start Conditions	After triggering wake-up, the ring is interrupted right before the DUT (output of tester disabled).
Test description	The AbilityToWake of the DUT will be set to TRUE and the AutoWakeup will be triggered by the tester via FB ET. If the tester detects a stable lock (MOST-signal), the timer t_Config will be started. The DUT has to switch off the MOST signal within t_Config.
Experimental set-up	#1 - Tester in SlaveMode
Device type	TM
Multi Node Device	All devices containing TM
Note	
Results	DUT ok (1): The DUT is not able to perform active wake-ups. DUT ok (2): The DUT has passed the test. DUT not ok (1): No signal from DUT received after wake-up. DUT not ok (2): DUT switches off the signal too early. DUT not ok (3): DUT switches off the signal too late.

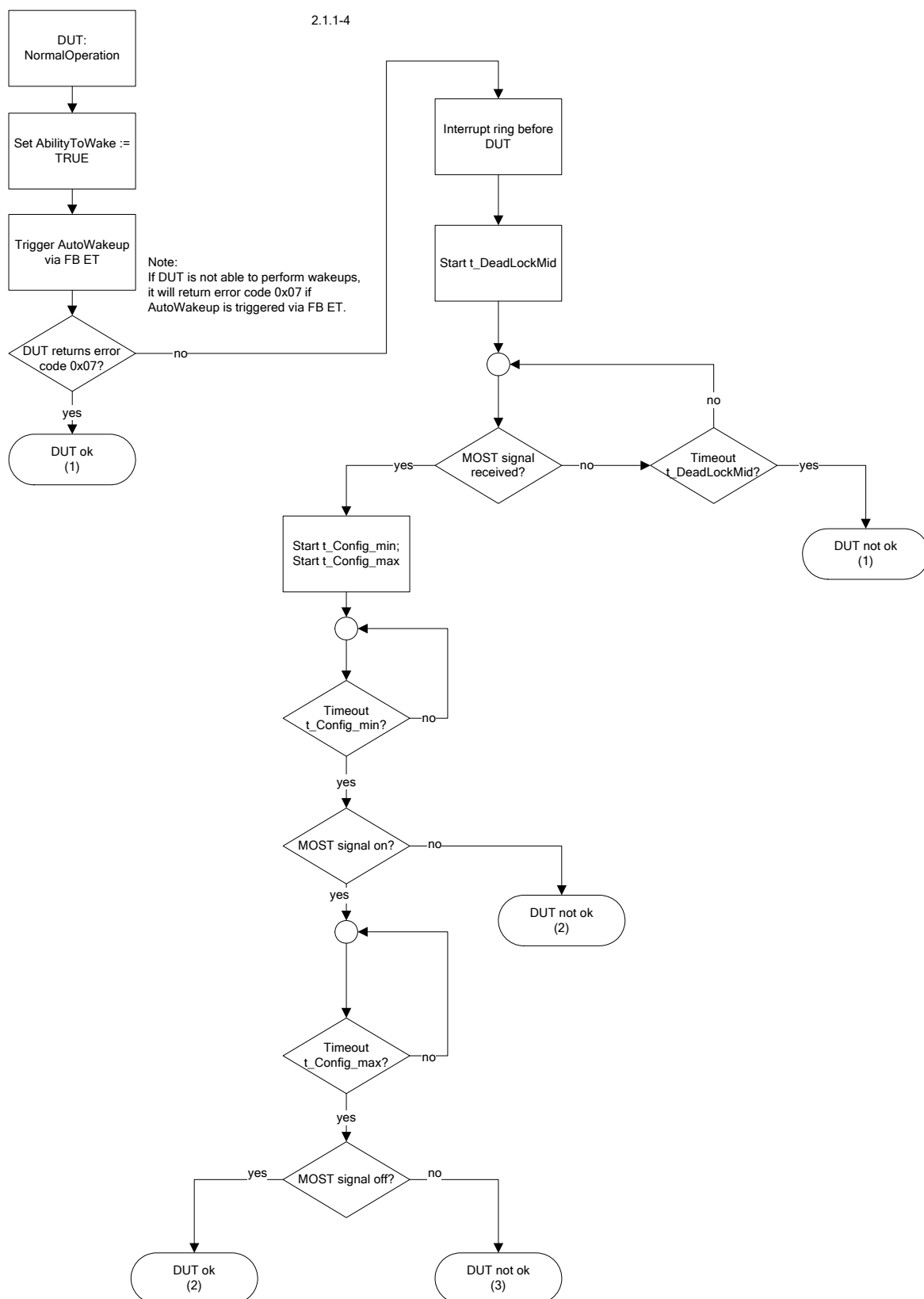


Figure 3-7 Flow of Wake-up / Shutdown test MOST2V4.

3.4.2.3 Wake-up / Shutdown test (2.1.1-4) MOST2V5

Name of test	Wake-up / Shutdown test (MOST2V5) 2.1.1-4
Reference to MOST Specification	Rev. 2.5: 3.2.2.2 NetInterfaceInit
Value of Interest	t_Config
Start Conditions	After triggering wake-up, the ring is interrupted right before the DUT (output of tester disabled).
Test description	The PermissionToWake of the DUT will be set to TRUE and the AutoWakeup will be triggered by the tester via FB ET. If the tester detects a stable lock (MOST-signal), the timer t_Config will be started. The DUT has to switch off the MOST signal within t_Config.
Experimental set-up	#1 - Tester in SlaveMode
Device type	TM [x] MOST25 [x] MOST50 oPHY [] MOST50 ePHY
Multi Node Device	All devices containing TM
Note	
Results	DUT ok (1): The DUT has passed the test (CapabilityToWake = FALSE). DUT ok (2): The DUT has passed the test. DUT not ok (1): No signal from DUT received after wake-up. DUT not ok (2): DUT switches off the signal too early. DUT not ok (3): DUT switches off the signal too late.

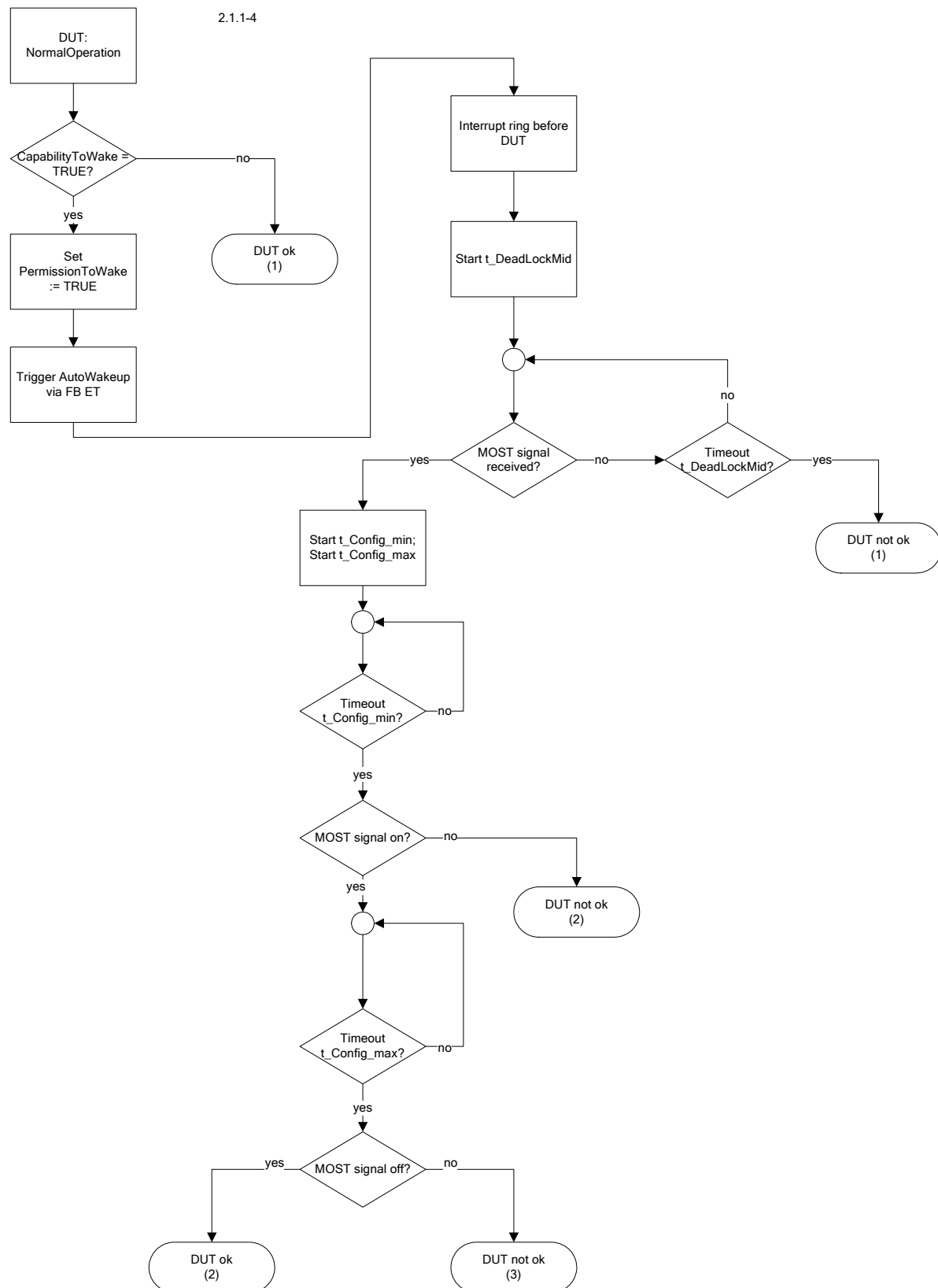


Figure 3-8 Flow of Wake-up / Shutdown test MOST2V5.

3.4.3 Wake-up – Timing Slave

3.4.3.1 SBC register check test (2.1.2-4)

Name of test	SBC register check test 2.1.2-4
Reference to MOST Specification	Rev. 2.4: 3.2.2.2 NetInterfaceInit Rev. 2.5: 3.2.2.2 NetInterfaceInit
Value of Interest	Change of NetInterface state
Start Conditions	DUT woken up but not in NO
Test description	The tester is configured as timing master. It generates the MOST signal and the value of the SBC register is equal to 0x04. Then the SBC register will be changed to a value > 0x05. The DUT has to change to NormalOperation.
Experimental set-up	# 1 - Tester in master mode
Device type	All devices except TM [x] MOST25 [] MOST50 oPHY [] MOST50 ePHY
Multi Node Device	All devices without TM
Note	T_Config will not be tested by this test as the timer depends on the initialization time of the application. The device has to enter NO, before t_Config expires.
Result	DUT ok: The DUT has passed the test. DUT not ok (1): DUT entered NO without checking the SBC register. DUT not ok (2): DUT fails to enter NO.

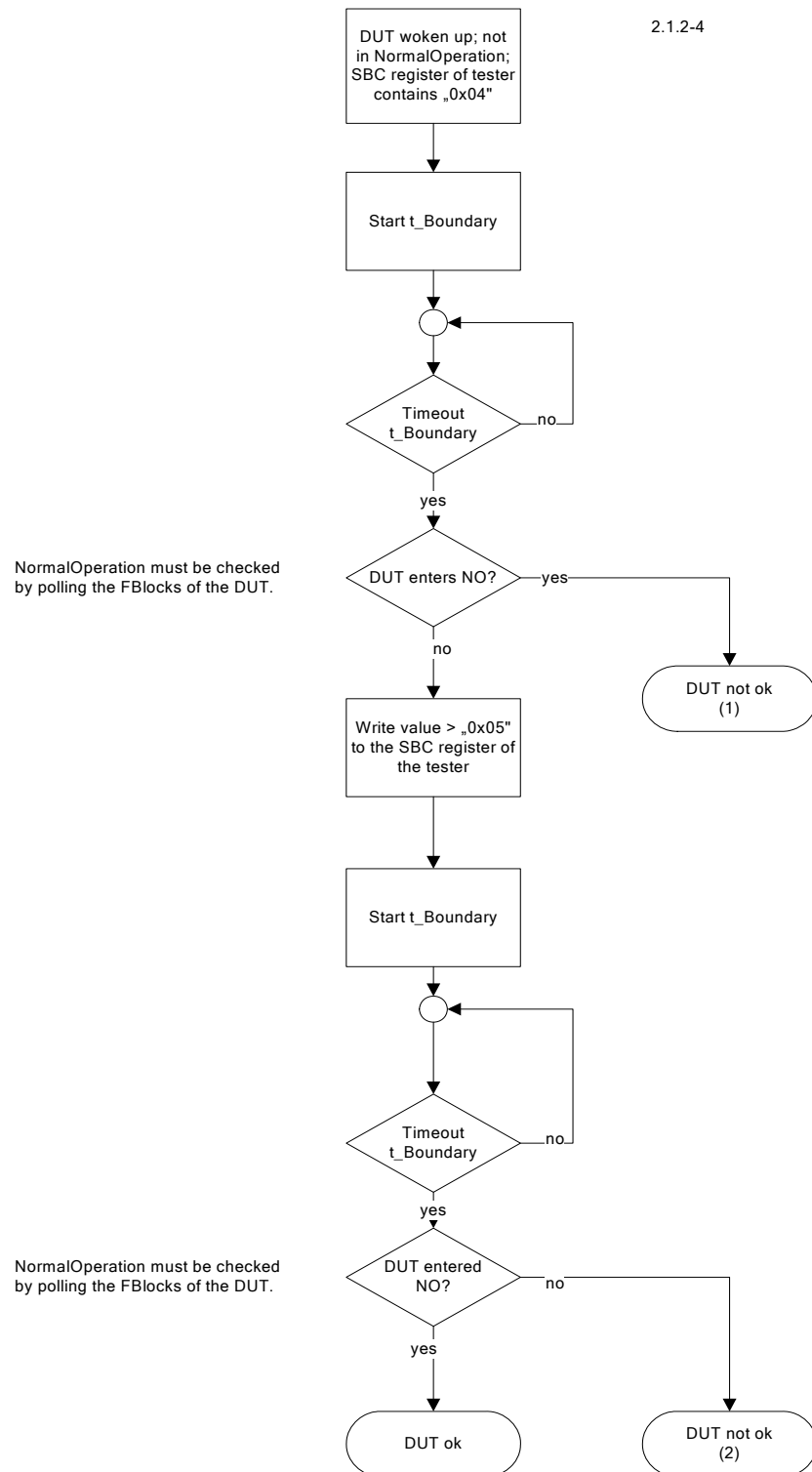


Figure 3-9 Flow of SBC register check test.

3.4.3.2 Slave lock detection test (2.1.2-5)

Name of test	Slave lock detection test 2.1.2-5
Reference to MOST Specification	Rev. 2.4: 3.2.2.2 NetInterfaceInit Rev. 2.5: 3.2.2.2 NetInterfaceInit
Value of Interest	SBC register t_Config + t_WaitNodes
Start Conditions	DUT: NetInterfacePowerOff
Test description	The tester is configured as timing master. It generates the MOST signal and the value of the SBC register is equal to 0x04. The DUT has to switch off the MOST signal as soon as (t_Config_max + t_WaitNodes_max) expires after switching on the MOST signal.
Experimental set-up	# 1 - Tester in master mode
Device type	All devices except TM [x] MOST25 [] MOST50 oPHY [] MOST50 ePHY
Multi Node Device	All devices without TM
Note	
Results	DUT ok: The DUT has passed the test. DUT not ok: The DUT fails to switch off the MOST signal right on time.

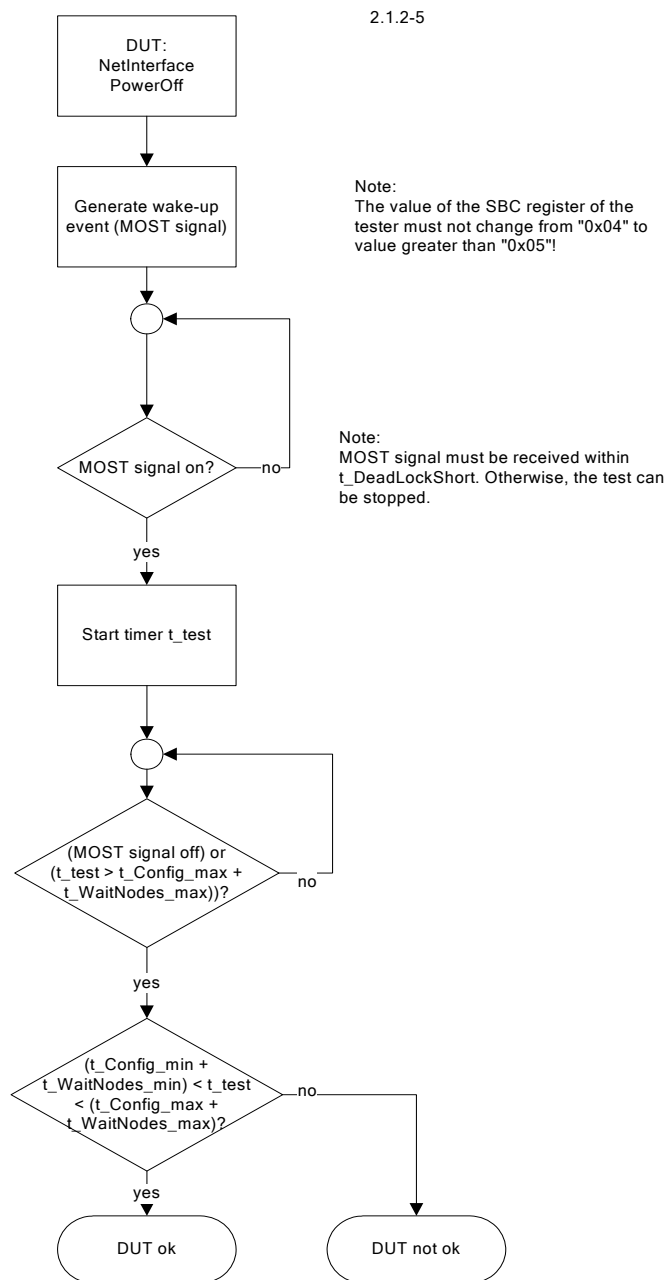


Figure 3-10 Flow of Slave lock detection test.

3.4.3.3 Slave wake-up (2.1.3-1) MOST2V4

Name of test	Slave wake-up (MOST2V4) 2.1.3-1
Reference to MOST Specification	Rev. 2.4: 3.2.2.2 NetInterfaceInit / 3.2.5.2.1 Waking
Value of Interest	wake-up event t_ShutDown t_Restart
Start Conditions	DUT in NormalOperation
Test description	The AbilityToWake of the DUT will be set to TRUE and the AutoWakeup will be triggered by the tester via FB ET. The DUT has to generate the MOST signal right on time. If the tester receives the MOST signal, it has to generate the MOST signal by itself. The DUT has to stop generating the MOST signal within t_ShutDown_max. Then it has to wait for t_Restart_min before switching on the MOST signal again.
Experimental set-up	# 1 - Tester in master mode.
Device type	All devices except TM
Multi Node Device	All devices without TM
Note	
Results	<p>DUT ok (1): The DUT is not able to perform active wake-ups.</p> <p>DUT ok (2): The DUT has passed the test.</p> <p>DUT not ok (1): The DUT fails to generate any MOST signal after wake-up. (Or: external wake-up not supported)</p> <p>DUT not ok (2): The DUT fails to stop generating the MOST signal within t_ShutDown.</p> <p>DUT not ok (3): The DUT fails to wait t_Restart before generating the MOST signal again.</p> <p>DUT not ok (4): The DUT fails to generate the MOST signal if t_Restart expires.</p>

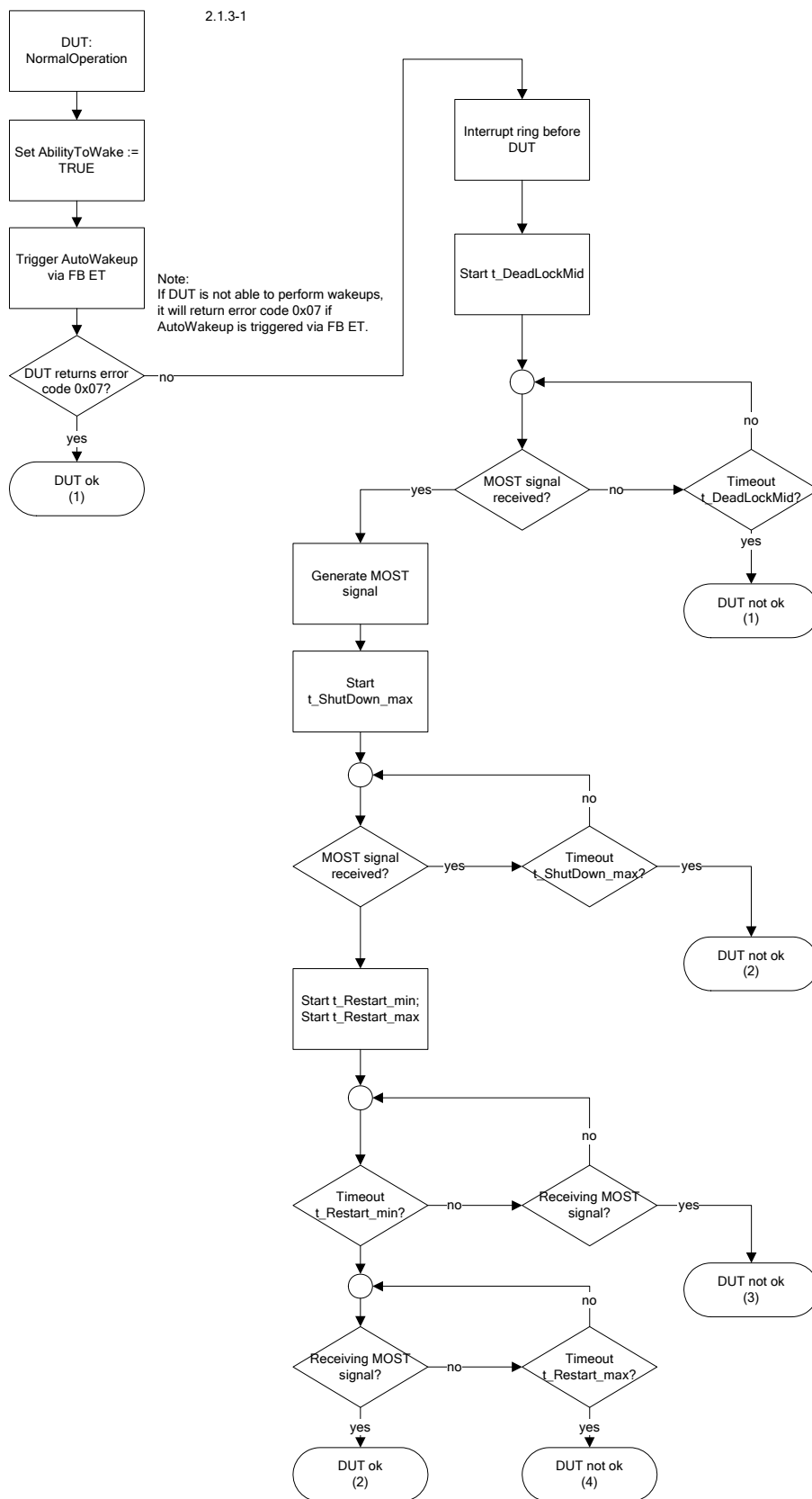


Figure 3-11 Flow of Slave wake-up MOST2V4.

3.4.3.4 Slave wake-up (2.1.3-1) MOST2V5

Name of test	Slave wake-up (MOST2V5) 2.1.3-1
Reference to MOST Specification	Rev. 2.5: 3.2.2.2 NetInterfaceInit / 3.2.5.2.1 Waking
Value of Interest	wake-up event t_ShutDown t_Restart
Start Conditions	DUT in NormalOperation
Test description	The PermissionToWake of the DUT will be set to TRUE and the AutoWakeup will be triggered by the tester via FB ET. The DUT has to generate the MOST signal right on time. If the tester receives the MOST signal, it has to generate the MOST signal by itself. The DUT has to stop generating the MOST signal within t_ShutDown_max. Then it has to wait for t_Restart_min before switching on the MOST signal again.
Experimental set-up	# 1 - Tester in master mode.
Device type	All devices except TM [x] MOST25 [x] MOST50 oPHY [] MOST50 ePHY
Multi Node Device	All devices without TM
Note	
Results	DUT ok (1): The DUT has passed the test (CapabilityToWake = FALSE). DUT ok (2): The DUT has passed the test. DUT not ok (1): The DUT fails to generate any MOST signal after wake-up. (Or: external wake-up not supported) DUT not ok (2): The DUT fails to stop generating the MOST signal within t_ShutDown. DUT not ok (3): The DUT fails to wait t_Restart before generating the MOST signal again. DUT not ok (4): The DUT fails to generate the MOST signal if t_Restart expires.

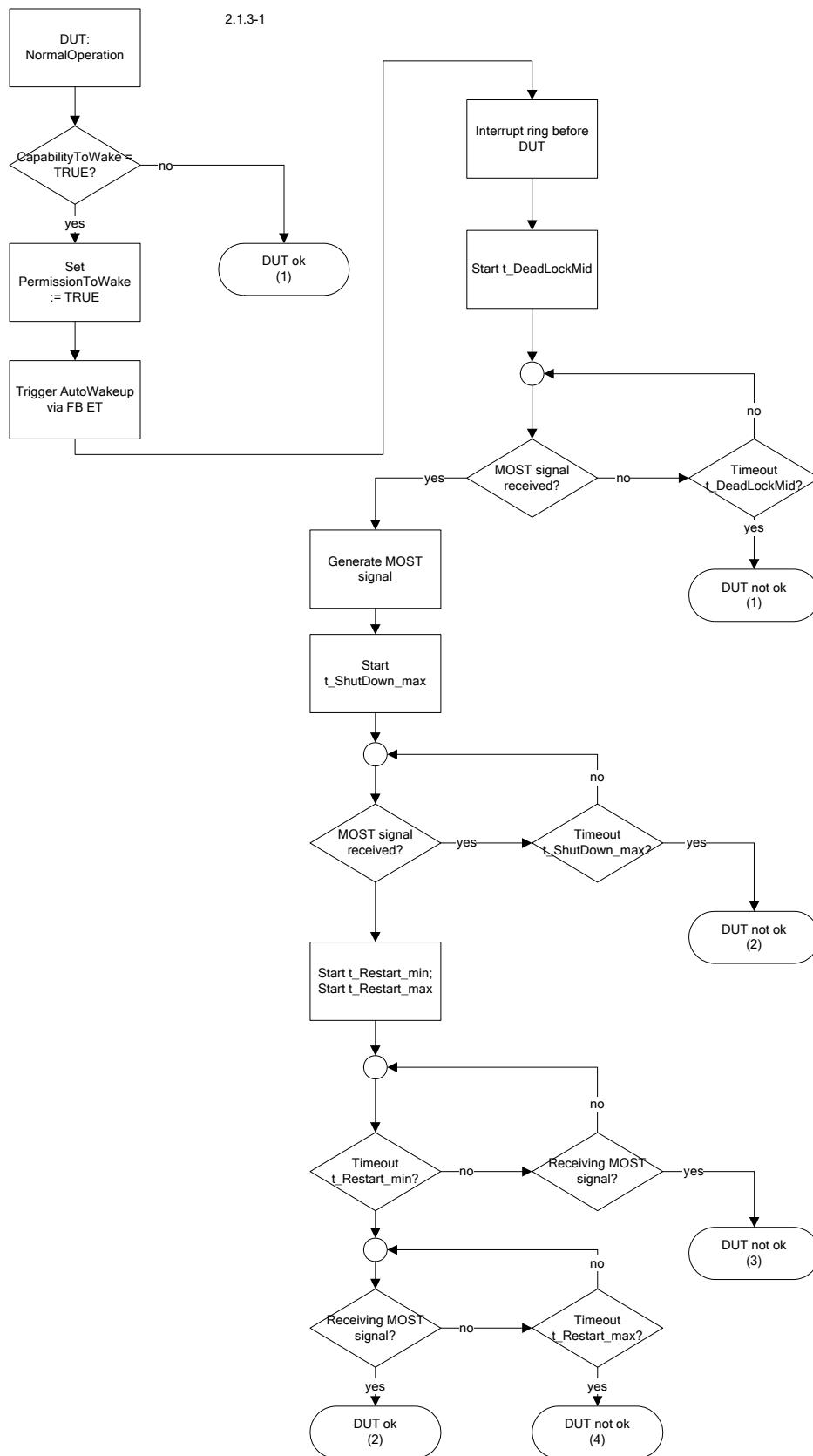


Figure 3-12 Flow of Slave wake-up MOST2V5.

3.4.3.5 Waking slave timeout (2.1.3-4) MOST2V4

Name of test	Waking slave timeout (MOST2V4) 2.1.3-4
Reference to MOST Specification	Rev. 2.4: 3.2.2.2 NetInterfaceInit
Value of Interest	t_Config t_Restart
Start Conditions	DUT in NormalOperation; Ring will be interrupted as soon as the AutoWakeup has been triggered,
Test description	The AbilityToWake of the DUT will be set to TRUE and the AutoWakeup will be triggered by the tester via FB ET. Then the ring will be interrupted before the DUT. The DUT has to generate the MOST signal right in time. If t_Config_max expires, the DUT has to switch off the MOST signal. DUT has to perform retries within t_Restart_max
Experimental set-up	#1 - Tester in master mode
Device type	All devices except TM
Multi Node Device	All devices without TM
Note	This test forces the DUT to perform one wake-up retry.
Results	<p>DUT ok (1): The DUT is not able to perform active wake-ups.</p> <p>DUT ok (2): The DUT has passed the test.</p> <p>DUT not ok (1): The DUT fails to generate any MOST signal after external wake-up. (Or: external wake-up not supported)</p> <p>DUT not ok (2): The DUT fails to generate any MOST signal after external wake-up (during retry).</p> <p>DUT not ok (3): The DUT fails to switch off the MOST signal right in time.</p> <p>DUT not ok (4): The DUT fails to switch off the MOST signal right in time (during retry).</p>

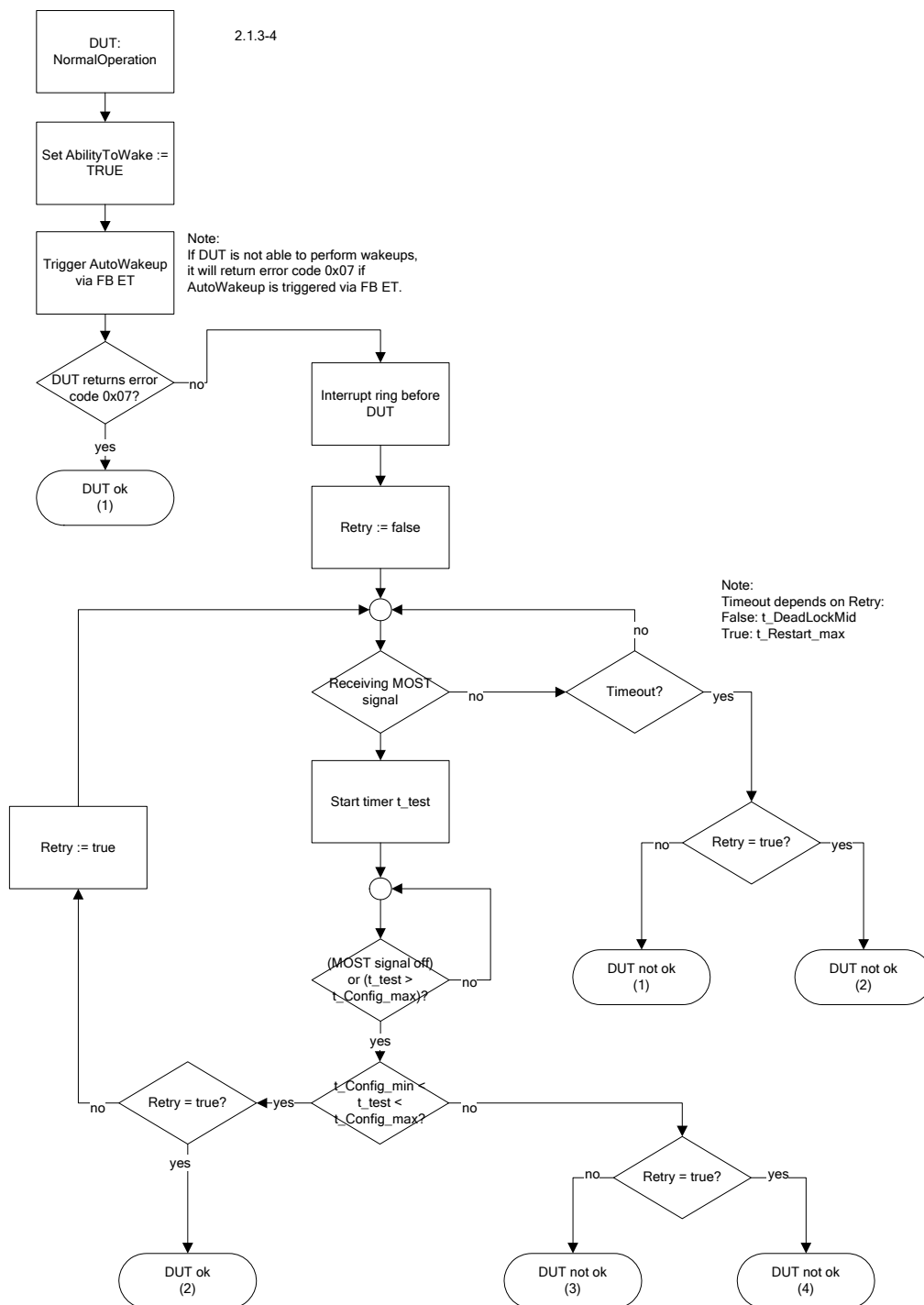


Figure 3-13 Flow of Waking slave timeout MOST2V4.

3.4.3.6 Waking slave timeout (2.1.3-4) MOST2V5

Name of test	Waking slave timeout (MOST2V5) 2.1.3-4
Reference to MOST Specification	Rev. 2.5: 3.2.2.2 NetInterfaceInit
Value of Interest	t_Config t_Restart
Start Conditions	DUT in NormalOperation; Ring will be interrupted as soon as the AutoWakeup has been triggered,
Test description	The PermissionToWake of the DUT will be set to TRUE and the AutoWakeup will be triggered by the tester via FB ET. Then the ring will be interrupted before the DUT. The DUT has to generate the MOST signal right in. If t_Config_max expires, the DUT has to switch off the MOST signal. DUT has to perform retries within t_Restart_max
Experimental set-up	#1 - Tester in master mode
Device type	All devices except TM [x] MOST25 [x] MOST50 oPHY [] MOST50 ePHY
Multi Node Device	All devices without TM
Note	This test forces the DUT to perform one wake-up retry.
Results	DUT ok (1): The DUT has passed the test (CapabilityToWake = FALSE). DUT ok (2): The DUT has passed the test. DUT not ok (1): The DUT fails to generate any MOST signal after external wake-up. (Or: external wake-up not supported) DUT not ok (2): The DUT fails to generate any MOST signal after external wake-up (during retry). DUT not ok (3): The DUT fails to switch off the MOST signal right in time. DUT not ok (4): The DUT fails to switch off the MOST signal right in time (during retry).

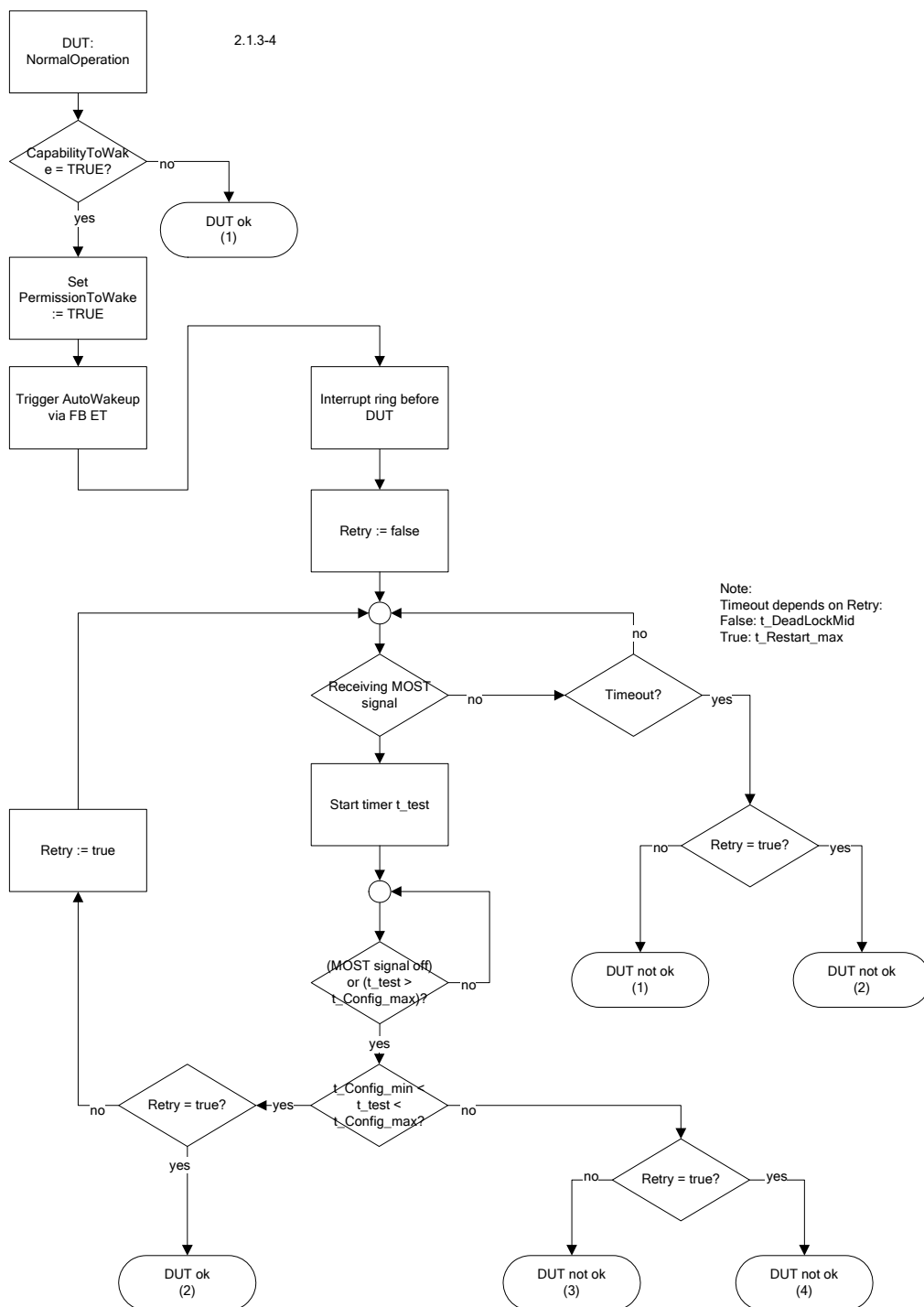


Figure 3-14 Flow of Waking slave timeout MOST2V5.

3.5 Normal Operation

3.5.1 Normal Operation – Unlock

3.5.1.1 Short unlock test (2.2.1-3)

Name of test	Short unlock test 2.2.1-3
Reference to MOST Specification	Rev. 2.4: 3.2.2.3 NetInterfaceNormalOperation Rev. 2.5: 3.2.2.3 NetInterfaceNormalOperation
Value of Interest	t_Unlock t_Lock
Start Conditions	DUT in NormalOperation
Test description	The tester generates 10 short unlocks (each $0.2 \times t_Unlock_typical$) with a pause of t_Lock_max between each unlock. The DUT must not stop generating the MOST signal. At the end of the test, the DUT has to continue with NormalOperation.
Experimental set-up	Depends on DUT: DUT is TM: #1 - Tester in slave mode DUT is not TM: #2 - Tester 2 in master mode - Tester 1 in slave mode
Device type	All devices [x] MOST25 [x] MOST50 oPHY [x] MOST50 ePHY
Multi Node Device	All devices
Note	The reaction of the application (mute) is not tested by this test. Procedure for unlock generation described in para 3.1 (General Notes)
Results	DUT ok: The DUT has passed the test. DUT not ok (1): The DUT has switched off the MOST signal. DUT not ok (2): The DUT does not perform NO at the end of the test.

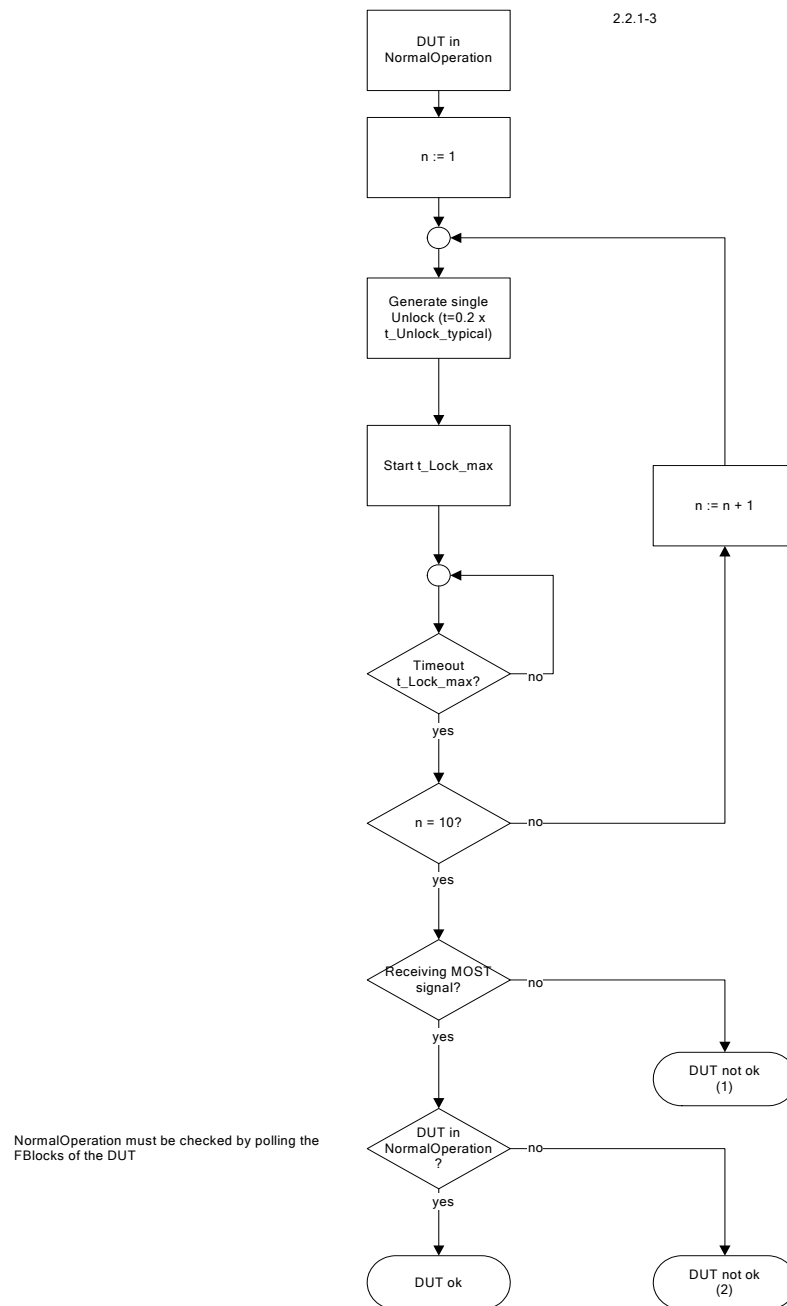


Figure 3-15 Flow of Short unlock test.

3.5.1.2 Accumulated short unlock test (2.2.1-4)

Name of test	Accumulated short unlock test 2.2.1-4
Reference to MOST Specification	Rev. 2.4: 3.2.2.3 NetInterfaceNormalOperation Rev. 2.5: 3.2.2.3 NetInterfaceNormalOperation
Value of Interest	t_Unlock t_Lock
Start Conditions	DUT in NormalOperation
Test description	The tester generates an unlock sequence (consists of a series of unlocks ($0.5 \times t_Unlock_min$) and locks ($0.8 \times t_Lock_min$). The DUT must not switch off MOST signal too early ($< 110ms$) or too late ($> 300ms$).
Experimental set-up	Depends on DUT: DUT is TM: #1 - Tester in slave mode DUT is not TM: #2 - Tester 2 in master mode - Tester 1 in slave mode
Device type	All devices [x] MOST25 [x] MOST50 oPHY [x] MOST50 ePHY
Multi Node Device	All devices
Note	Procedure for unlock generation described in para 3.1 (General Notes)
Result	DUT ok: The DUT has passed the test. DUT not ok (1): The DUT fails to switch off MOST signal in case of accumulated short unlocks. DUT not ok (2): The DUT switches off MOST signal too early in case of accumulated short unlocks.

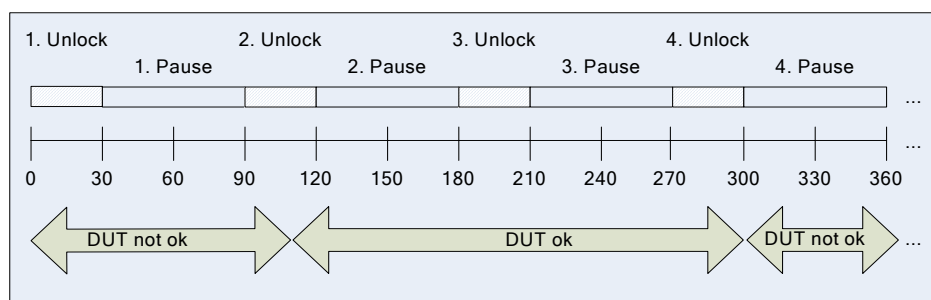
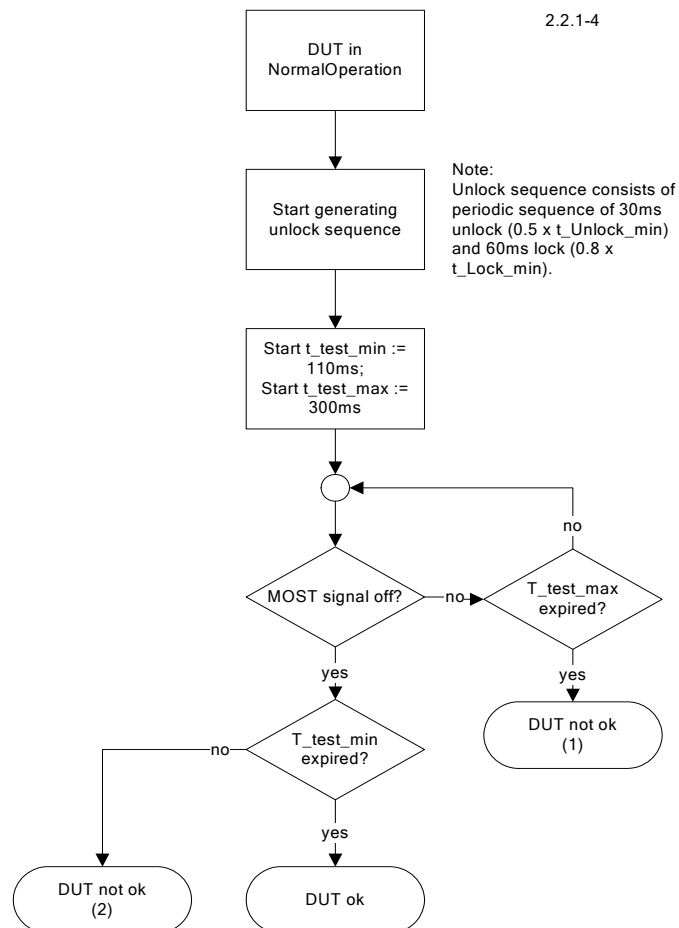


Figure 3-16 Flow of Accumulated short unlock test.

3.5.1.3 Critical unlock test (2.2.1-5)

Name of test	Critical unlock test 2.2.1-5
Reference to MOST Specification	Rev. 2.4: 3.2.2.3 NetInterfaceNormalOperation Rev. 2.5: 3.2.2.3 NetInterfaceNormalOperation
Value of Interest	t_Unlock t_Restart
Start Conditions	DUT in NormalOperation
Test description	The tester starts to generate an unlock. As soon as t_Unlock_max expires, the DUT has to switch off the MOST signal. The DUT must not switch off the MOST signal before t_Unlock_min expires. If the MOST signal is switched on again (lock possible), the DUT has to wait for t_Restart_min before generating the MOST signal again.
Experimental set-up	Depends on DUT: DUT is TM: #1 - Tester in slave mode DUT is not TM: #2 - Tester 2 in master mode - Tester 1 in slave mode
Device type	All devices [x] MOST25 [x] MOST50 oPHY [x] MOST50 ePHY
Multi Node Device	All devices
Note	Procedure for unlock generation described in para 3.1 (General Notes) If DUT does not support wake-up neither via MOST nor via electrical wake-up, the second part of the test (t_Restart) is not applicable If DUT is PM, t_Restart_max must not be considered.
Results	DUT ok: The DUT has passed the test. DUT not ok (1): The DUT fails to switch off the MOST signal right in time. DUT not ok (2): The DUT fails to wait for t_Restart before switching on the MOST signal again (OR: <i>el. Wake-up/MOST signal wake-up not supported</i>)

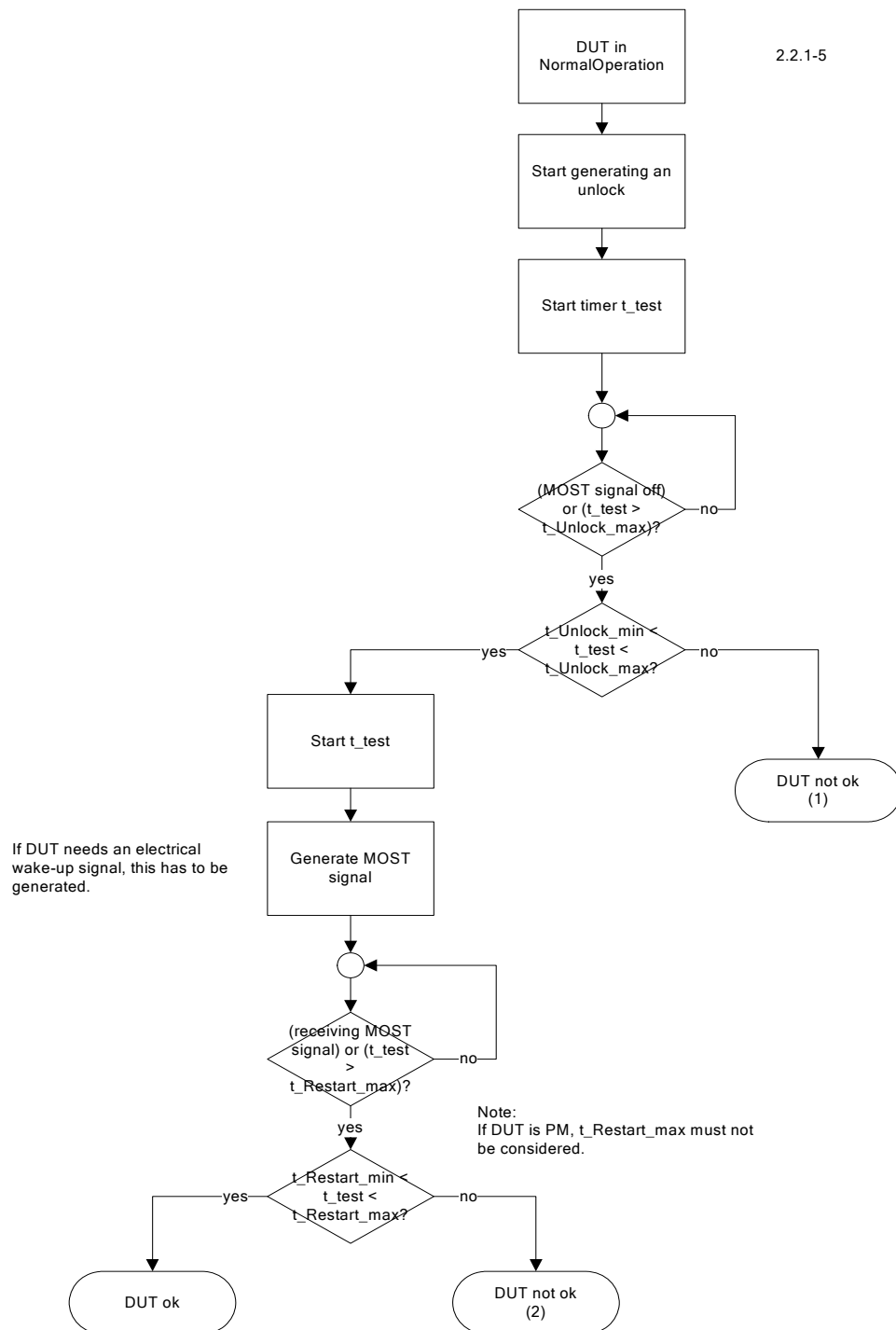


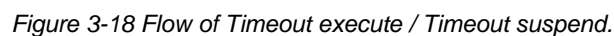
Figure 3-17 Flow of Critical unlock test.

3.6 Power Management

3.6.1 Power Management – Power Master

3.6.1.1 Timeout execute / Timeout suspend (2.3.1-3)

Name of test	Timeout execute / Timeout suspend 2.3.1-3
Reference to MOST Specification	Rev. 2.4: 3.2.4.2 Network Shutdown Rev. 2.5: 3.2.4.2 Network Shutdown
Value of Interest	ShutDown.Start(Query) t_Suspend ShutDown.Start(Execute) t_ShutDownWait
Start Conditions	DUT in NormalOperation
Test description	The DUT is triggered to perform a ShutDown (via FBlock ET). It has to perform a shutdown by sending ShutDown.Start(Query). Then it has to wait for t_Suspend_min (test 2.3.1-6 could be performed) before sending a ShutDown.Start(Execute). The DUT has to wait for t_ShutDownWait_min (ignoring ShutDown.Result(Suspend)) before switching off the MOST signal.
Experimental set-up	#1 - Tester in master mode or in slave mode (depends on DUT)
Device type	PM [x] MOST25 [x] MOST50 oPHY [x] MOST50 ePHY
Multi Node Device	All devices containing PM. The power slaves must be configured to not send ShutDown.Result(Suspend).
Note	
Result	DUT ok (1): The DUT has passed the test. DUT not ok (1): The DUT fails to start the shutdown procedure. DUT not ok (2): DUT sends ShutDown.Start(Execute) too early. DUT not ok (3): DUT switches off the MOST signal too early. DUT not ok (4): DUT switches off the MOST signal too late. DUT not ok (5): DUT fails to perform NormalOperation during t_Suspend



3.6.1.2 Timeout suspend (2.3.1-6)

Name of test	Timeout suspend 2.3.1-6
Reference to MOST Specification	Rev. 2.4: 3.2.4.2 Network Shutdown Rev. 2.5: 3.2.4.2 Network Shutdown
Value of Interest	t_Suspend ShutDown.Start(Suspend) t_RetryShutDown
Start Conditions	DUT in NormalOperation
Test description	The test is continued from 2.3.1-2. The tester sends a ShutDown.Result(Suspend) to the DUT before t_Suspend expires. The DUT has to stay in NormalOperation until t_RetryShutDown_min expires. Then it has to start a new ShutDown attempt before t_RetryShutDown_max expires.
Experimental set-up	#1 - Tester in master mode or in slave mode (depends on DUT)
Device type	PM [x] MOST25 [x] MOST50 oPHY [x] MOST50 ePHY
Multi Node Device	All devices containing PM. The power slaves must be configured to not send ShutDown.Result(Suspend).
Note	
Results	DUT ok: The DUT has passed the test. DUT not ok (1): The DUT fails to perform NO during t_RetryShutDown. DUT not ok (2): DUT sends ShutDown.Start(Execute) too early. DUT not ok (3): DUT fails to send ShutDown.Start(Query). DUT not ok (4): DUT sends ShutDown.Start(Query) too early.

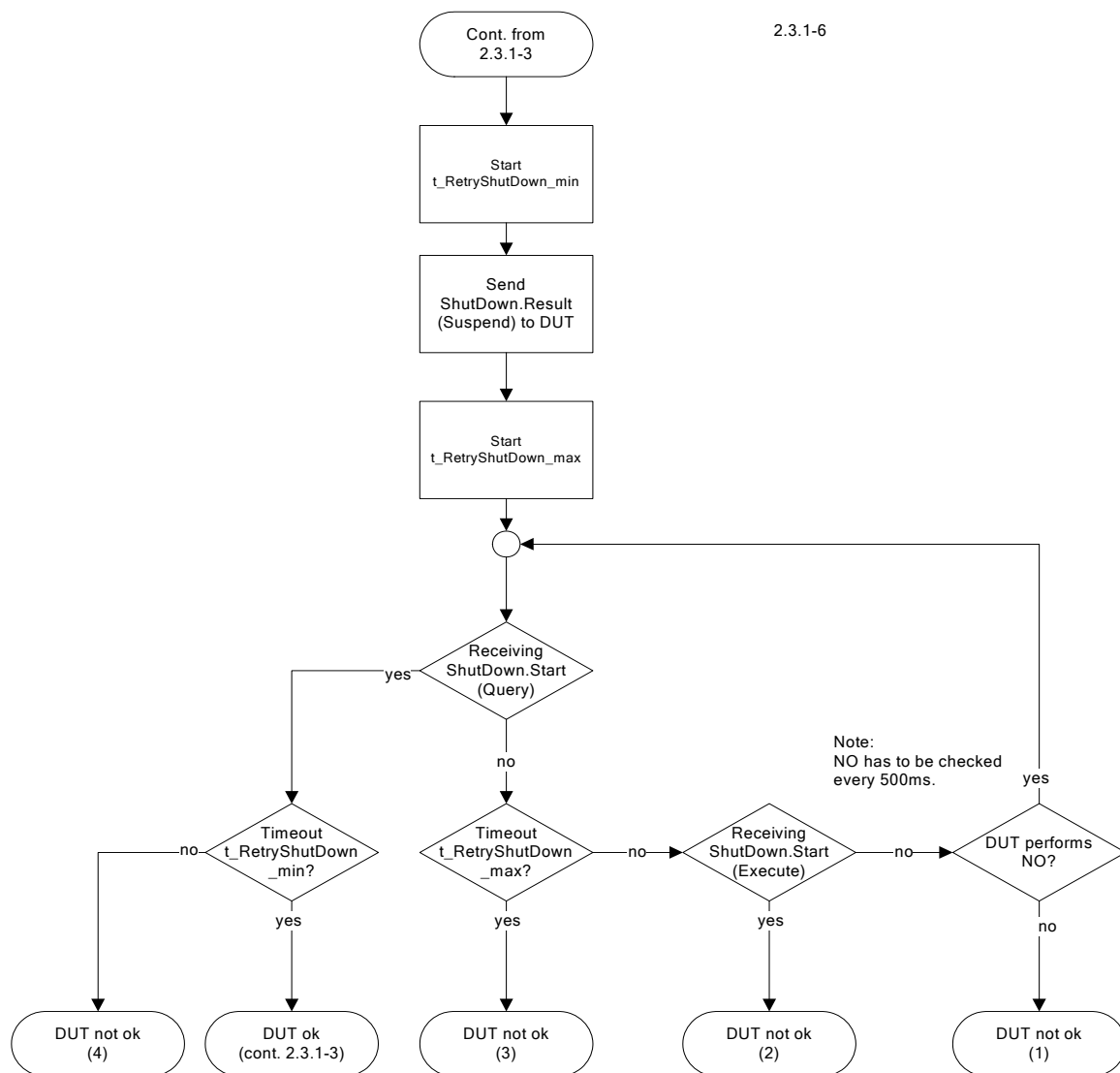


Figure 3-19 Flow of Timeout suspend.

3.6.1.3 Temperature ShutDown Reaction Test (2.3.3-5)

Name of test	Temperature ShutDown Reaction Test 2.3.3-5
Reference to MOST Specification	Rev. 2.4: 3.2.6.2 Levels of Temperature Alert 3.2.6.3 Re-Start Behavior MOST Dynamic Specification: 5.7 Network restart after over-temperature shutdown Rev. 2.5: 3.2.5.6.1 Levels of Temperature Alert 3.2.5.6.1 Re-Start Behavior MOST Dynamic Specification: 5.7 Network restart after over-temperature shutdown
Value of Interest	t_WaitAfterOvertempShutdown
Start Conditions	DUT: NormalOperation
Test description	Tester 1 sets the DUT into "overtemperature mode" by sending ShutDown.Result(TemperatureShutDown) and switches off the MOST signal. The DUT has to wait for t_WaitAfterOvertempShutdown before performing restart.
Experimental set-up	# 1 - Tester 1 in master mode or slave mode (depends on DUT)
Device type	PM [x] MOST25 [x] MOST50 oPHY [x] MOST50 ePHY
Multi Node Device	All devices containing PM
Note	
Results	DUT ok: The DUT has passed the test. DUT not ok: DUT fail to enter overtemperature mode (does not keep t_WaitAfterOvertempShutdown) before restart

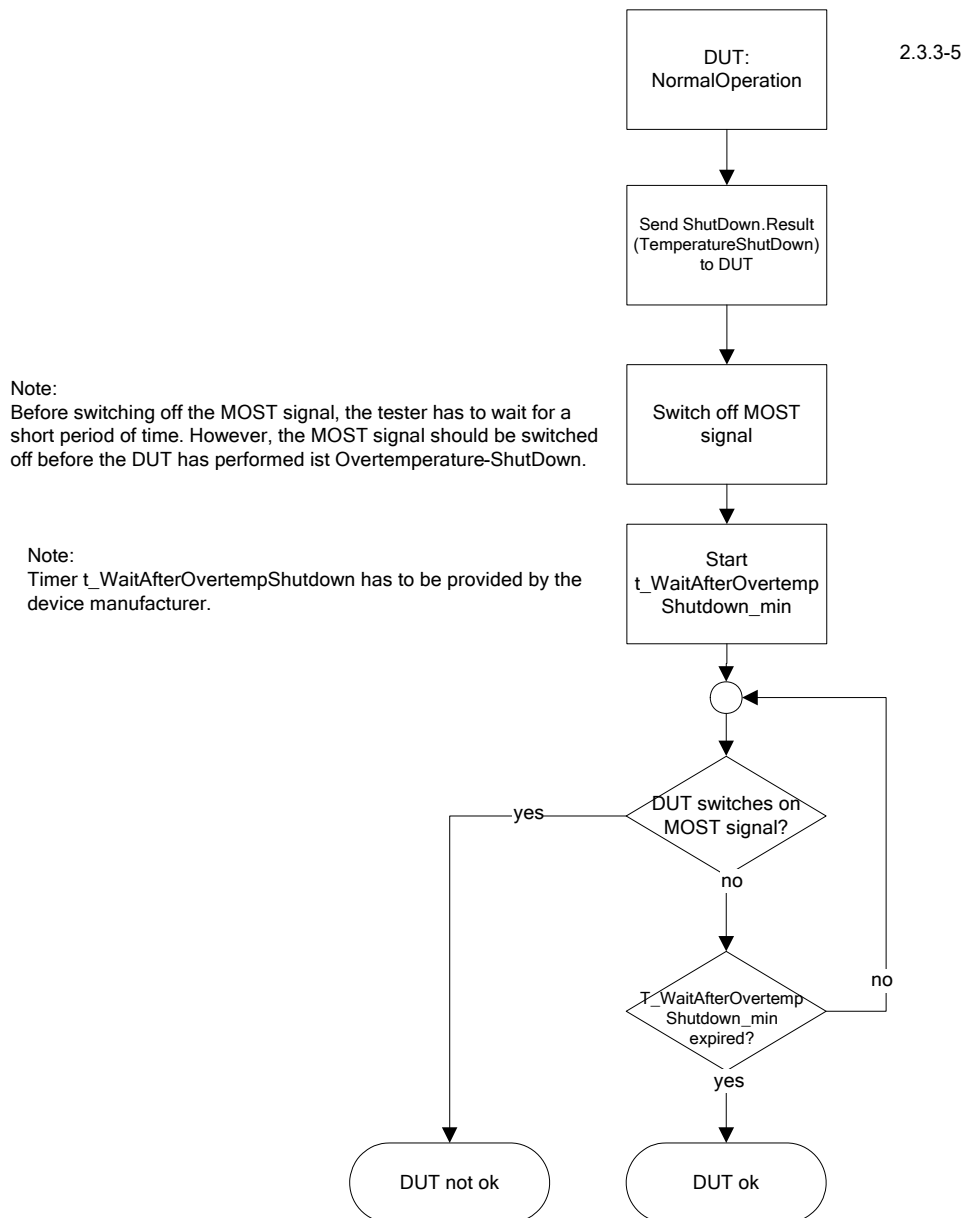


Figure 3-20 Flow of Temperature ShutDown Reaction test.

3.6.2 Power Management – Power Slave

3.6.2.1 ShutDown.Start(Query) test (2.3.2-2)

Name of test	ShutDown.Start(Query) test 2.3.2-2
Reference to MOST Specification	Rev. 2.4: 3.2.4.2 Network Shutdown Rev. 2.5: 3.2.4.2 Network Shutdown
Value of Interest	Message ShutDown.Start(Query) Message ShutDown.Result(Suspend) t_Suspend
Start Conditions	DUT in NormalOperation.
Test description	The DUT is triggered (via FB ET) to perform any activity that must not be interrupted by a ShutDown. The tester sends a ShutDown.Start(Query). The DUT has to send a ShutDown.Result(Suspend) within t_Suspend_max to the tester. The tester waits for t_RetryShutDown. The slave's activity that must not be interrupted by a ShutDown has to be stopped (via FBlock ET). The tester sends a ShutDown.Start(Query), again. The DUT has to continue performing NormalOperation.
Experimental set-up	#1 - Tester in master mode or in slave mode or slave mode (depends on DUT)
Device type	All devices except PM [x] MOST25 [x] MOST50 oPHY [x] MOST50 ePHY
Multi Node Device	All devices without PM
Note	
Results	DUT ok (1): The DUT has passed the test. DUT ok (2): DUT does not support ShutDown.Suspend mechanism DUT not ok (1): The DUT fails to send a ShutDown.Result(Suspend) within t_Suspend. DUT not ok (2): The DUT fails to continue NO after ShutDown.Start(Query). DUT not ok (3): The DUT sends any ShutDown.Result. DUT not ok (4): The DUT switches off MOST signal by itself.

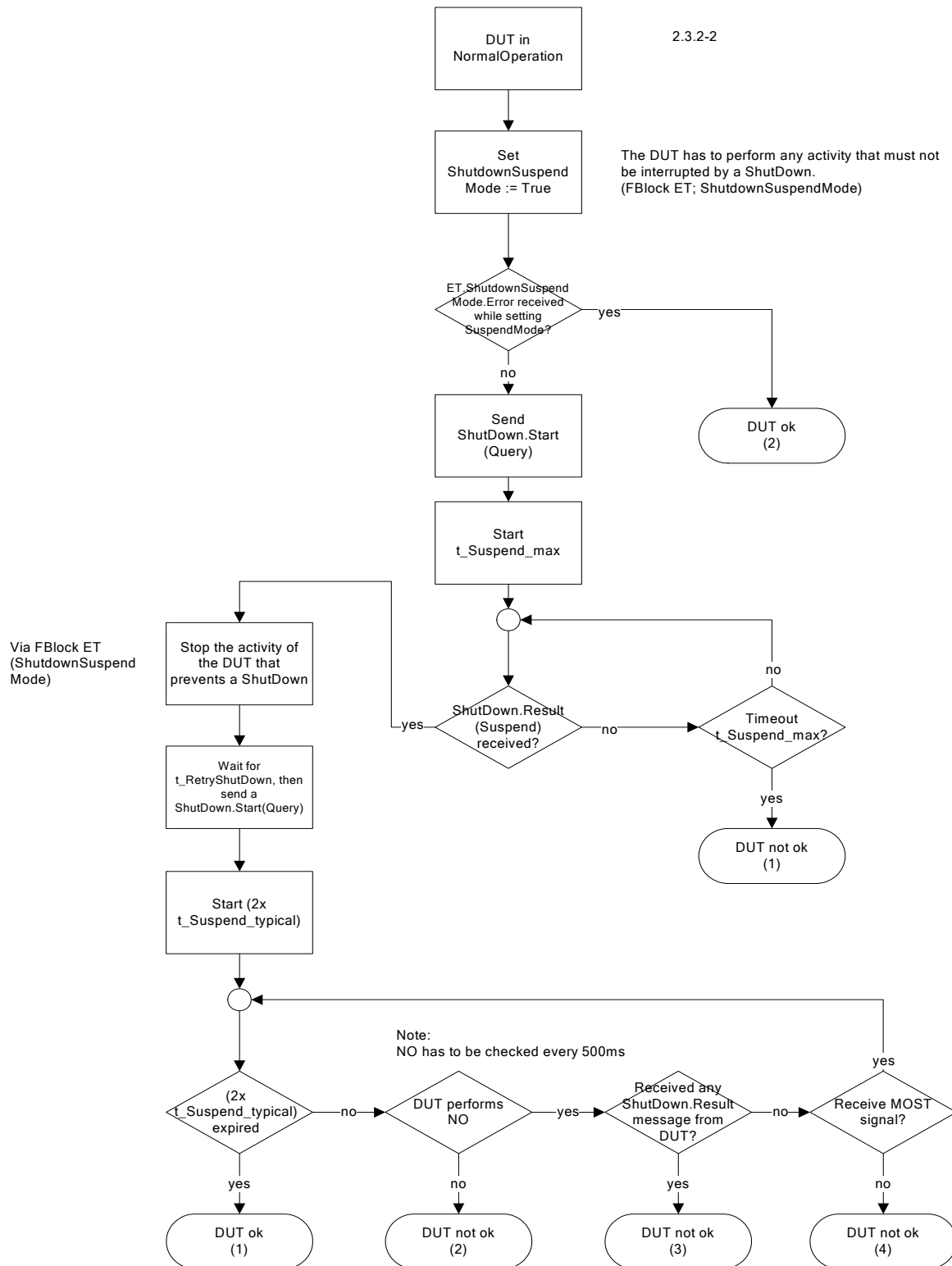


Figure 3-21 Flow of ShutDown.Start(Query) test.

3.6.2.2 ShutDown.Start(Execute) test (2.3.2-3)

Name of test	ShutDown.Start(Execute) test 2.3.2-3
Reference to MOST Specification	Rev. 2.4: 3.2.4.2 Network Shutdown Rev. 2.5: 3.2.4.2 Network Shutdown
Value of Interest	Message ShutDown.Start(Execute) t_PwrSwitchOffDelay t_ShutDown
Start Conditions	The test continues directly from “ShutDown.Start(Query) test” (2.3.2-2).
Test description	After sending ShutDown.Start(Query) and waiting for t_Suspend, the tester sends a ShutDown.Start(Execute). The DUT has to prepare for shutdown. It has to continue performing NormalOperation. All kinds of communication have to be possible until the tester switches off the MOST signal. The DUT must not switch off the signal by itself. If the tester switches off the MOST signal, the DUT must not enter SleepMode before t_PwrSwitchOffDelay_min is expired.
Experimental set-up	#1 - Tester in master mode or slave mode (depends on DUT) - Power consumption of DUT is monitored
Device type	All devices except PM [x] MOST25 [x] MOST50 oPHY [x] MOST50 ePHY
Multi Node Device	All devices without PM
Note	
Results	DUT ok: The DUT has passed the test. DUT not ok (1): DUT fails to perform NormalOperation after reception of ShutDown.Start(Execute). DUT not ok (2): DUT switches off MOST signal by itself too early. DUT not ok (3): DUT fails to switch off MOST signal within t_ShutDown. DUT not ok (4): DUT enters SleepMode too early.

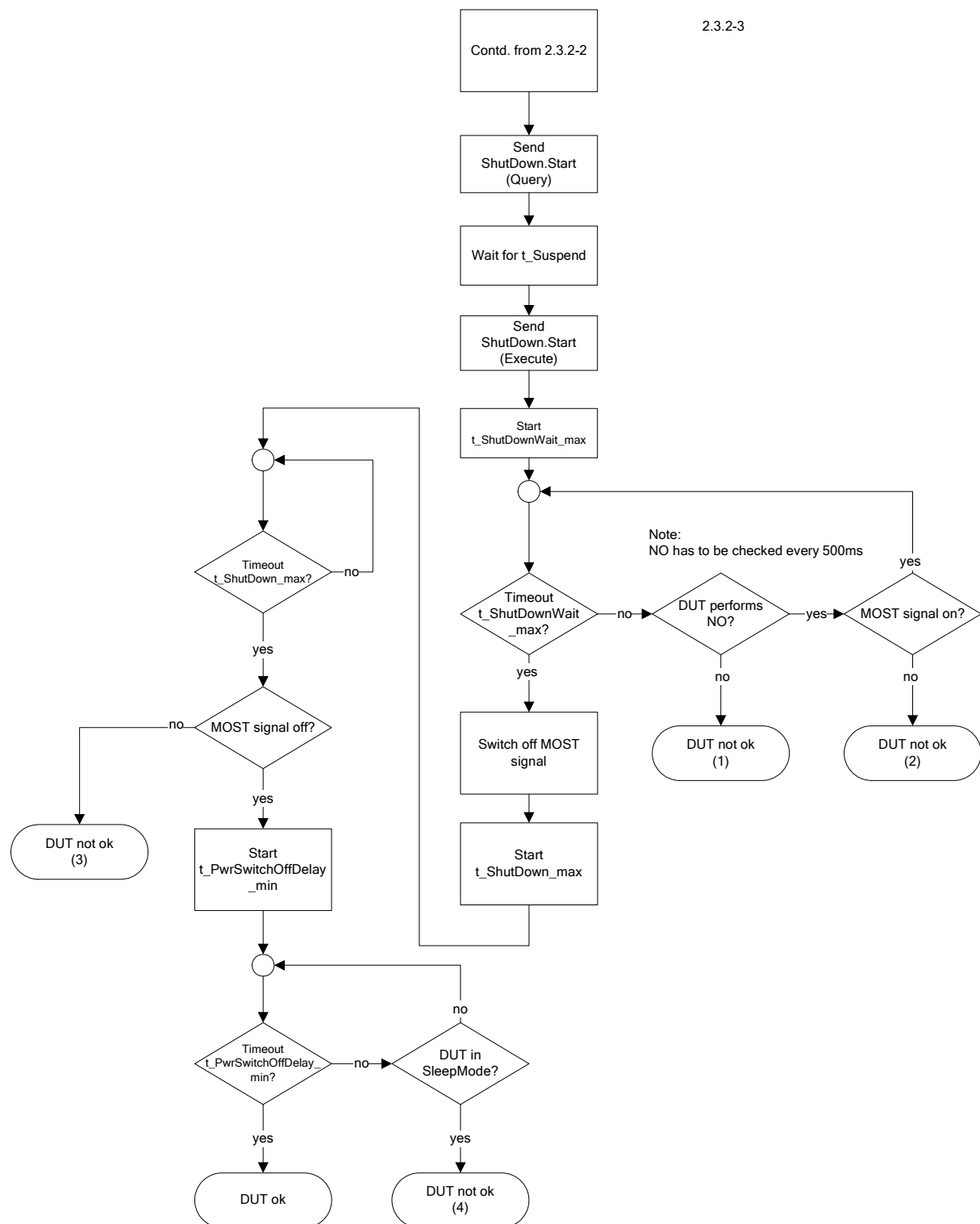
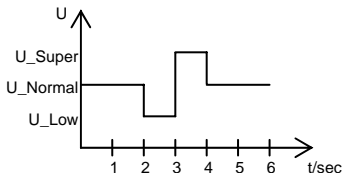


Figure 3-22 Flow of ShutDown.Start(Execute) test.

3.6.3 Power Management – General

3.6.3.1 Over-/ undervoltage test (2.3.3-1)

Name of test	Over-/ undervoltage test 2.3.3-1
Reference to MOST Specification	Rev. 2.4: 3.2.5.8 Low Voltage / 4.7 Voltage Levels Rev. 2.5: 3.2.5.5 Supply Voltage / 4.6 Voltage Levels
Value of Interest	Supply voltage (depends on manufacturer of the DUT)
Start Conditions	DUT: NetInterfacePowerOff
Test description	The voltage level of the power supply is adjusted to normal operation level (U_Normal). The voltage level is reduced to U_Low, then it is increased to U_Super. Every voltage level is hold for 1 second. The DUT must not start generating any kind of MOST signal.
Experimental set-up	#1 - Tester in slave mode or master mode (depends on DUT) - no MOST signal generation
Device type	All devices except PM [x] MOST25 [x] MOST50 oPHY [x] MOST50 ePHY
Multi Node Device	All devices without PM
Note	<p>Test has to be performed twice:</p> <p>a) Ramp function of the power supply for testing: Increasing and decreasing of voltage with ramps of 0,1V/0,5s.</p> <p>b) Square egde function of the power supply for testing:</p>  <p>The voltage levels U_Low, U_Normal and U_Super are provided by the manufacturer and can be read out via FBlock ET.</p>
Results	<p>DUT ok: The DUT has passed the test.</p> <p>DUT not ok (1): Change to U_Low results in MOST signal generation.</p> <p>DUT not ok (2): Change to U_Super results in MOST signal generation.</p> <p>DUT not ok (3): Change to U_Normal results in MOST signal generation.</p>

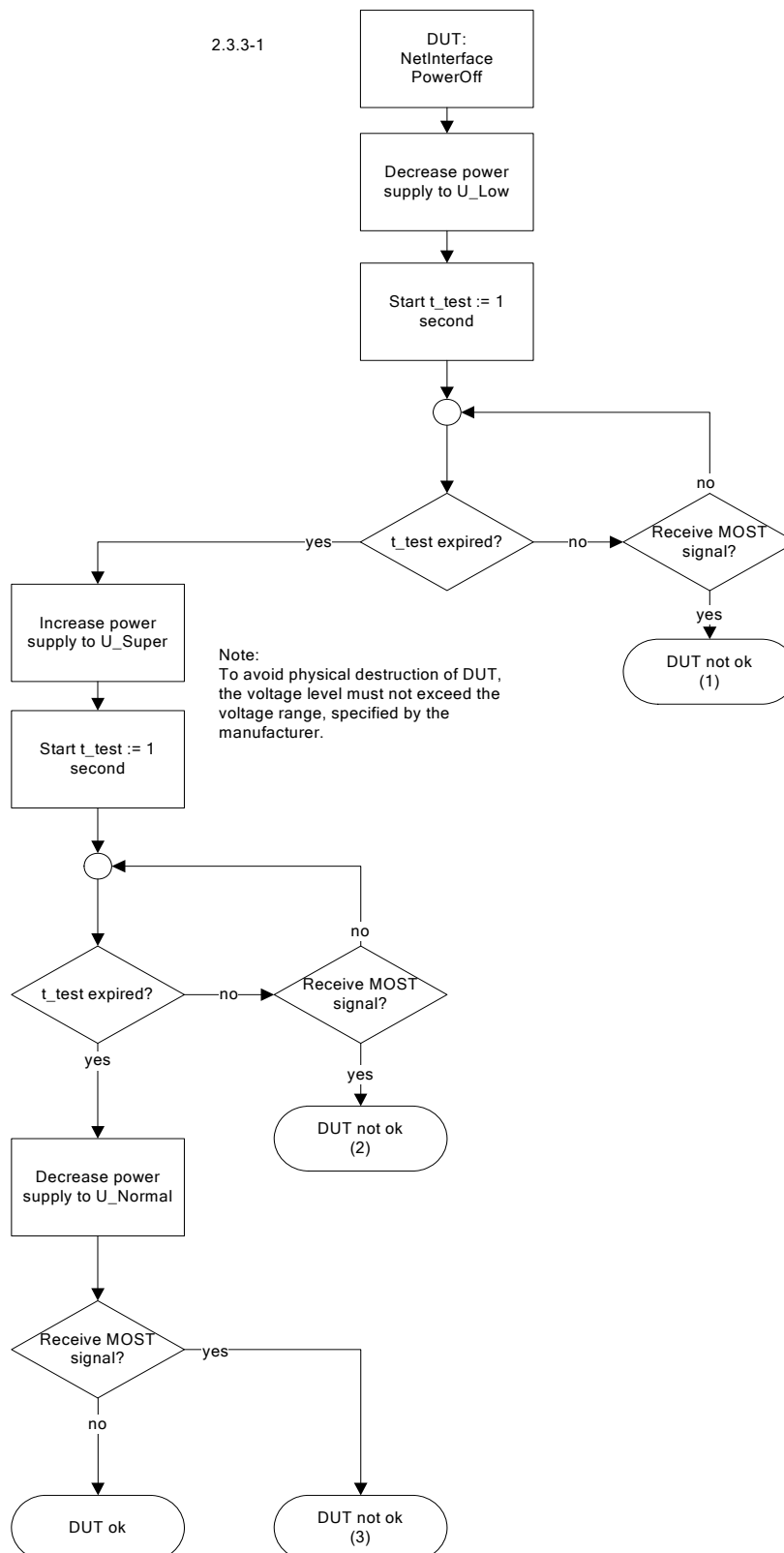


Figure 3-23 Flow of Over-/ undervoltage test.

3.6.3.2 Over-temperature signal off test (2.3.3-4)

Name of test	Over-temperature signal off test 2.3.3-4
Reference to MOST Specification	Rev. 2.4: 3.2.6 Over-Temperature Management Rev. 2.5: 3.2.5.6 Over-Temperature Management
Value of Interest	NetBlock.ShutdownResult(0x03)
Start Conditions	DUT in NormalOperation
Test description	The DUT performs NormalOperation. The temperature is increased to 9_shutdown (simulated by FBlock ET only). The DUT has to send a temperature warning message; communication must not be disturbed. The temperature is increased again to 9_dead (simulated by FBlock ET only). The DUT has to switch off MOST signal.
Experimental set-up	#2 - Tester 1 in slave mode or master mode (depends on DUT) - Tester 2 in slave mode
Device type	All devices [x] MOST25 [x] MOST50 oPHY [x] MOST50 ePHY
Multi Node Device	All devices
Note	This test covers overtemperature behavior only. All other critical events are covered by other tests, already (Undervoltage / Overvoltage: 2.4.2-1; Unlock: 2.2.1-5)
Results	DUT ok (1): The DUT has passed the test. DUT ok (2): DUT does not support temperature monitoring / management DUT not ok (1): DUT fails to send temperature warning message. DUT not ok (2): DUT disturbs communication. DUT not ok (3): DUT fails to switch off in case of over-temperature.

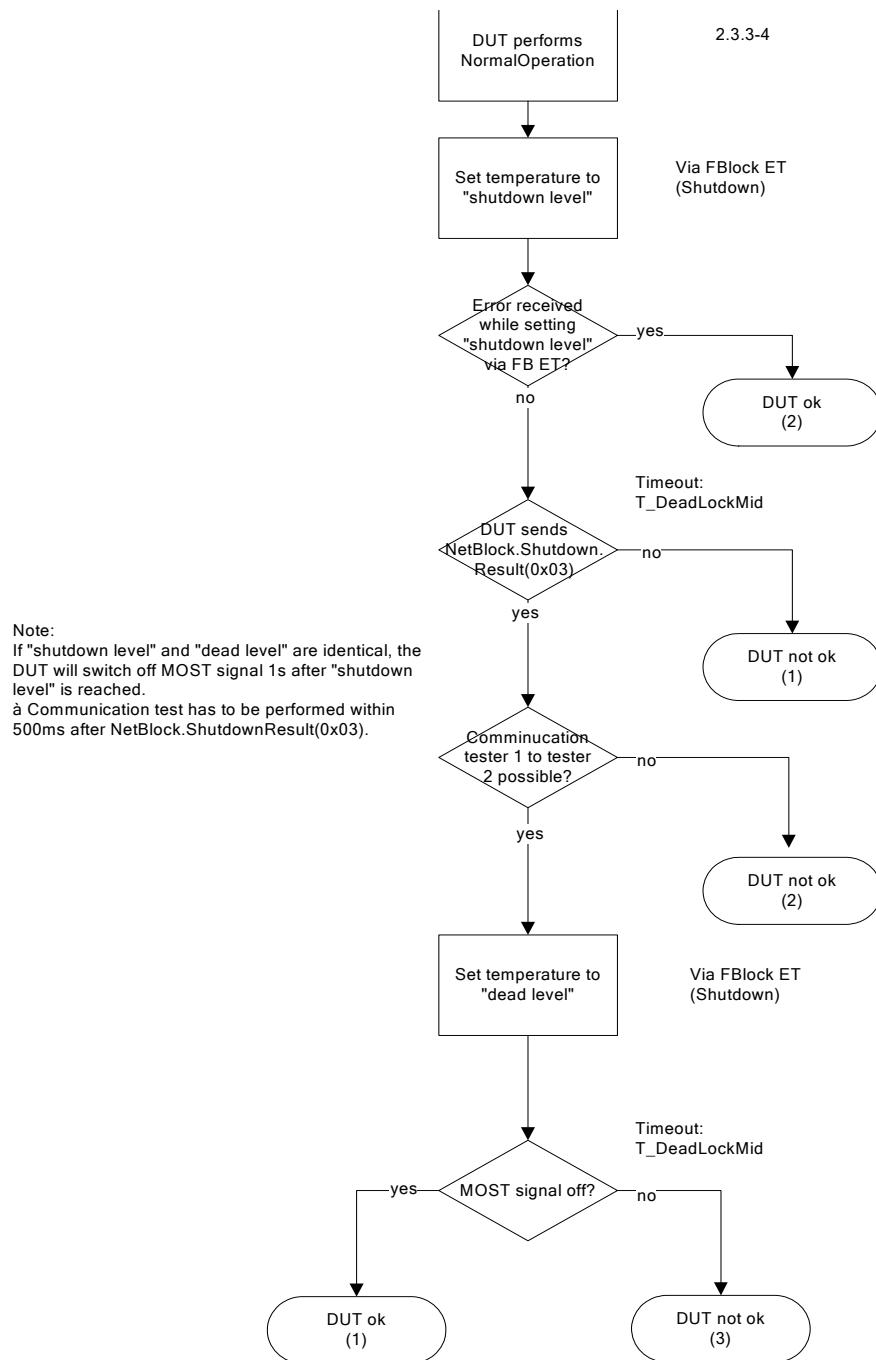


Figure 3-24 Flow of Over-temperature signal off test.

3.7 Error Management

3.7.1 Error Management – MOST Signal Path

3.7.1.1 Restart stop test (2.4.1-1) MOST2V4

Name of test	Restart stop test (MOST2V4) 2.4.1-1
Reference to MOST Specification	Rev. 2.4: 3.2.5.2.1 Waking
Value of Interest	Wake-up retries t_Restart t_Config
Start Conditions	DUT in NormalOperation
Test description	The AbilityToWake of the DUT will be set to TRUE and the AutoWakeup will be triggered by the tester via FB ET (it will be generated permanently during the whole test). Then the ring will be interrupted before the DUT. The DUT has to perform up to three wake-up retries. Then it has to stop the wake-up attempts.
Experimental set-up	#1 - Tester in slave mode or master mode (depends on DUT).
Device type	All devices except PM.
Multi Node Device	All devices without PM
Note	Test not applicable to a device that includes the PM.
Results	DUT ok (1): The DUT is not able to perform active wake-ups. DUT ok (2): The DUT has passed the test. DUT not ok (1): The DUT performs too much / too less wake-up retries. DUT not ok (2): The DUT performs more than three wake-up retries.

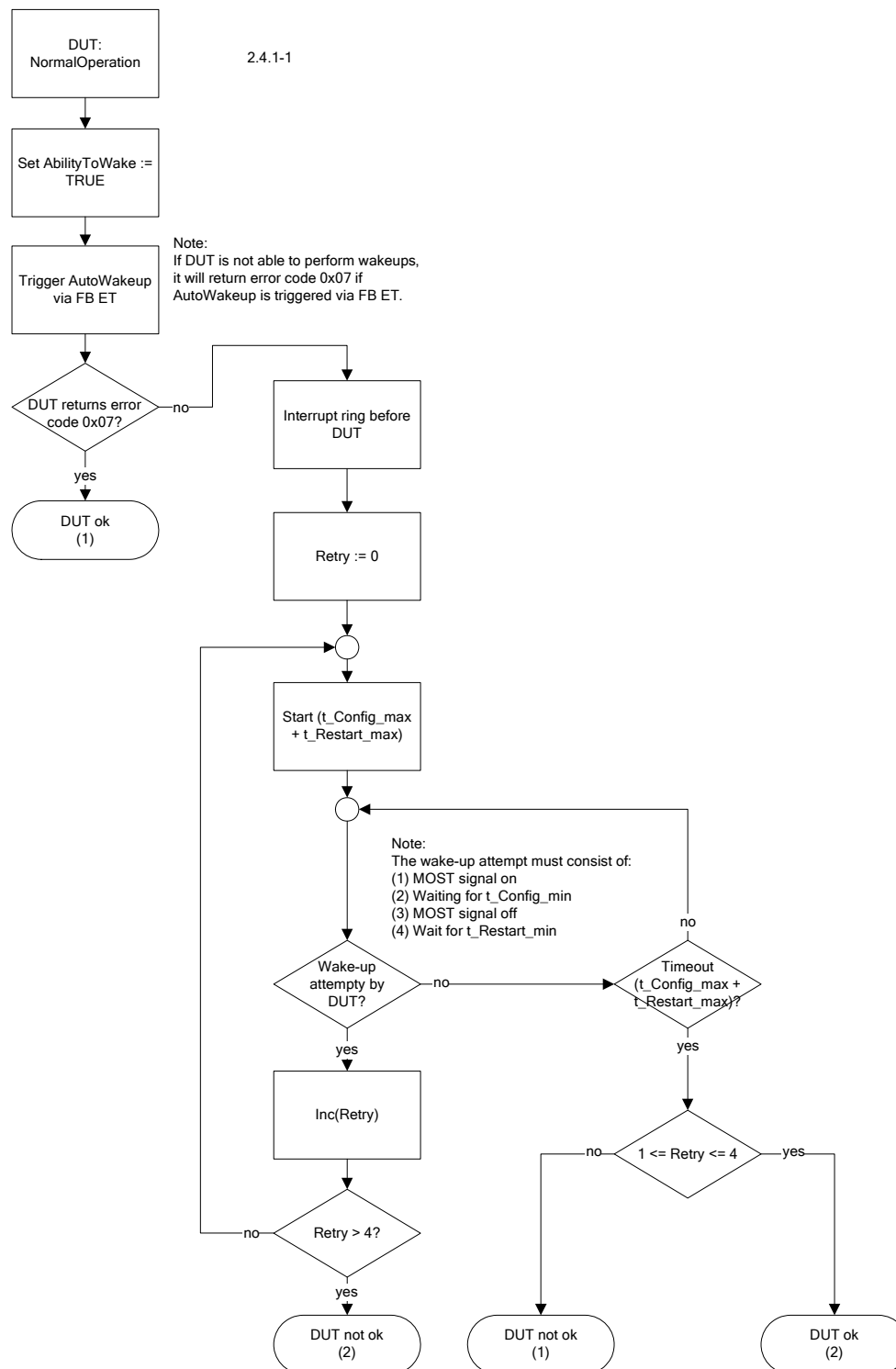


Figure 3-25 Flow of Restart stop test MOST2V4.

3.7.1.2 Restart stop test (2.4.1-1) MOST2V5

Name of test	Restart stop test (MOST2V5) 2.4.1-1
Reference to MOST Specification	Rev. 2.5: 3.2.5.1.2 Waking
Value of Interest	Wake-up retries t_Restart t_Config
Start Conditions	DUT in NormalOperation
Test description	The PermissionToWake of the DUT will be set to TRUE and the AutoWakeup will be triggered by the tester via FB ET (it will be generated permanently during the whole test). Then the ring will be interrupted before the DUT. The DUT has to perform up to three wake-up retries. Then is has to stop the wake-up attempts.
Experimental set-up	#1 - Tester in slave mode or master mode (depends on DUT).
Device type	All devices except PM. [x] MOST25 [x] MOST50 oPHY [] MOST50 ePHY
Multi Node Device	All devices without PM
Note	Test not applicable to a device that includes the PM.
Results	DUT ok (1): The DUT has passed the test (CapabilityToWake = FALSE). DUT ok (2): The DUT has passed the test. DUT not ok (1): The DUT performs too much / too less wake-up retries. DUT not ok (2): The DUT performs more than three wake-up retries.

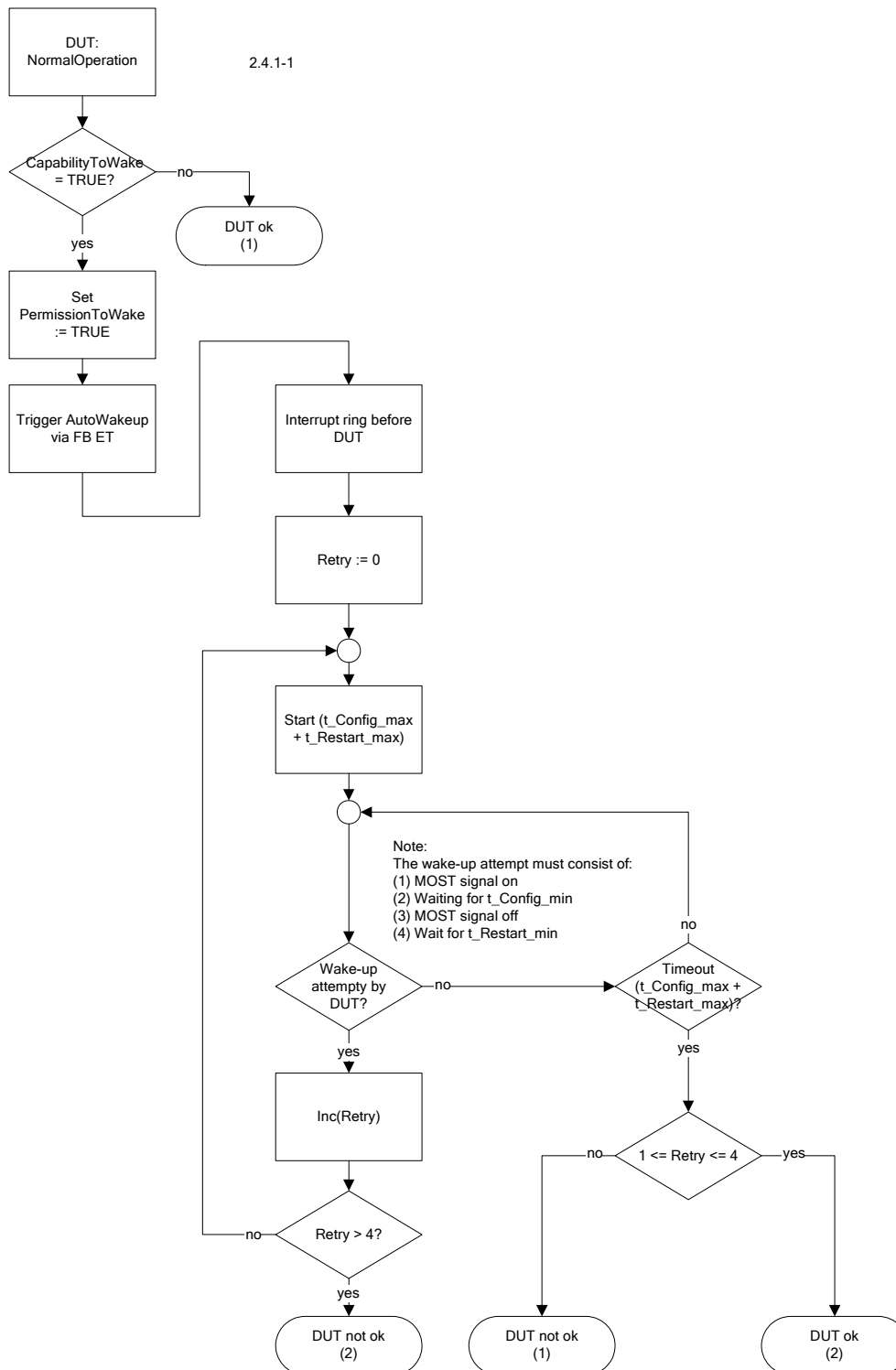


Figure 3-26 Flow of Restart stop test MOST2V5.

3.7.1.3 Restart continue test (2.4.1-2) MOST2V4

Name of test	Restart continue test (MOST2V4) 2.4.1-2
Reference to MOST Specification	Rev. 2.4: 3.2.5.2.1 Waking
Value of Interest	Wake-up retries t_Restart t_Config
Start Conditions	DUT in NormalOperation
Test description	The AbilityToWake of the DUT will be set to TRUE and the AutoWakeup will be triggered by the tester via FB ET (it will be generated permanently during the whole test). Then the ring will be interrupted before the DUT. The DUT has to perform at least three wake-up retries. If the DUT generates more than three wake-up retries, it has to stop as soon as the wake-up event is stopped.
Experimental set-up	#1 - Tester in slave mode or in master mode (depends on DUT)
Device type	PM
Multi Node Device	All devices containing PM
Note	
Results	DUT ok (1): The DUT has passed the test (stops after three retries). DUT ok (2): The DUT has passed the test (perform wake-ups as long as the wake-up event is active). DUT ok (3): The DUT is not able to perform active wake-ups. DUT not ok (1): The DUT fails to perform at least three wake-up retries. DUT not ok (2): The DUT continues wake-up attempts without wake-up event.

2.4.1-2

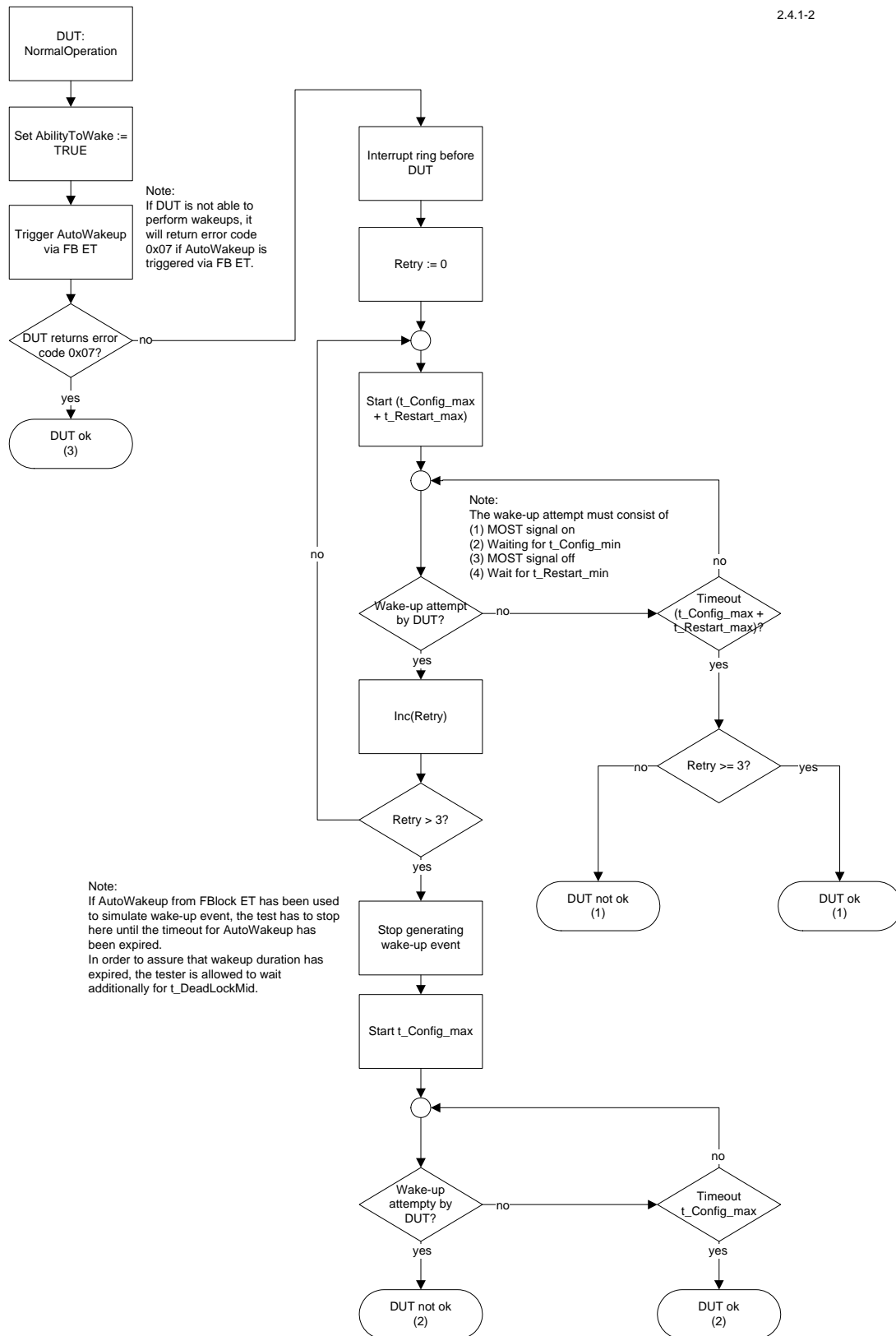


Figure 3-27 Flow of Restart continue test MOST2V4.

3.7.1.4 Restart continue test (2.4.1-2) MOST2V5

Name of test	Restart continue test (MOST2V5) 2.4.1-2
Reference to MOST Specification	Rev. 2.5: 3.2.5.1.2 Waking
Value of Interest	Wake-up retries t_Restart t_Config
Start Conditions	DUT in NormalOperation
Test description	The PermissionToWake of the DUT will be set to TRUE and the AutoWakeup will be triggered by the tester via FB ET (it will be generated permanently during the whole test). Then the ring will be interrupted before the DUT. The DUT has to perform at least three wake-up retries. If the DUT generates more than three wake-up retries, it has to stop as soon as the wake-up event is stopped.
Experimental set-up	#1 - Tester in slave mode or in master mode (depends on DUT)
Device type	PM [x] MOST25 [x] MOST50 oPHY [] MOST50 ePHY
Multi Node Device	All devices containing PM
Note	
Results	DUT ok (1): The DUT has passed the test (stops after three retries). DUT ok (2): The DUT has passed the test (perform wake-ups as long as the wake-up event is active). DUT ok (3): The DUT has passed the test (CapabilityToWake = FALSE). DUT not ok (1): The DUT fails to perform at least three wake-up retries. DUT not ok (2): The DUT continues wake-up attempts without wake-up event.

2.4.1-2

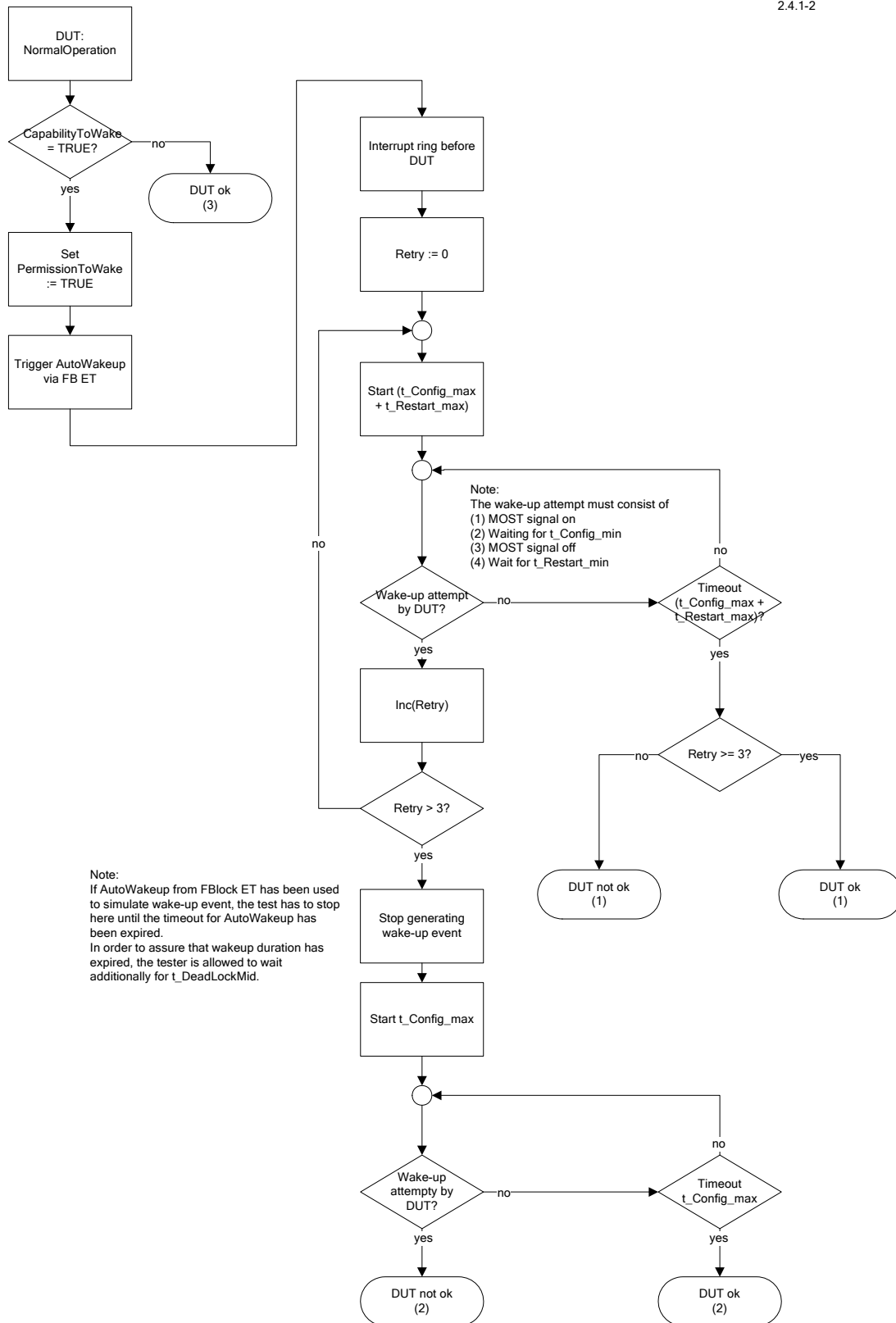


Figure 3-28 Flow of Restart continue test MOST2V5.

3.7.1.5 Reaction of NCE test (2.4.1-9)

Name of test	Reaction of NCE test 2.4.1-9
Reference to MOST Specification	Rev. 2.4: 3.2.5.4 Network Change Event Rev. 2.5: 3.2.5.3 Network Change Event
Value of Interest	NCE t_WaitAfterNCE ConfigStatus(Invalid)
Start Conditions	DUT in NormalOperation
Test description	Tester 2 opens its bypass. The DUT has to check the system configuration between t_WaitAfterNCE_min and t_DeadLockMid. It has to send ConfigStatus(New). Tester 2 closes its bypass. The DUT has to check the system configuration between t_WaitAfterNCE_min and t_DeadLockMid. It has to send ConfigStatus(Invalid). Tester 2 generates NCE without FBlock changes (tester 2 opens all-bypass; waits for t_WaitAfterNCE_min/2 and closes the bypass again). . The DUT has to check the system configuration between t_WaitAfterNCE_min and t_DeadLockMid. It has to send ConfigStatus(New) with empty FBlock list.
Experimental set-up	#2 - Tester 1 in slave mode or master mode (depends on DUT) - Tester 2 in slave mode; bypass closed
Device type	NWM [x] MOST25 [x] MOST50 oPHY [x] MOST50 ePHY
Multi Node Device	All devices containing NWM. Test case "NCE with unlock" only applicable if no TM node in front of NWM node inside the DUT.
Note	The test has to be performed twice: a) With and b) without unlock during NCE generation. To generate a NCE without unlock, the NCE has to be generated between DUT and TM. In this case, the DUT only detects the NCE, not the unlock. Tester1 has to respond to every system configuration check of the DUT normally but with other FBlocks than tester2.
Results	DUT ok (a/b): The DUT has passed the test. DUT not ok (1a/1b): The DUT fails to check the system configuration. DUT not ok (2a/2b): The DUT fails to send ConfigStatus(New). DUT not ok (3a/3b): The DUT fails to check the system configuration. DUT not ok (4a/4b): The DUT fails to send ConfigStatus(Invalid). DUT not ok (5a/5b): The DUT checks the system configuration too early. DUT not ok (6a/6b): The DUT checks the system configuration too early. DUT not ok (7a/7b): The DUT fails to check the system configuration. DUT not ok (8a/8b): The DUT checks the system configuration too early. (fails to restart t_WaitAfterNCE if new NCE occurs after timer has been started) DUT not ok (9a/9b): The DUT fails to send ConfigStatus(New) with empty FBlock list.

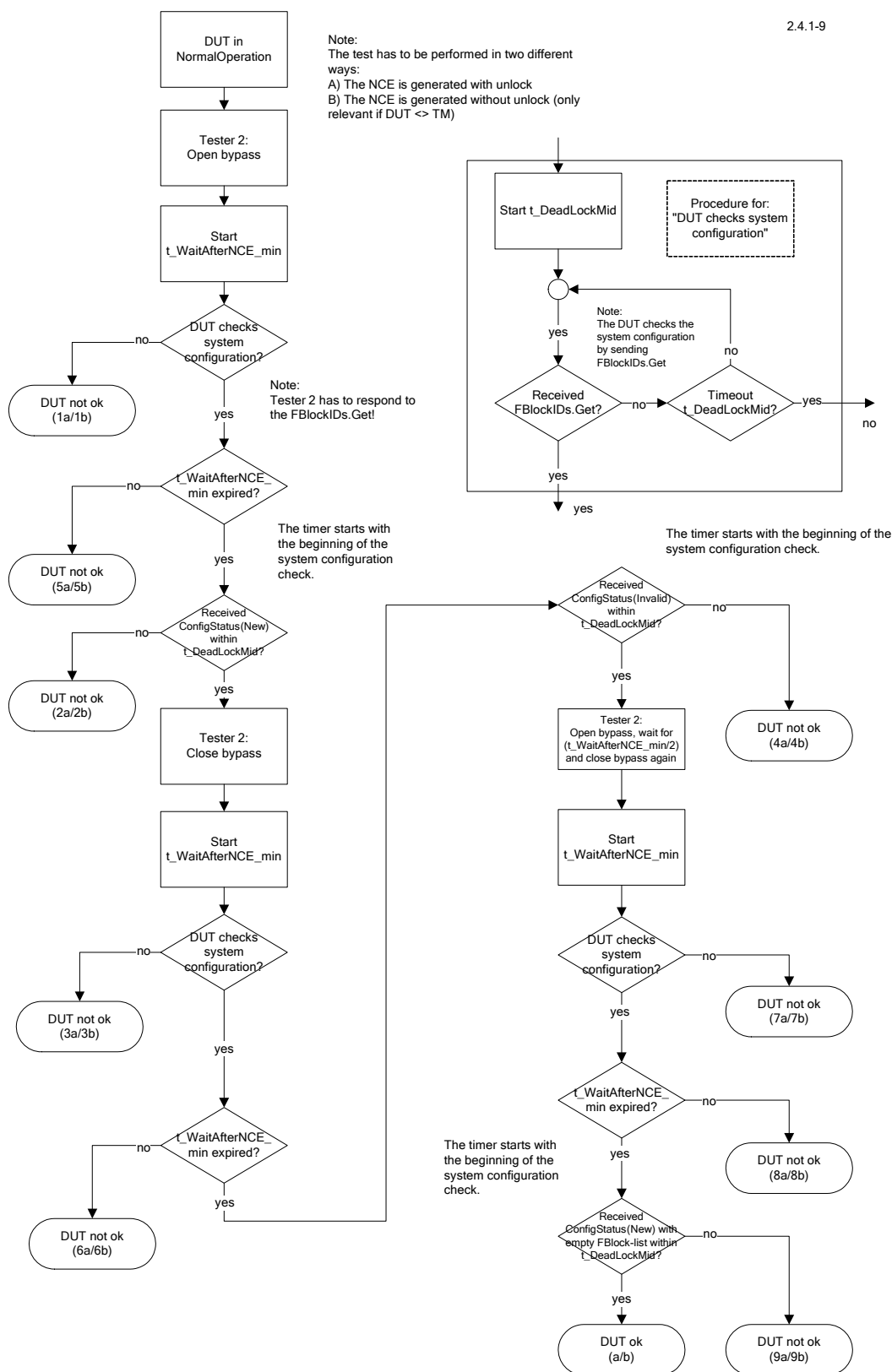


Figure 3-29 Flow of Reaction of NCE test.

3.7.1.6 Re-enter delay test (2.4.1-11)

Name of test	Re-enter delay test 2.4.1-11
Reference to MOST Specification	Rev. 2.4: 3.9 Timing Definitions Rev. 2.5: 3.8 Timing Definitions
Value of Interest	t_Bypass
Start Conditions	DUT in NormalOperation
Test description	The DUT is triggered to leave the ring for a short time (via FBlock ET; Reset). It must not enter the ring again, before t_Bypass_min expires.
Experimental set-up	#1 - Tester 1 in master mode
Device type	All devices except TM. [x] MOST25 [x] MOST50 oPHY [] MOST50 ePHY
Multi Node Device	All devices without TM
Note	By means of monitoring the MPR register, the tester could detect whether DUT opens / closes bypass
Results	DUT ok: The DUT has passed the test. DUT not ok (1): The DUT enters the ring too early.

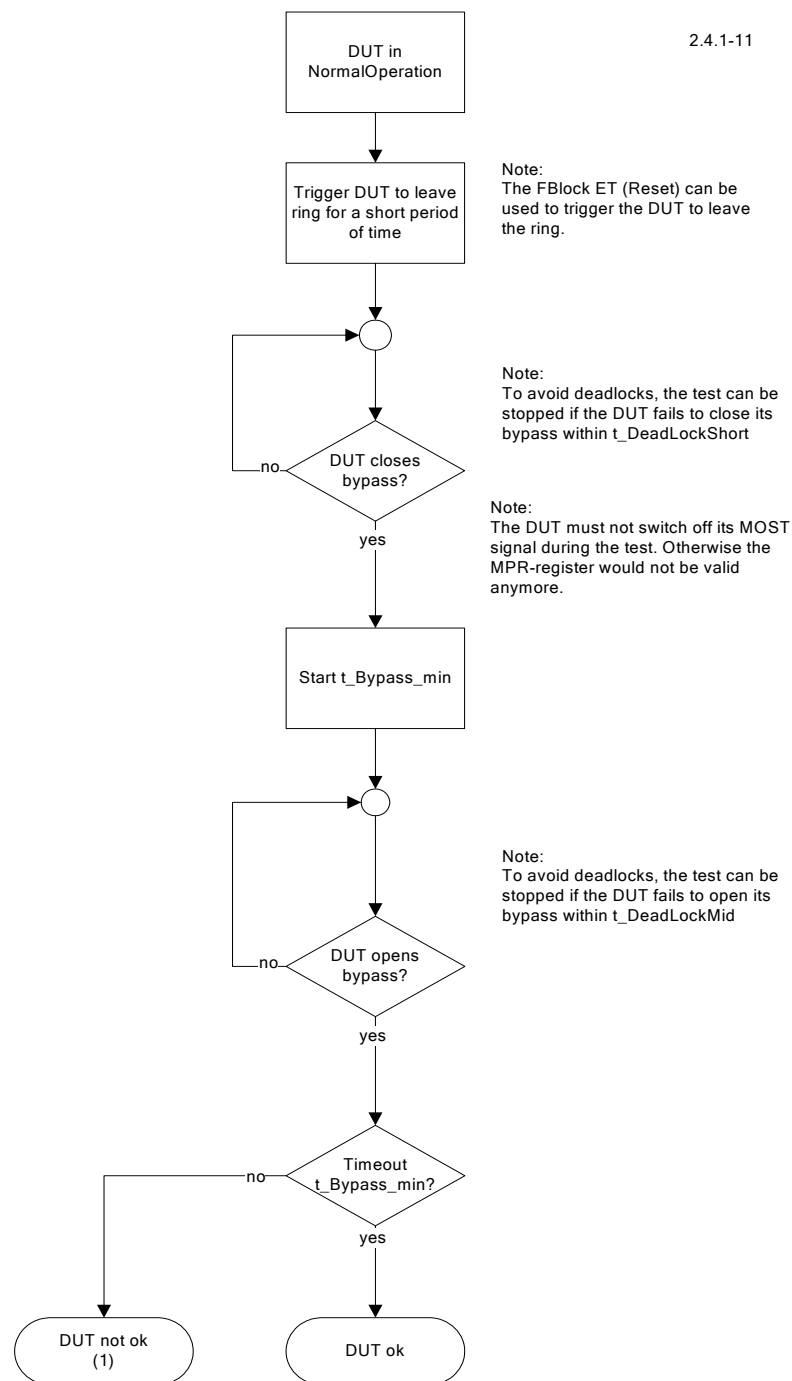


Figure 3-30 Flow of Re-enter delay test.

3.7.2 Error Management – Voltage Level

3.7.2.1 Critical Voltage test (2.4.2-1)

Name of test	Critical Voltage test 2.4.2-1
Reference to MOST Specification	Rev. 2.4: 3.2.5.8 Low Voltage Rev. 2.5: 3.2.5.5 Supply Voltage
Value of Interest	Voltage Level of Power Supply
Start Conditions	DUT in NormalOperation Voltage level of power supply in "U_Normal range"
Test description	The voltage level is reduced to U_Critical, then the voltage level is raised to U_Normal. During the test, the DUT must not switch off the MOST signal or disturb communication. There must be a hysteresis between both voltage levels. Then the voltage level will be increased to U_Super. Until U_Super is reached, the DUT must not switch off or disturb communication.
Experimental set-up	#2 - Tester 1 in slave mode or master mode (depends on DUT). - Tester 2 in slave mode.
Device type	All devices [x] MOST25 [x] MOST50 oPHY [x] MOST50 ePHY
Multi Node Device	All devices
Note	1. The voltage levels (U_Critical, U_Normal, U_Super) are provided by the DUT manufacturer and can be read out via FBlock ET 2. Increasing and decreasing voltage must be done in steps of 0.1V/0.5s
Results	DUT ok: The DUT has passed the test. DUT not ok (1): DUT switches off MOST signal too early (before U_Critical). DUT not ok (2): DUT disturbs communication in case of U_Normal → U_Critical. DUT not ok (3): DUT disturbs communication in case of U_Critical → U_Normal. DUT not ok (4): No Hysteresis as specified by manufacturer (U_Normal <-> U_Critical). DUT not ok (5): DUT switches off too early (before U_Super) DUT not ok (6): DUT disturbs communication in case of U_Normal → U_Super.

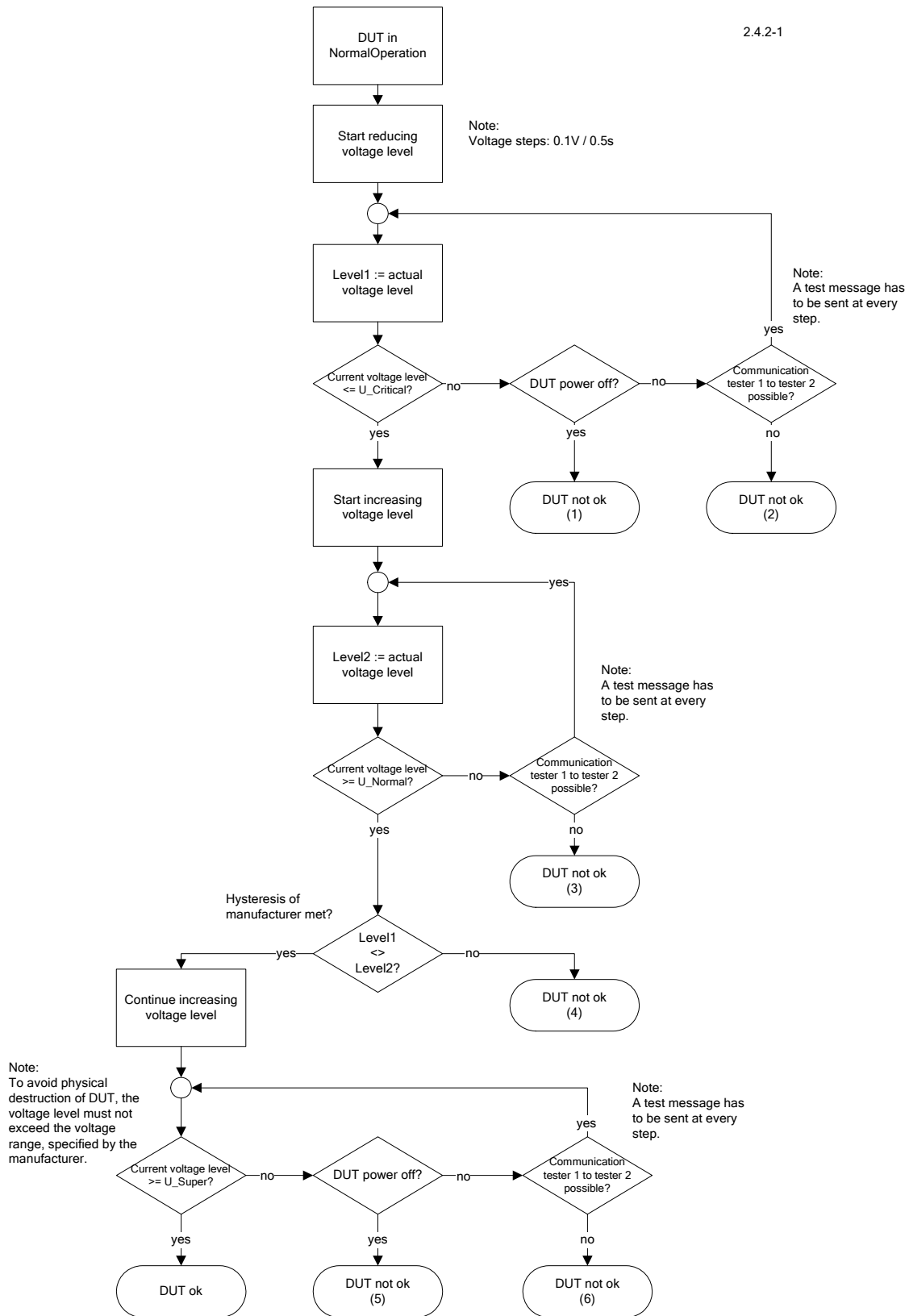


Figure 3-31 Flow of Critical Voltage test.

3.7.2.2 Low Voltage test (2.4.2-3)

Name of test	Low Voltage test 2.4.2-3
Reference to MOST Specification	Rev. 2.4: 3.2.5.8 Low Voltage Rev. 2.5: 3.2.5.5 Supply Voltage
Value of Interest	Voltage Level of Power Supply
Start Conditions	DUT in NormalOperation Voltage level of power supply in "U_Normal range"
Test description	During NO, the voltage level is reduced slowly (0.1V/0.5s). As long as the DUT continues sending MOST signal, the communication between tester 1 and tester 2 has to be possible. If the voltage level drops below U_Low, the DUT is allowed to switch off the MOST signal. The voltage level is set to U_Normal slowly (0.1V/0.5s) and a wake-up event is generated. The DUT has to start generating the MOST signal.
Experimental set-up	#2 - Tester 1 in slave mode or master mode (depends on DUT). - Tester 2 in slave mode.
Device type	All devices [x] MOST25 [x] MOST50 oPHY [x] MOST50 ePHY
Multi Node Device	All devices
Note	1. The voltage levels (U_Critical, U_Normal, U_Low) and the tolerances are provided by the DUT manufacturer and can be read out via FBlock ET 2. .Increasing and decreasing voltage must be done in steps of 0.1V/0.5s
Results	DUT ok: The DUT has passed the test. DUT not ok (1): DUT disturbs communication before U_Low is reached or before MOSTsignal is switched off. DUT not ok (2): DUT fails to meet requirement of voltage level U_Low (switches off too late / too early). DUT not ok (3): DUT fails to process a wake-up after entering U_Normal state from U_Critical state.

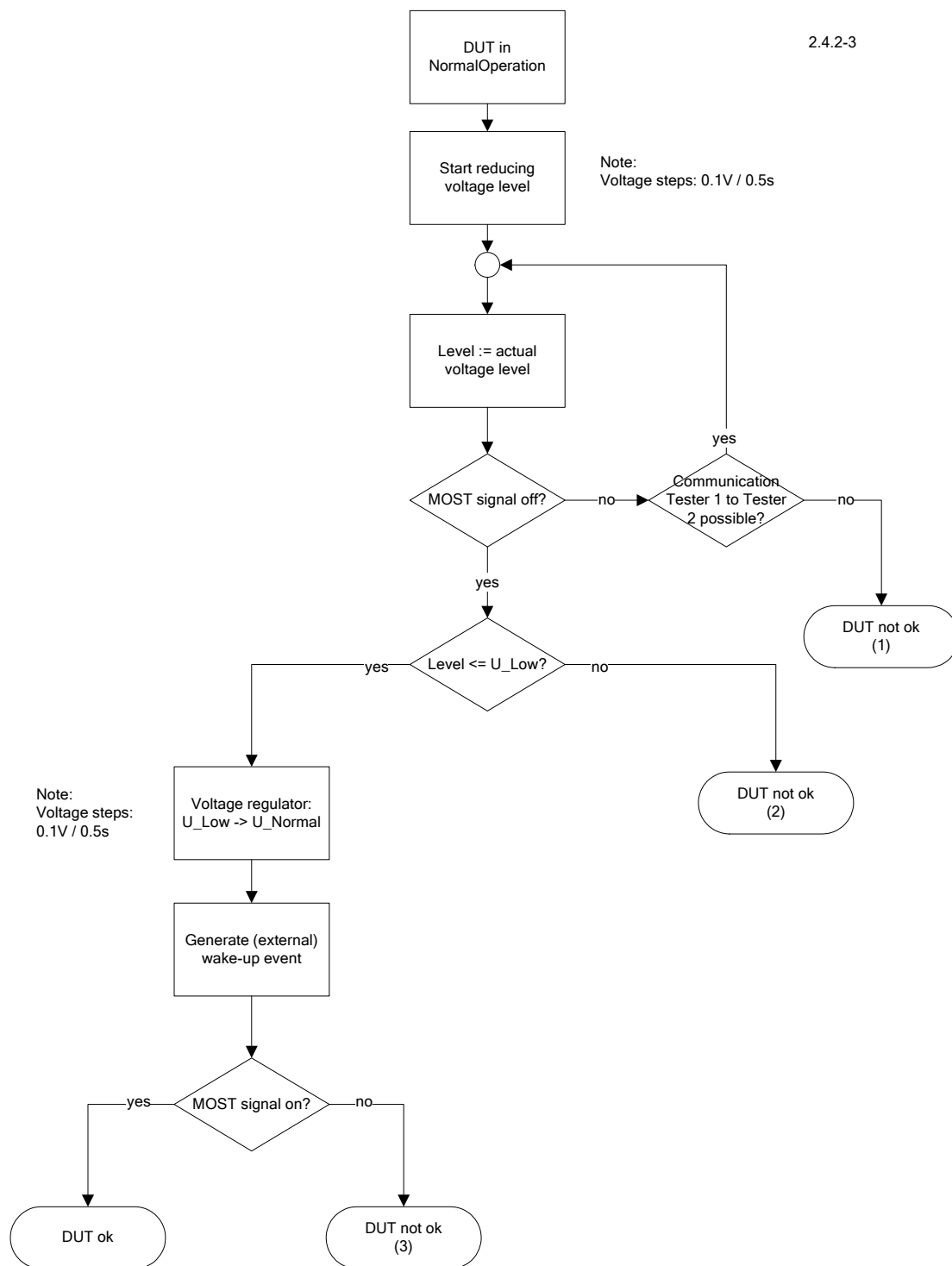


Figure 3-32 Flow of Low Voltage test.

3.8 Ring Break Diagnosis

3.8.1.1 RBD procedure test (2.5-2)

Name of test	RBD procedure test 2.5-2
Reference to MOST Specification	Rev. 2.4: 3.2.2.4 NetInterface Ring Break Diagnosis Rev. 2.5: 3.2.2.4 NetInterface Ring Break Diagnosis
Value of Interest	t_Diag_Slave t_Diag_Master t_PwrSwitchOffDelay RBD result
Start Conditions	DUT in NormalOperation Ring closed
Test description	The DUT is triggered to start the RBD. It has to perform and finish the RBD within (t_Diag_Slave_max or t_Diag_Master_max) + t_PwrSwitchOffDelay_max; depends on manufacturer. At the end of the RBD, it has to switch off MOST signal. The ring is closed and the tester performs a wake-up to read out the result of the RBD from the DUT. The result has to show an interruption of the ring right before the DUT. The tester triggers the RBD again and powers down the ring. The DUT has to perform and finish the RBD within t_Diag_Slave_max / t_Diag_Master_max. At the end of the RBD, the DUT has to perform NormalOperation (detectable by polling the FBlocks). It must not stop generating MOST signal. The RBD result has to show a closed ring.
Experimental set-up	#1 - Tester 1 in slave mode or master mode (depends on DUT).
Device type	All devices [x] MOST25 [x] MOST50 oPHY [] MOST50 ePHY
Multi Node Device	All devices
Note	The tester has to determinate the timings (t_Diag_Master / t_Diag_Slave) via FBlock ET of the DUT. The reported node position of the RBD result has to be ignored.
Results	DUT ok: The DUT has passed the test. DUT not ok (1): DUT fails to finish RBD within t_Diag_Master / t_Diag_Slave (ring interrupted). DUT not ok (2): Wrong RBD result (in case of interrupted ring). DUT not ok (3): DUT fails to finish RBD within t_Diag_Master / t_Diag_Slave (ring closed). DUT not ok (4): DUT stops generating MOST signal. DUT not ok (5): Wrong RBD result (in case of closed ring).



3.8.1.2 TM RBD procedure test (2.5-3)

Name of test	TM RBD procedure test 2.5-3
Reference to MOST Specification	Rev. 2.4: 3.2.2.4 NetInterface Ring Break Diagnosis Rev. 2.5: 3.2.2.4 NetInterface Ring Break Diagnosis
Value of Interest	t_Diag_Slave t_Diag_Master t_PwrSwitchOffDelay RBD result
Start Conditions	DUT in NormalOperation Ring closed
Test description	<p>The DUT is triggered to start the RBD and the ring will be interrupted between Tester 2 and Tester 1. The DUT has to perform and finish the RBD within (t_Diag_Slave_max or t_Diag_Master_max) + t_PwrSwitchOffDelay_max; depends on manufacturer. At the end of the RBD, it has to switch off MOST signal. The ring is closed and the tester 1 performs a wake-up to read out the result of the RBD from the DUT. The result has to show an interruption of the ring right before Tester 1.</p> <p>The tester triggers the RBD again and powers down the ring. The DUT has to perform and finish the RBD within t_Diag_Slave_max / t_Diag_Master_max. At the end of the RBD, the DUT has to perform NormalOperation (detectable by polling the FBlocks). It must not stop generating MOST signals.</p> <p>The RBD result has to show a closed ring.</p>
Experimental set-up	#2 - Tester 1 in slave mode or master mode (depends on DUT) - Tester 2 in slave mode
Device type	All devices [x] MOST25 [x] MOST50 oPHY [] MOST50 ePHY
Multi Node Device	All devices
Note	<p>The tester has to determinate the timings (t_Diag_Master / t_Diag_Slave) via FBlock ET of the DUT.</p> <p>Tester 1 has to behave like normal MOST device regarding to RBD procedure (t_Diag_Master of tester1 = t_Diag_Master_Min)</p> <p>The reported node position of the RBD result has to be ignored.</p>
Results	<p>DUT ok: The DUT has passed the test.</p> <p>DUT not ok (1): DUT fails to finish RBD within t_Diag_Master / t_Diag_Slave (ring interrupted).</p> <p>DUT not ok (2): Wrong RBD result (in case of interrupted ring).</p> <p>DUT not ok (3): DUT fails to finish RBD within t_Diag_Master / t_Diag_Slave (ring closed).</p> <p>DUT not ok (4): DUT stops generating MOST signal.</p> <p>DUT not ok (5): Wrong RBD result (in case of closed ring).</p>

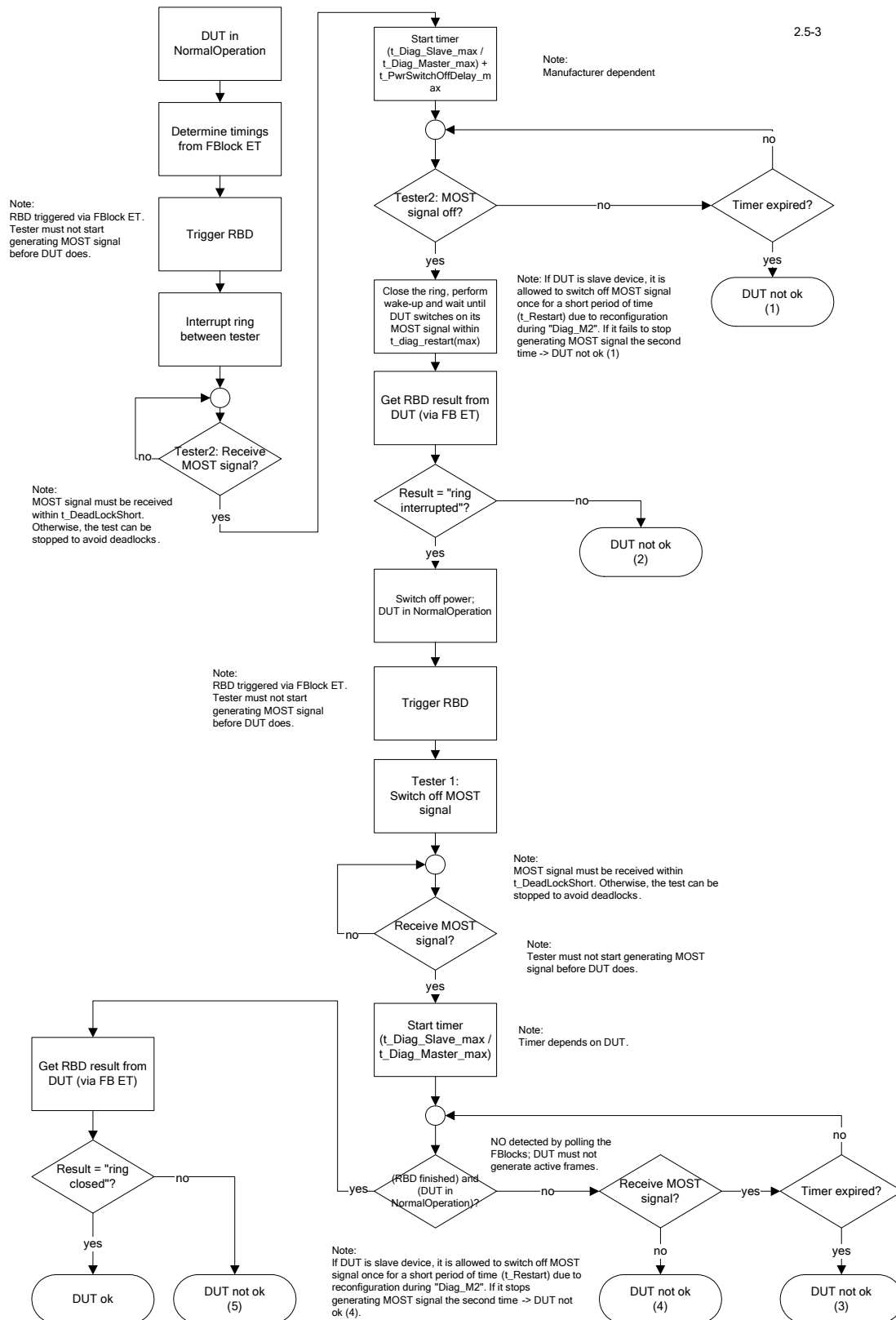


Figure 3-34 Flow of TM RBD procedure test.

3.8.1.3 Wakeup after RBD test (2.5-7)

Name of test	Wakeup after RBD test 2.5-7
Reference to MOST Specification	Rev. 2.4: 3.2.2.4 NetInterface Ring Break Diagnosis Rev. 2.5: 3.2.2.4 NetInterface Ring Break Diagnosis
Value of Interest	t_Diag_Restart
Start Conditions	DUT in NormalOperation Ring closed
Test description	The DUT is triggered to start the RBD. If the RBD is finished, the DUT has to switch off MOST signal. A new wake-up event is generated (MOST signal on). The DUT has to be able to detect this wake-up event. It has to wait for t_Diag_Restart_min before generating MOST signal at its output.
Experimental set-up	#1 - Tester 1 in slave mode or in master mode (depends on DUT).
Device type	All devices [x] MOST25 [x] MOST50 oPHY [] MOST50 ePHY
Multi Node Device	All devices
Note	Test not applicable if DUT does not support wake-up via MOST interface or electrical wake-up. The reported node position of the RBD result has to be ignored.
Results	DUT ok: The DUT has passed the test. DUT not ok: DUT fail to meet t_Diag_Restart after RBD

2.5-7

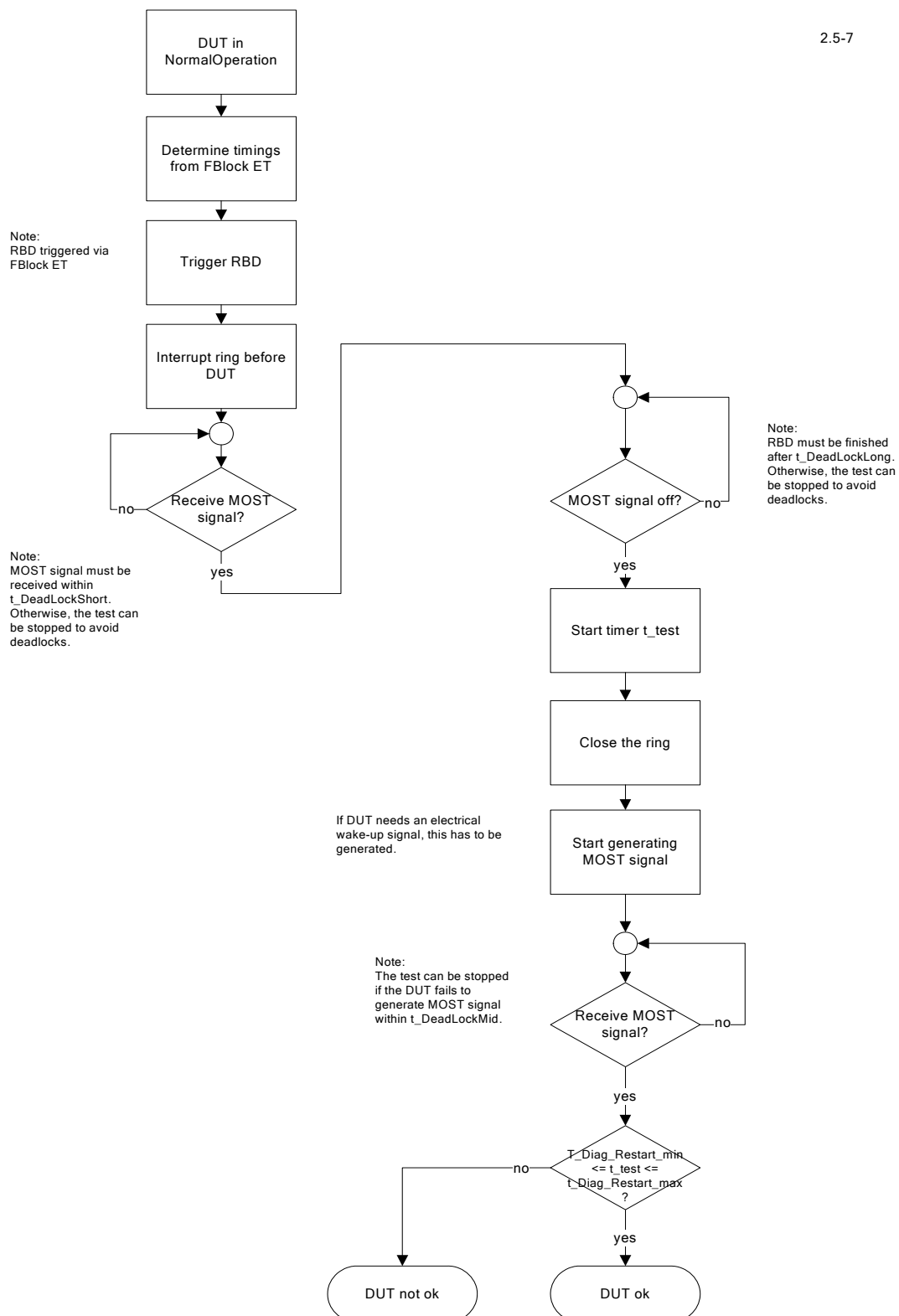


Figure 3-35 Flow of Wakeup after RBD test.

3.8.1.4 RBD missing TM test (2.5-8)

Name of test	RBD missing TM test 2.5-8
Reference to MOST Specification	Rev. 2.4: 3.2.2.4 NetInterface Ring Break Diagnosis Rev. 2.5: 3.2.2.4 NetInterface Ring Break Diagnosis
Value of Interest	RBD result t_Diag_Slave
Start Conditions	DUT in NormalOperation Ring closed
Test description	The DUT is triggered to start the RBD. If the RBD is finished, the DUT has to switch off MOST signal. The DUT is woken-up again and the result of the RBD is checked. It has to show "No TM" or "No Lock" (depends on DUT).
Experimental set-up	#1 - Tester 1 in master mode
Device type	All devices except TM. [x] MOST25 [x] MOST50 oPHY [] MOST50 ePHY
Multi Node Device	All devices without TM.
Note	Tester 1 has to behave like normal MOST device regarding to RBD procedure for TimingSlave (t_Diag_Slave of tester1 = t_Diag_Slave_Max). The reported node position of the RBD result has to be ignored.
Results	DUT ok: The DUT has passed the test. DUT not ok (1): DUT fails to finish RBD right in time. DUT not ok (2): Wrong RBD-result.

2.5-8

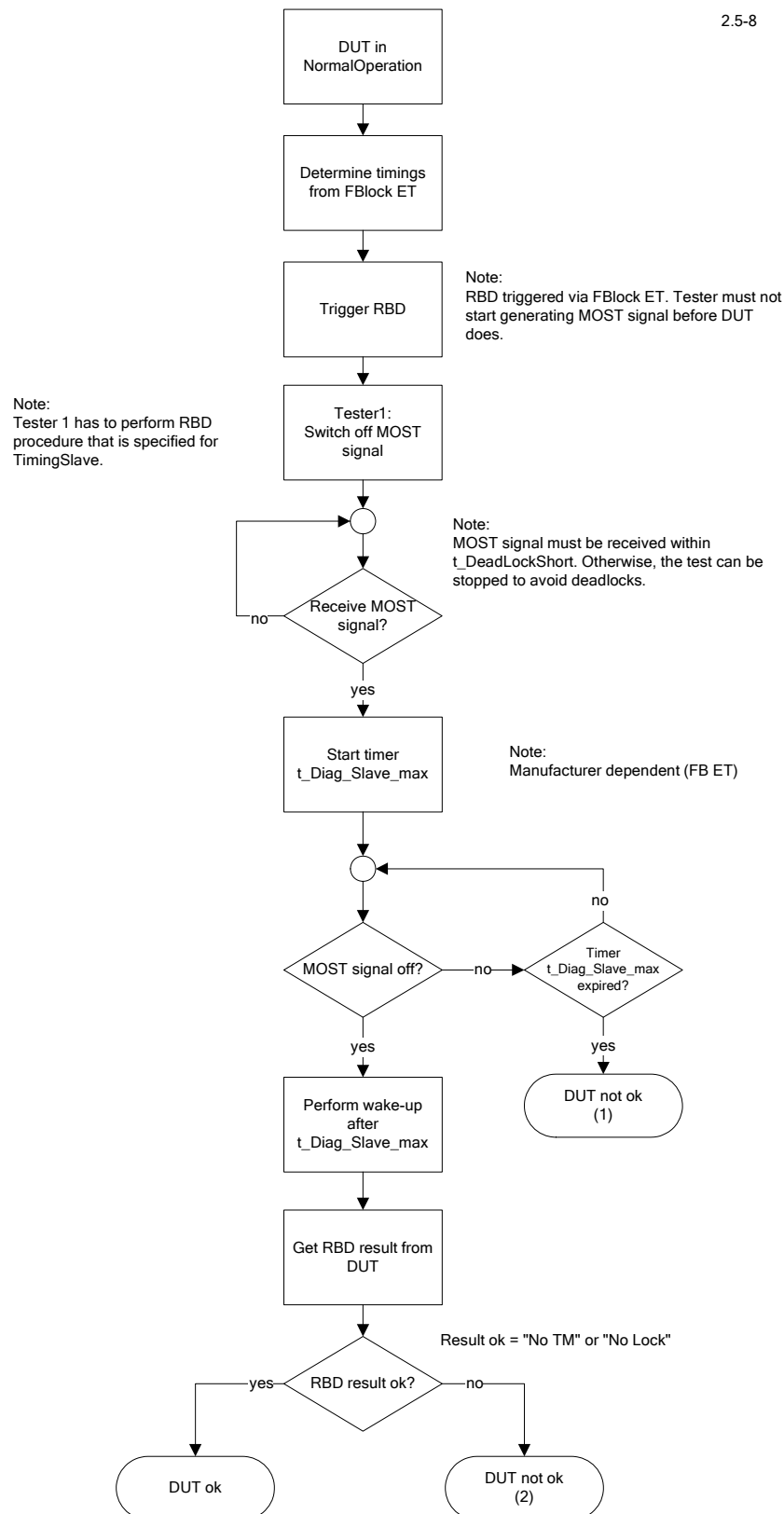


Figure 3-36 Flow of RBD missing TM / MultiMaster test.

3.9 System Configuration

3.9.1 Configuration / System Configuration (NWM)

3.9.1.1 NWM address initialization test (2.6.1-1)

Name of test	NWM address initialization test 2.6.1-1
Reference to MOST Specification	
Value of Interest	Address of DUT
Start Conditions	DUT in NormalOperation Ring closed
Test description	During NO, the tester stores the address of the DUT. After the next wake-up, the DUT has to have still the same address. The DUT is disconnected from power until the buffer capacitors are empty (at least 1 minute). After the next wake-up, the DUT must not use address " uninitialized_node_address ". The address, used by the DUT has to be valid.
Experimental set-up	#1 - Tester 1 in slave mode or master mode (depends on DUT).
Device type	NWM [x] MOST25 [x] MOST50 oPHY [x] MOST50 ePHY
Multi Node Device	All devices containing NWM
Note	
Results	DUT ok: The DUT has passed the test. DUT not ok (1): The DUT fails to store its address during power-off mode. DUT not ok (2): The DUT uses address " uninitialized_node_address " at first power-on. DUT not ok (3): The DUT uses an invalid address at first power-on (address not within valid range).

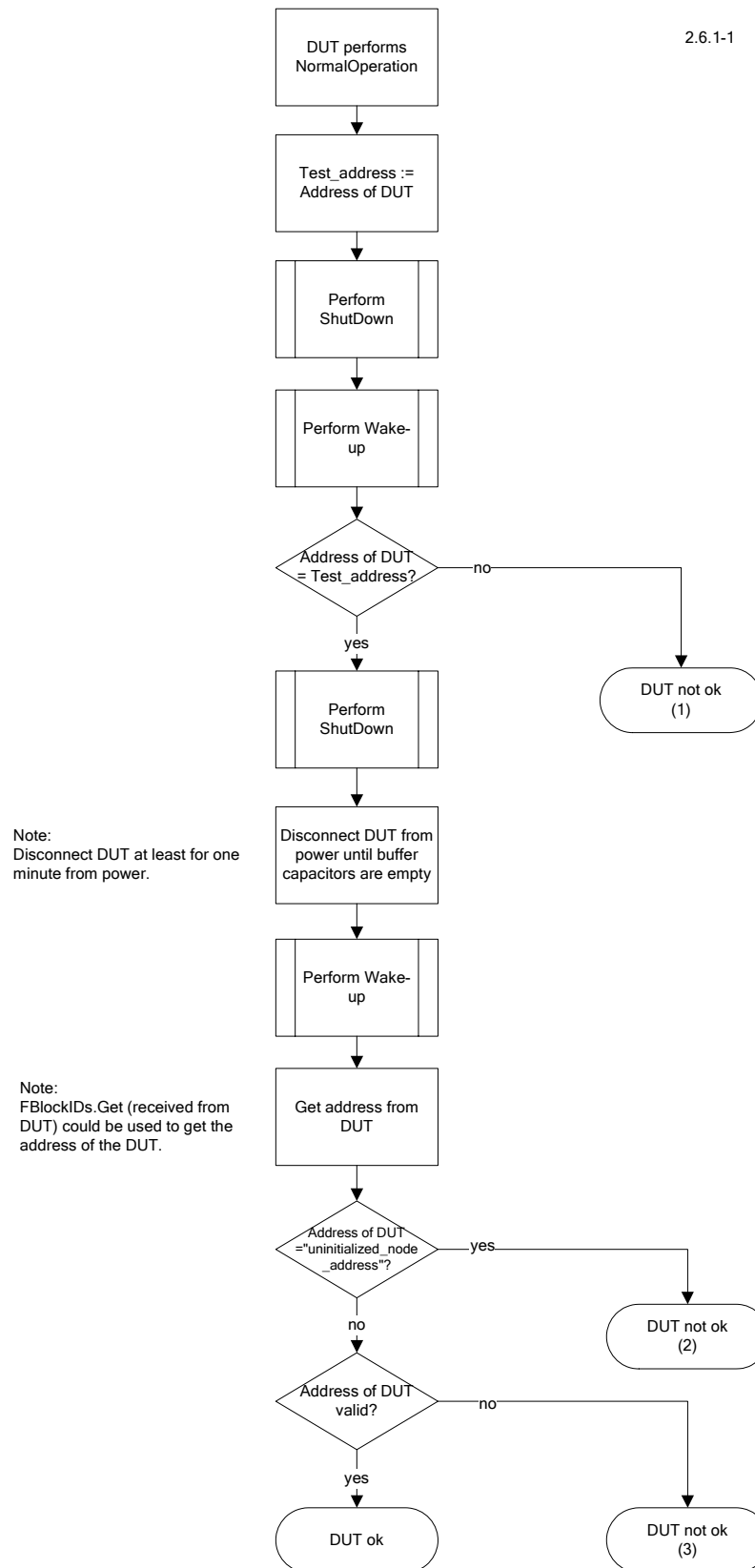


Figure 3-37 Flow of NWM address initialization test.

3.9.1.2 FBlock polling test (2.6.2-1)

Name of test	FBlock polling test 2.6.2-1
Reference to MOST Specification	Rev. 2.4: 3.3.3.4.1 Configuration Request Description Rev. 2.5: 3.3.3.4.1 Configuration Request Description
Value of Interest	t_WaitForAnswer FBlockIDs.Get Configuration.Status(ok)
Start Conditions	DUT: NetInterfacePowerOff Ring closed
Test description	After wake-up, the DUT has to check all function blocks of both testers in the sequence of their ring position. Tester 2 does not respond to the FBlock check. As soon as the DUT asks tester 2, t_WaitForAnswer_min will be started. Tester 1 has to respond to the FBlock request by the DUT normally. As the CR was build-up successfully, the DUT has to broadcast Configuration.Status(Ok) not before t_WaitForAnswer_min expires.
Experimental set-up	#2 - Tester 1 in slave mode or in master mode (depends on DUT) - Tester 2 in slave mode
Device type	NWM [x] MOST25 [x] MOST50 oPHY [x] MOST50 ePHY
Multi Node Device	All devices containing NWM
Note	If DUT is NWM but not TM, it has to check tester 1 before tester 2 (devices has to be checked in ring position order).
Results	DUT ok: The DUT has passed the test. DUT not ok (1): The DUT fails to check the FBlocks of Tester 2. DUT not ok (2): The DUT fails to check the FBlocks of Tester 1. DUT not ok (3): The DUT fails to wait long enough for response of tester 1 t_WaitForAnswer_min). DUT not ok (4): The DUT fails to send Configuration.Status(Ok).

2.6.2-1

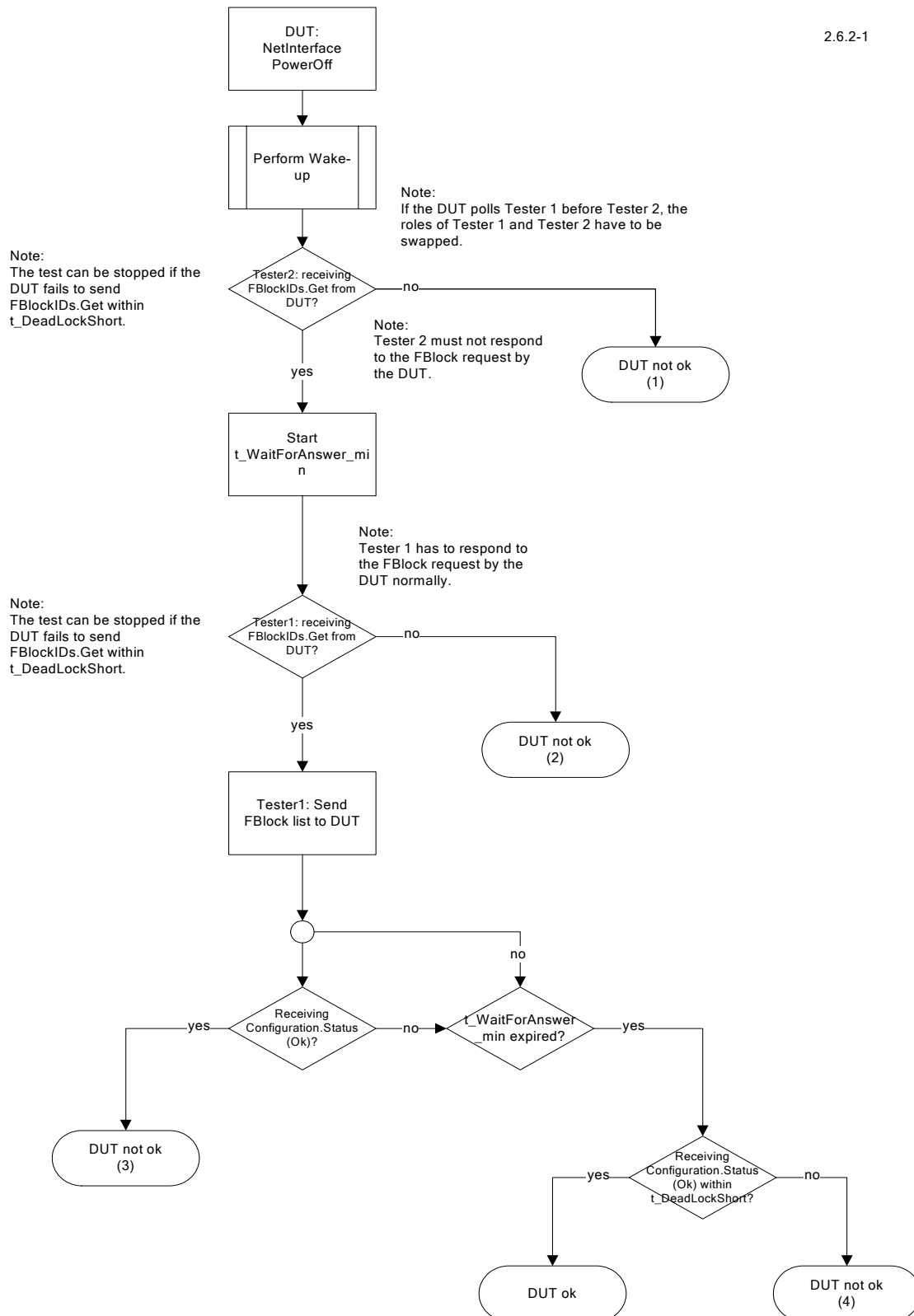


Figure 3-38 Flow of FBlock polling test.

3.9.1.3 Device Ignore test (a) (2.6.2-3a)

Name of test	Device Ignore test (a) 2.6.2-3a
Reference to MOST Specification	Rev. 2.4: 3.3.3.4.5 Network Slave Continuous causes for System State NotOk / 3.3.3.5.1 Un-initialized Logical Node Address Rev. 2.5: 3.3.3.4.5 Network Slave Continuous causes for System State NotOk 3.3.3.5.1 Un-initialized Logical Node Address
Value of Interest	CR Configuration.Status(NotOk) Configuration.Status(Ok)
Start Conditions	DUT: NetInterfacePowerOff Ring closed
Test description	After wake-up, the DUT has to detect that Tester 1 has address "uninitialized_node_address" and has to broadcast Config(NotOK). The Tester does not change its address. The DUT has to check the tester two more times. Then it has to ignore it until the next NCE or system startup (the tester must not be stored into the CR) and has to send Configuration.State(Ok).
Experimental set-up	#1 - Tester 1 in slave mode or in master mode (depends on DUT); Address="uninitialized_node_address"
Device type	NWM [x] MOST25 [x] MOST50 oPHY [x] MOST50 ePHY
Multi Node Device	All devices containing NWM
Note	
Results	DUT ok: The DUT has passed the test. DUT not ok (1): DUT fails to check Tester 1. DUT not ok (2): DUT fails to broadcast Configuration.Status(NotOK). DUT not ok (3): DUT checks the Tester less than three times. DUT not ok (4): DUT fails to broadcast Configuration.Status(OK). DUT not ok (5): DUT stores Tester 1 into CR.

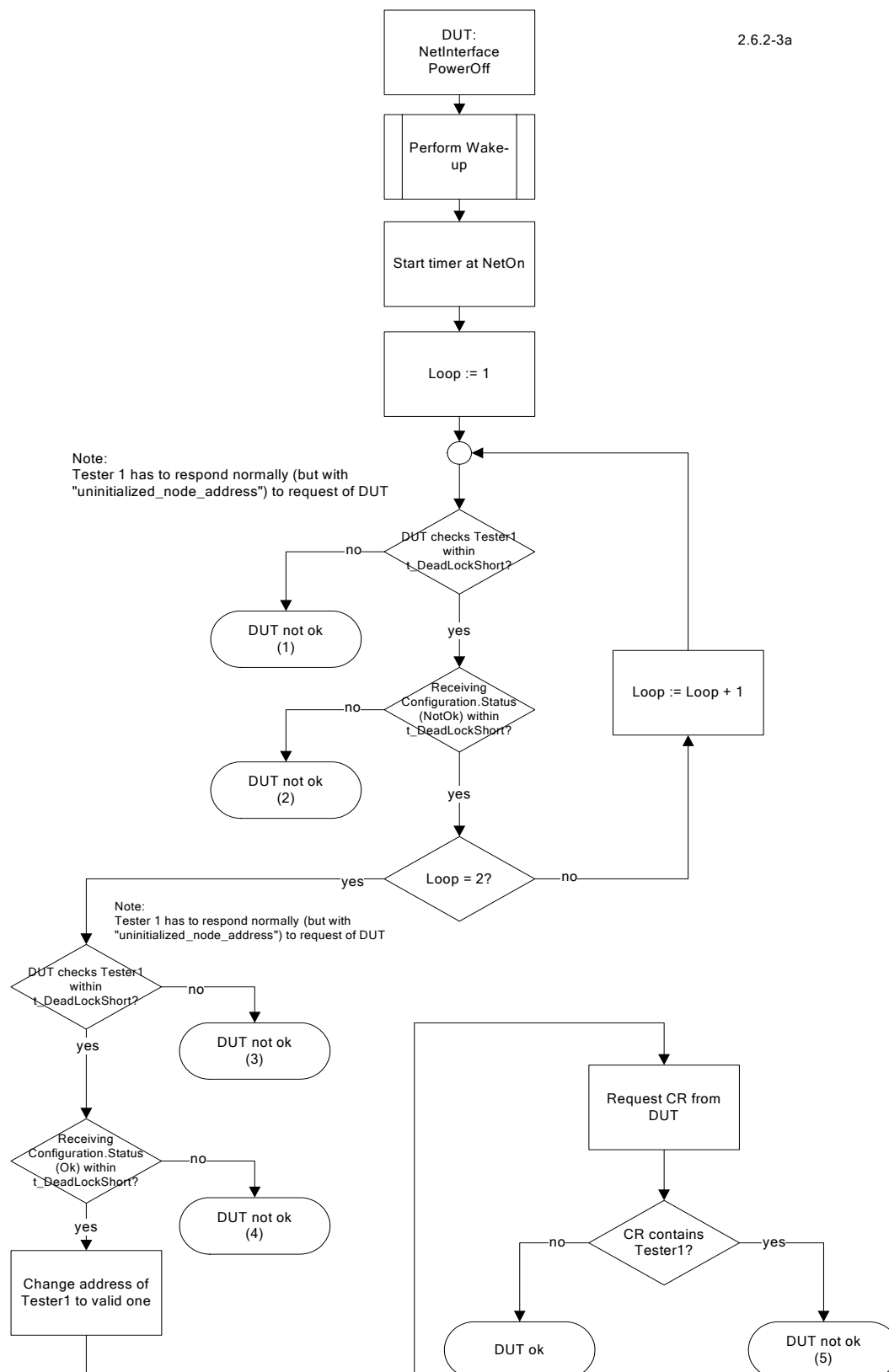


Figure 3-39 Flow of Device Ignore test (a).

3.9.1.4 Device Ignore test (b) (2.6.2-3b)

Name of test	Device Ignore test (b) 2.6.2-3b
Reference to MOST Specification	Rev. 2.4: 3.3.3.4.4 Retries of Non Responding Network Slaves Rev. 2.5: 3.3.3.4.4 Retries of Non Responding Network Slaves
Value of Interest	t_DelayCfgRequest1 t_DelayCfgRequest2 t_WaitForAnswer CR
Start Conditions	DUT: NetInterfacePowerOff Ring closed
Test description	After wake-up, the DUT has to detect that Tester 1 permanently responds with NAK or does not respond at all to its requests. The DUT has to check tester 1 at least 10 times with short interval (t_WaitForAnswer + t_DelayCfgRequest1). From the 23. check on, the DUT has to use long check interval (t_WaitForAnswer + t_DelayCfgRequest2). The DUT must not store Tester 1 into the CR. As soon as the Tester 1 responds to request from DUT, the DUT has to store Tester 1 into the CR.
Experimental set-up	#2 - Tester 1 in slave mode; bypass closed - Tester 2 in slave mode or in master mode (depends on DUT) behaves like normal - MOST device
Device type	NWM [x] MOST25 [x] MOST50 oPHY [x] MOST50 ePHY
Multi Node Device	All devices containing NWM
Note	The test has to be performed twice: a) Tester always responds with NAK during the loops (receive buffer locked). The first message received by the tester may be acknowledged (due to potential empty receive buffer of the tester). During the test, the LowLevel and MidLevel retries of the DUT are not considered. The manufacturer has to provide information about number and timing of LowLevel and MidLevel retries to allow tester to filter them out. b) Tester acknowledged reception but does not respond to request from DUT. Test does not focus on border between 20. check and 21. check. Test only checks whether DUT changes from short check interval to long check interval. Tester2 has to prevent potential shutdown during test. It has to send ShutDown.Result(..., Suspend) to the PM in case receiving ShutDown.Start(..., Query).
Results	DUT ok (a/b): The DUT has passed the test. DUT not ok (1a/1b): DUT fails to check Tester 1. DUT not ok (2a/2b): DUT checks Tester1 not within t_DelayCfgRequest1_min/max. DUT not ok (3a/3b): DUT checks Tester1 not within t_DelayCfgRequest2_min/max. DUT not ok (4a/4b): DUT erroneously stores Tester 1 into the CR. DUT not ok (5a/5b): DUT fails to detect delayed responding Tester 1. DUT not ok (6a/6b): DUT fails to store Tester 1 into the CR.

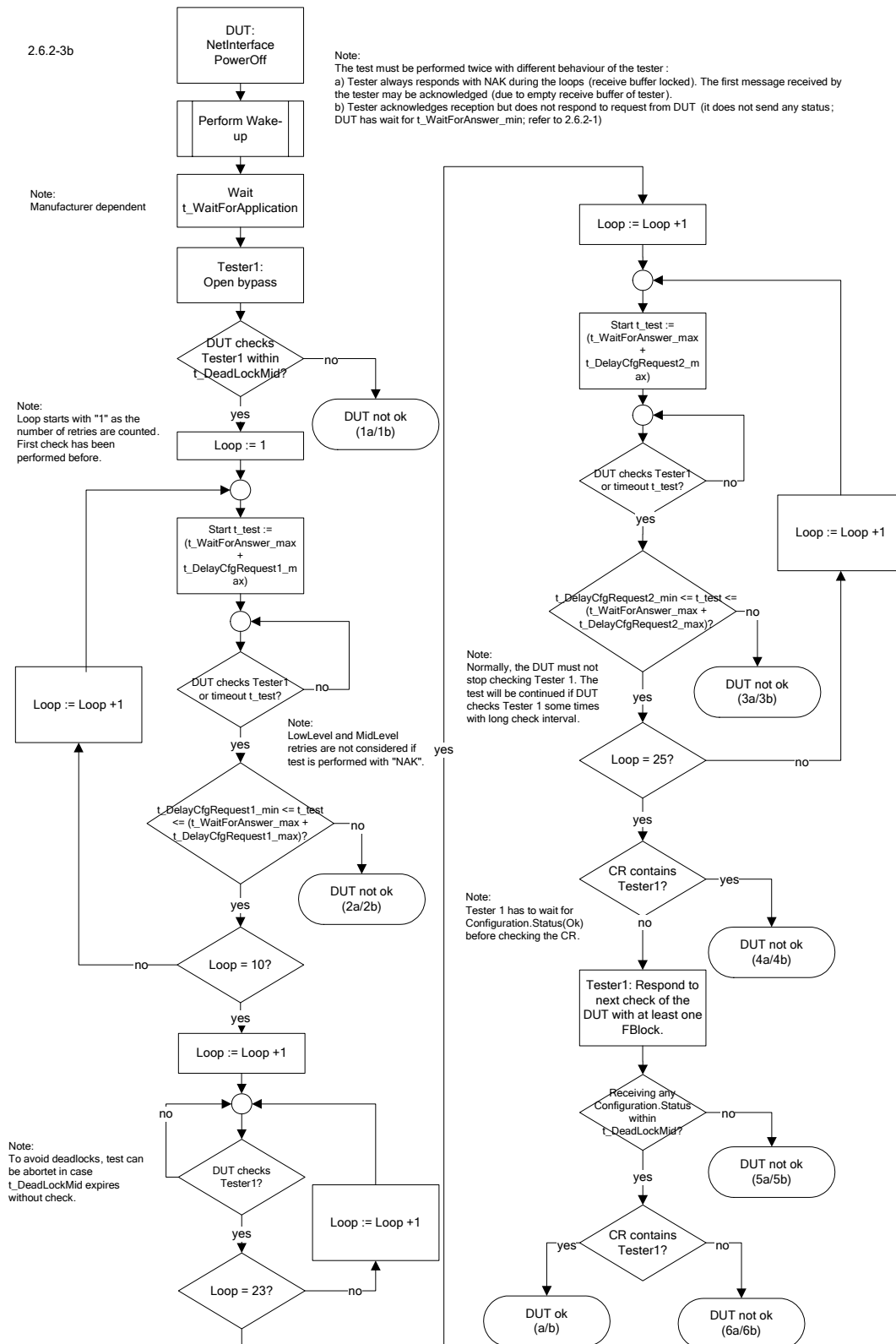


Figure 3-40 Flow of Device Ignore test (b).

3.9.1.5 Device Integration test (a) (2.6.2-4a)

Name of test	Device Integration test (a) 2.6.2-4a
Reference to MOST Specification	Rev. 2.4: 3.3.3.4 Scanning the System (System Scan) Rev. 2.5: 3.3.3.4 Scanning the System (System Scan)
Value of Interest	CR FBlockIDs.Get Configuration.Status(ok)
Start Conditions	DUT: NetInterfacePowerOff Ring closed
Test description	After wake-up, the DUT has to integrate the tester 1 into the CR and send Config(Ok).
Experimental set-up	#1 - Tester 1 in master mode or slave mode (depends on DUT); valid address. It has to respond to FBlockIDs.Get from DUT like a normal MOST device.
Device type	NWM [x] MOST25 [x] MOST50 oPHY [x] MOST50 ePHY
Multi Node Device	All devices containing NWM
Note	
Results	DUT ok: The DUT has passed the test. DUT not ok (1): DUT fails to send Config(Ok) right in time. DUT not ok (2): DUT fails to store Tester 1 into the CR.

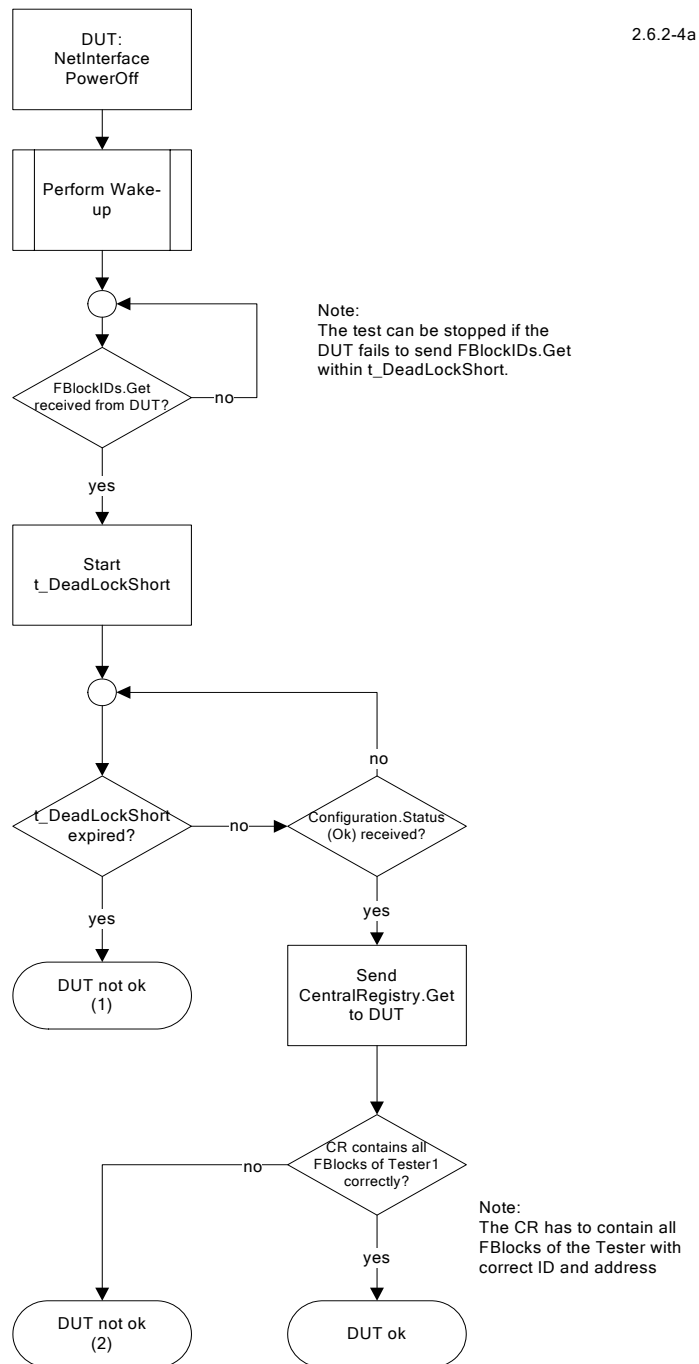


Figure 3-41 Flow of Device Integration test (a).

3.9.1.6 Device Integration test (b) (2.6.2-4b)

Name of test	Device Integration test (b) 2.6.2-4b
Reference to MOST Specification	Rev. 2.4: 3.3.3.2 Central Registry Rev. 2.5: 3.3.3.2 Central Registry
Value of Interest	CR Configuration.Status(ok/new)
Start Conditions	DUT: NetInterfacePowerOff Ring closed
Test description	The test is identical to test 2.6.2-4a. At this test, the Tester 1 and tester 2 provide more FBlocks than the DUT can store into its CR. The DUT has to store the max. possible number of FBlocks (FBlocks provided by DUT, tester 1 and tester 2) without any malfunction.
Experimental set-up	#2 - Tester 1 in master mode or slave mode (depends on DUT); valid address. It has to respond to FBlockIDs.Get from DUT like a normal MOST device. It has to fill up the CR of the DUT, considering the FBlocks of the DUT that are stored already in the CR. - Tester 2 has to have additional FBlocks. In addition to FBlocks of tester 1, they have to exceed the number, the DUT can store into its CR.
Device type	NWM [x] MOST25 [x] MOST50 oPHY [x] MOST50 ePHY
Multi Node Device	All devices containing NWM
Note	If DUT supports dynamic CR (detectable via FBlock ET), the testers must have 257 Fblocks. There have to be two FBlockID.Status Messages provided to the DUT. The content of the first (sent by tester 1) has to fit into the CR. The second (sent by tester 2) has to add some Fblocks which exceed the size of the CR."
Results	DUT ok: The DUT has passed the test. DUT not ok (1): DUT fails to send Config(Ok) right in time. DUT not ok (2): DUT fails to send Config(Ok/New) if receiving new FBlocks from tester 1. DUT not ok (3): DUT fails to send Config(Ok/New) if receiving and storing new FBlocks from tester 2. DUT not ok (4): DUT fails to store the max. number of FBlocks into its CR.

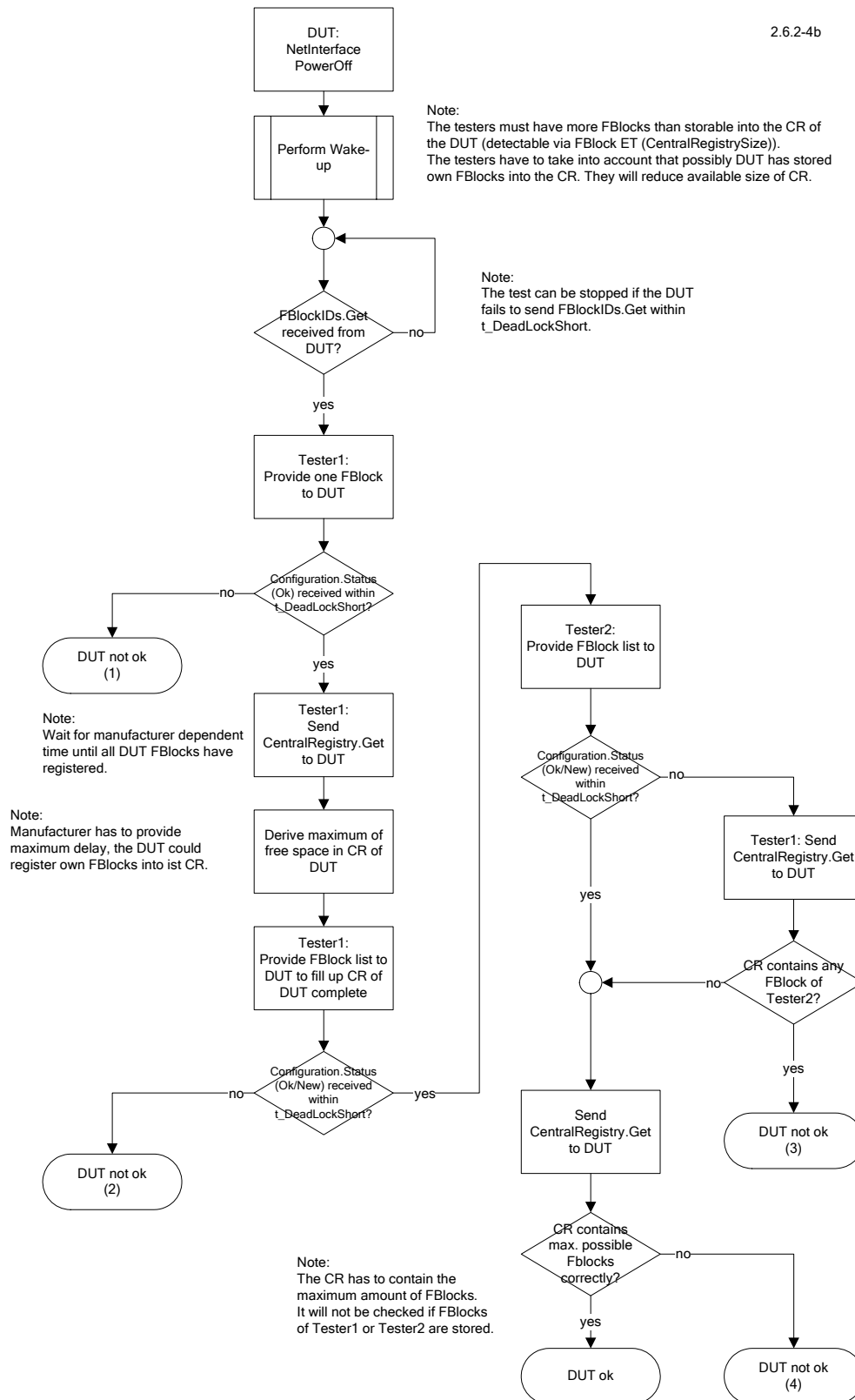


Figure 3-42 Flow of Device Integration test (b).

3.9.1.7 Config(Ok) Delay test (2.6.2-5)

Name of test	Config(Ok) Delay test 2.6.2-5
Reference to MOST Specification	Rev. 2.4: 3.3.3.1.2 Setting the System State to NotOk 3.3.3.4.3 Non Responding Network Slaves Rev. 2.5: 3.3.3.1.2 Setting the System State to NotOk 3.3.3.4.3 Non Responding Network Slaves
Value of Interest	t_WaitBeforeScan t_WaitForAnswer Configuration.Status
Start Conditions	DUT: NormalOperation
Test description	Tester 2 triggers Config(NotOk) by sending FBlockIDs.Status to DUT, using sender address 0xFFFF. The DUT has to send Configuration.Status(NotOk). Then it has to wait for t_WaitBeforeScan_min before performing a system scan. During this system scan, tester 2 must not respond to DUT. The DUT must not send Configuration.Status(Ok) before t_WaitForAnswer_min expires.
Experimental set-up	# 2 - Tester 1 in slave mode - Tester 2 in slave mode
Device type	NWM [x] MOST25 [x] MOST50 oPHY [x] MOST50 ePHY
Multi Node Device	All devices containing NWM
Note	Tester 1 has to behave like normal MOST device
Results	DUT ok: The DUT has passed the test. DUT not ok (1): The DUT fails to send Configuration.Status(NotOk). DUT not ok (2): The DUT performs system scan too early. DUT not ok (3): DUT sends Configuration.Status(Ok) too early.

2.6.2-5

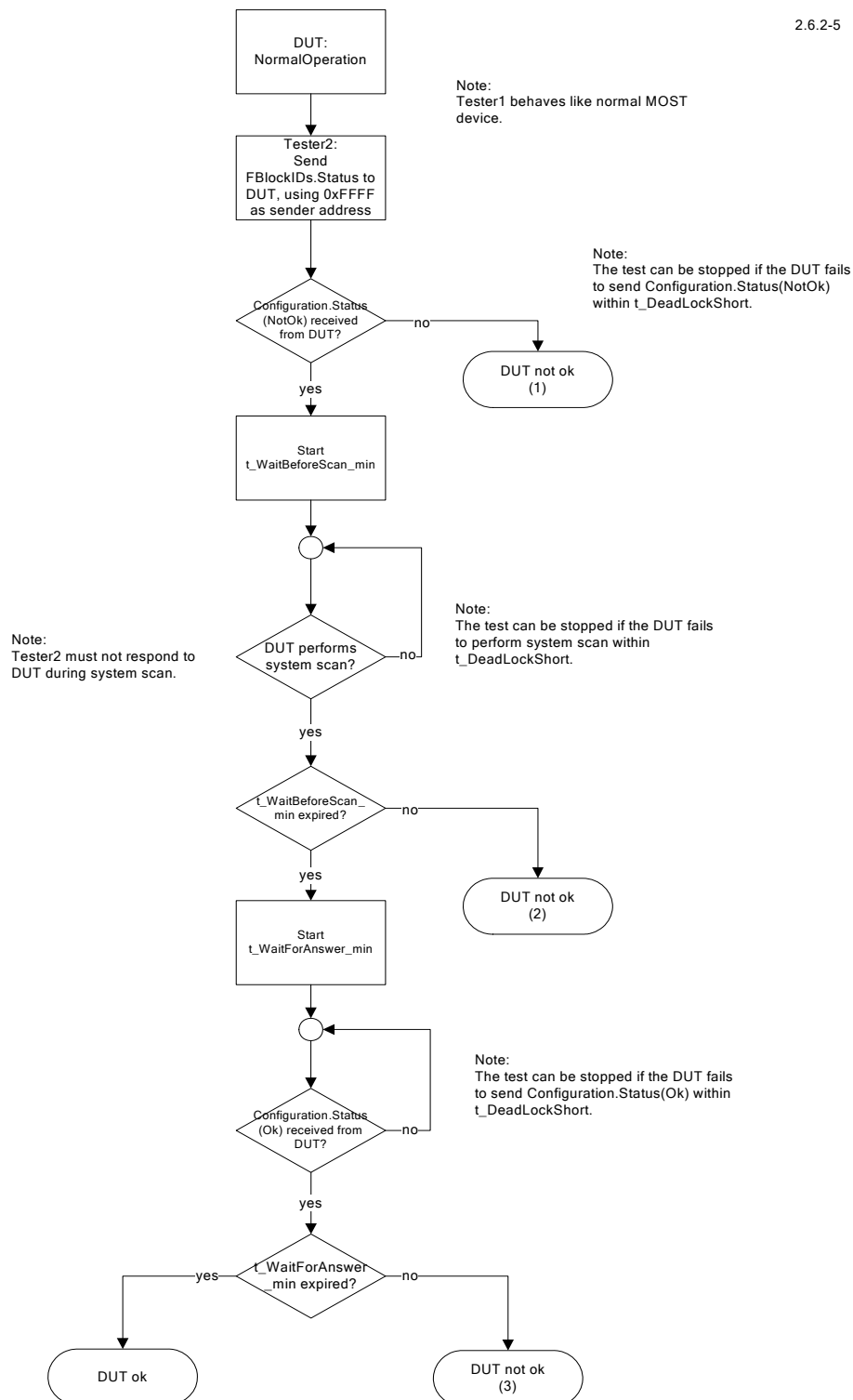


Figure 3-43 Flow of Config(Ok) Delay test.

3.9.1.8 Double FBlock test (2.6.2-6)

Name of test	Double FBlock test 2.6.2-6
Reference to MOST Specification	Rev. 2.4: 3.3.3.5.4 Duplicate InstID Registrations Rev. 2.5: 3.3.3.5.4 Duplicate InstID Registrations
Value of Interest	CR FBlockIDs.Get Configuration.Status(Ok)
Start Conditions	DUT: NetInterfacePowerOff Ring closed
Test description	After wake-up, the DUT has to perform system scan. It has to scan the devices in correct order. Tester 1 and Tester 2 send the same FBlock-ID, Inst-ID to the DUT. The DUT has to detect the identical FBlocks and has to try to change the InstID of the FBlock of Tester 1 or tester 2. The tester must not change its InstID. As soon as the DUT sends Config(ok), the CR will be checked. It must contain the "normal FBlocks" of both tester. It must not contain both "fix FBlocks".
Experimental set-up	#2 - Tester 1 in master mode or slave mode; depends on DUT - Tester 2 in slave mode
Device type	NWM [x] MOST25 [x] MOST50 oPHY [x] MOST50 ePHY
Multi Node Device	All devices containing NWM
Note	During system scan, the DUT could start checking tester 1 before tester 2 responds. Each tester contain 2 FBlocks. One with normal behaviour (named "normal FBlock") that will change its InstID if commanded by the DUT and one FBlock which will not change its InstID if commanded (named "fix FBlock"). For the "fix FBlocks", both tester have to use same FBlockID and InstID. For the "normal FBlock" they have to use different FBlockIDs and different InstIDs.
Results	DUT ok (1): The DUT has passed the test. DUT ok (2): "fix FBlock" neither of Tester1 nor of Tester2 stored in CR of DUT DUT not ok (1): DUT fails to check Tester 2. DUT not ok (2): DUT fails to check Tester 1. DUT not ok (3): DUT fails to change FBlock-ID of Tester 1 or Tester 2. DUT not ok (4): DUT fails to store "normal FBlock" of tester into the CR. DUT not ok (5): DUT stores "fix FBlocks" of both tester into CR.

2.6.2-6

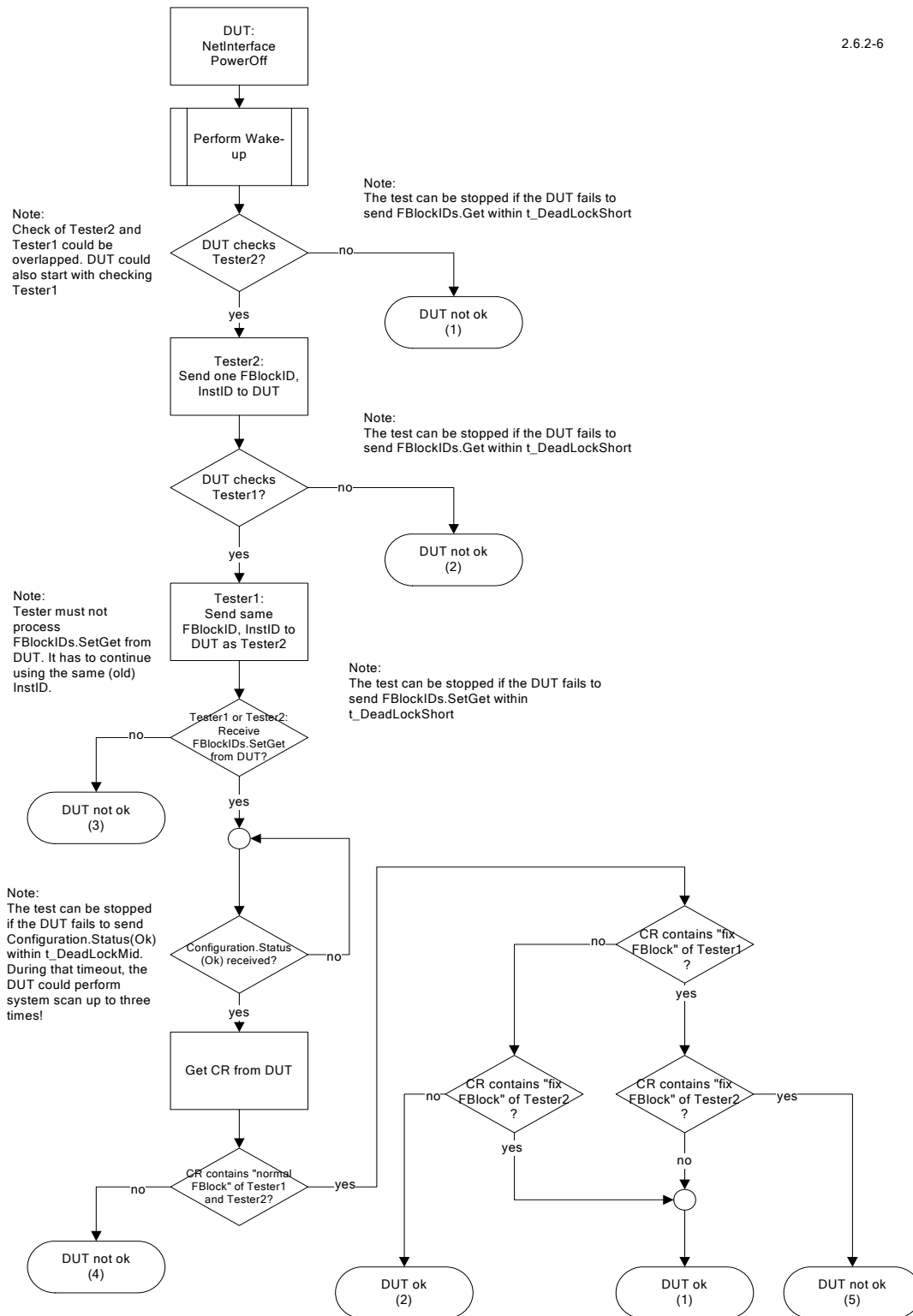


Figure 3-44 Flow of Double FBlock test.

3.9.1.9 Config(New) Order test (2.6.2-7)

Name of test	Config(New) Order test 2.6.2-7
Reference to MOST Specification	Rev. 2.4: 3.3.2 System States Rev. 2.5: 3.3.2 System States
Value of Interest	Configuration.Status
Start Conditions	DUT: NormalOperation
Test description	Tester 2 triggers Config(NotOk) by sending FBlockIDs.Status to DUT, using sender address 0xFFFF. The DUT has to send Configuration.Status(NotOk). During the system scan, tester 1 has to indicate an additional FBlock and one missing FBlock (compared with initial system scan). Tester 2 must not respond to the system scan. The DUT has to broadcast Configuration.Status(Ok). Before broadcasting Configuration.Status(Ok), the DUT must not broadcast any other Configuration.Status message.
Experimental set-up	# 2 - Tester 1 in slave mode - Tester 2 in slave mode
Device type	NWM [x] MOST25 [x] MOST50 oPHY [x] MOST50 ePHY
Multi Node Device	All devices containing NWM
Note	Tester 1 has to behave like normal MOST device
Results	DUT ok: The DUT has passed the test. DUT not ok (1): The DUT fails to send Configuration.Status(NotOk). DUT not ok (2): DUT sends Configuration.Status(New) or Configuration.Status(Invalid) before sending Configuration.Status(Ok).

2.6.2-7

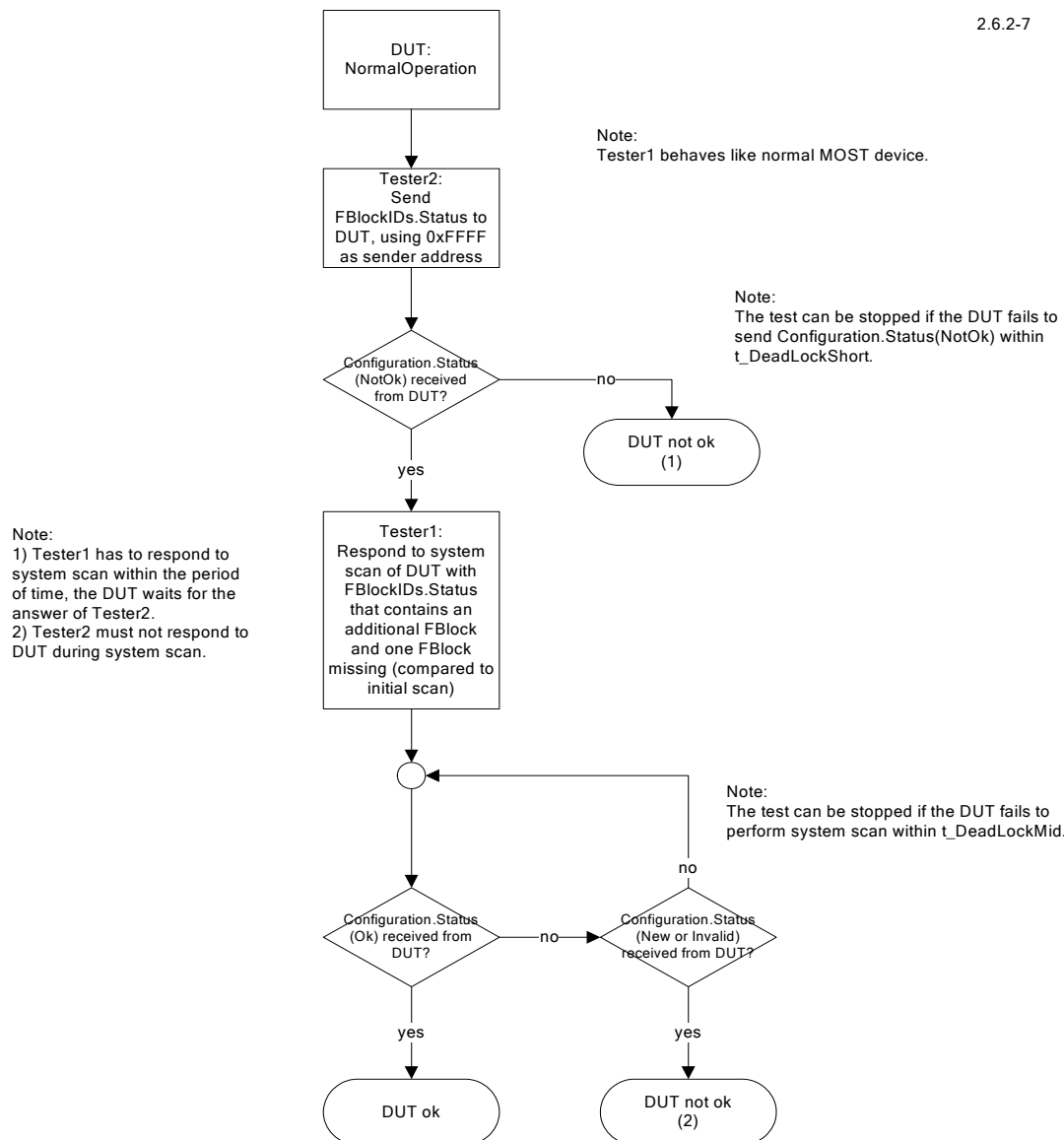


Figure 3-45 Flow of Config(New) Order test.

3.9.1.10 FBlock Status Change Detection test (2.6.3-1)

Name of test	FBlock Status Change Detection test 2.6.3-1
Reference to MOST Specification	Rev. 2.4: 3.3.3.6.1 Disappearing Function Blocks in System State Ok Rev. 2.5: 3.3.3.6.1 Disappearing Function Blocks in System State Ok
Value of Interest	CR Configuration.Status(Invalid)
Start Conditions	DUT in NormalOperation Ring closed
Test description	The Tester 1 changes the state of an preregistered FBlock from available to unavailable and indicates it to the DUT. The DUT has to broadcast "Configuration.Status(Invalid)" and delete it from the CR.
Experimental set-up	#1 - Tester 1 in slave mode or in master mode (depends on DUT); valid address
Device type	NWM [x] MOST25 [x] MOST50 oPHY [x] MOST50 ePHY
Multi Node Device	All devices containing NWM
Note	
Results	DUT ok: The DUT has passed the test. DUT not ok (1): DUT fails to broadcast "Configuration.Status(Invalid)". DUT not ok (2): DUT fails to delete unavailable FBlock from CR.

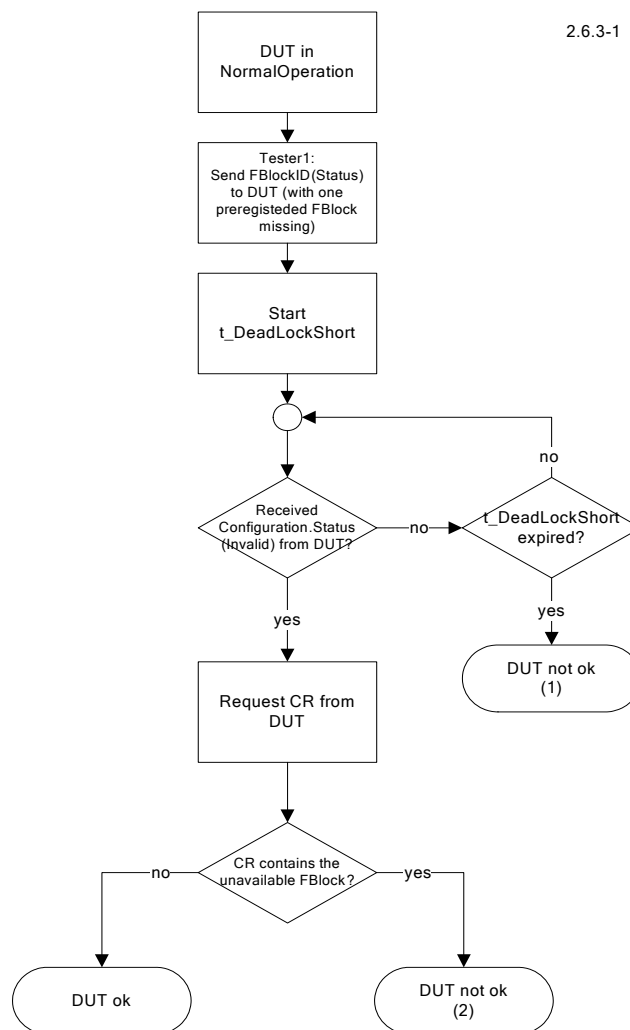


Figure 3-46 Flow of FBlock Status Change Detection test.

3.9.2 Configuration / System Configuration (Slave)

3.9.2.1 Address Initialization test (2.6.4-1)

Name of test	Address Initialization test 2.6.4-1
Reference to MOST Specification	Rev. 2.4: 3.3.4.2 Specific Startup Behavior Rev. 2.5: 3.3.4.2 Specific Startup Behavior
Value of Interest	Address of DUT
Start Conditions	DUT in NormalOperation Ring closed
Test description	The address of the DUT will be set to a value, different from default address of DUT. The address of the DUT is stored. After ShutDown, disconnecting from power (for at least 1 minute) and Wake-up, the DUT has to use valid node address (in static or dynamic address range) or "uninitialized_node_address". Any other address is not allowed.
Experimental set-up	#1 - Tester 1 in master mode or slave mode (depends on DUT)
Device type	All devices except NWM [x] MOST25 [x] MOST50 oPHY [x] MOST50 ePHY
Multi Node Device	All devices without NWM
Note	
Results	DUT ok (1): The DUT has passed the test (uses valid node address). DUT ok (2): The DUT has passed the test (uses "uninitialized_node_address" before deriving new address from ring position). DUT not ok (1): DUT changes address between two operation cycles. DUT not ok (2): DUT neither uses "uninitialized_node_address" nor valid node address.

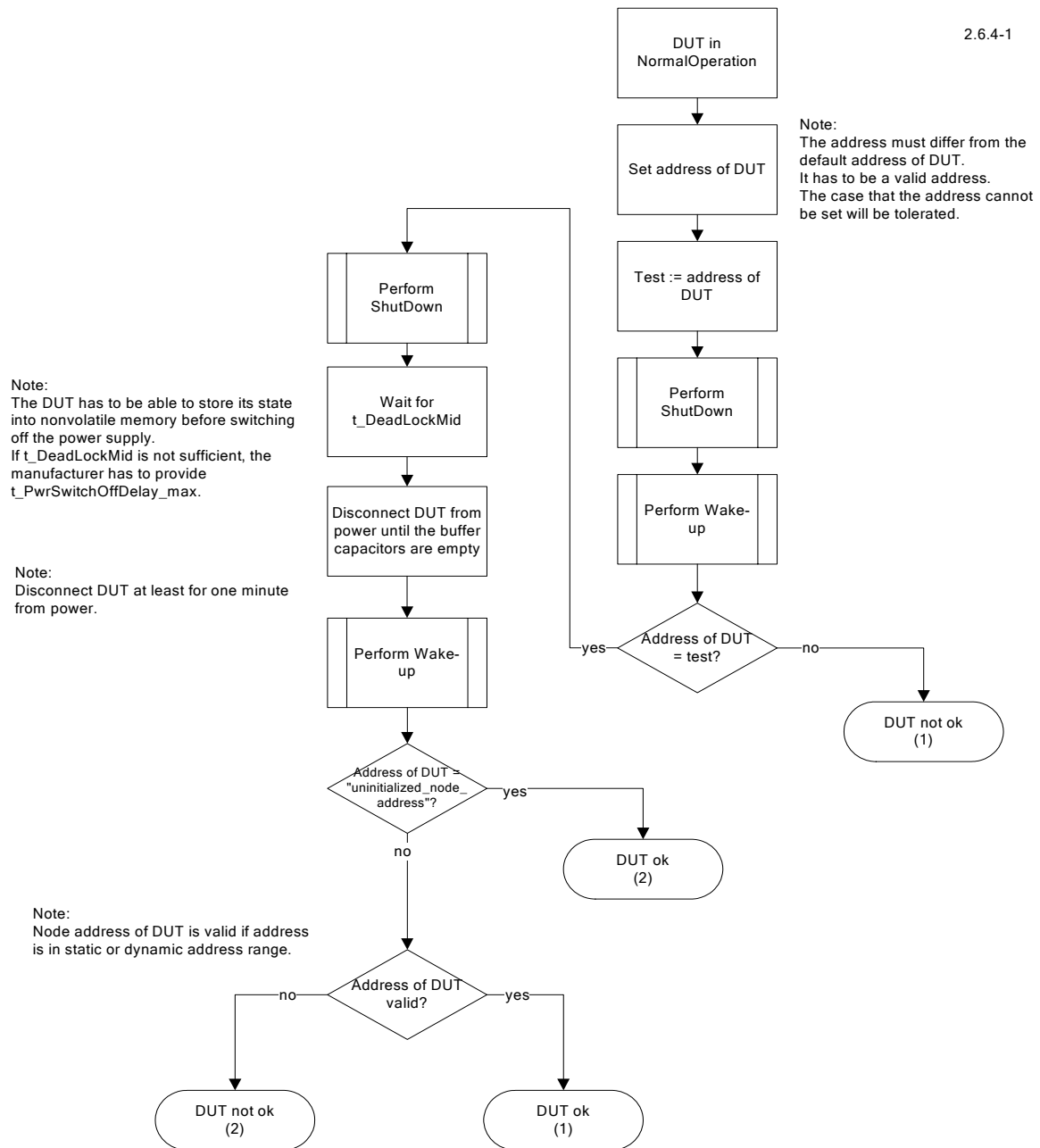


Figure 3-47 Flow of Address Initialization test.

3.9.2.2 NWM Address Storage test (2.6.4-3)

Name of test	NWM Address Storage test 2.6.4-3
Reference to MOST Specification	Rev. 2.4: 3.3.4.2.3 Deriving the Logical Node Address of the Network Master Rev. 2.5: 3.3.4.2.3 Deriving the Logical Node Address of the Network Master
Value of Interest	NWM address, used by DUT CentralRegistry.Get
Start Conditions	DUT: NetInterfacePowerOff Ring closed
Test description	After the DUT has been woken up, the tester 1 broadcasts some Configuration.Status message. The DUT has to derive the address of the tester 1. The DUT is triggered to send any message to the NWM (tester 1) to check whether the DUT has derived the address correctly. After a shutdown, the address of tester 1 is changed and the same procedure is performed, again. The DUT has to respond to the new address of tester 1.
Experimental set-up	#1 - Tester 1 in master mode or slave mode (depends on DUT)
Device type	All devices that use the NWM during NormalOperation (e.g. each device which uses the CR) except the NWM. [x] MOST25 [x] MOST50 oPHY [x] MOST50 ePHY
Multi Node Device	All devices without NWM (that are using the NWM/CR during NormalOperation)
Note	The test has to be performed three times, using Configuration.Status(Ok), Configuration.Status(New) and Configuration.Status(Invalid). In case of "New", an empty FBlock list has to be sent to ensure the DUT requests the CR from NWM. In case of "Invalid", the FBlock.InstID 0xC8.0x01 has to be used.
Results	DUT ok (1): The DUT has passed the test (CR not requested). DUT ok (2): The DUT has passed the test (CR not requested). DUT ok (3): The DUT has passed the test. DUT not ok: DUT stored address of NWM during Power-Off mode.

Note:
Between test loops, switch off MOST signal
without sending ShutDown.Start(Execute), i.e.
irregular shutdown.

2.6.4-3

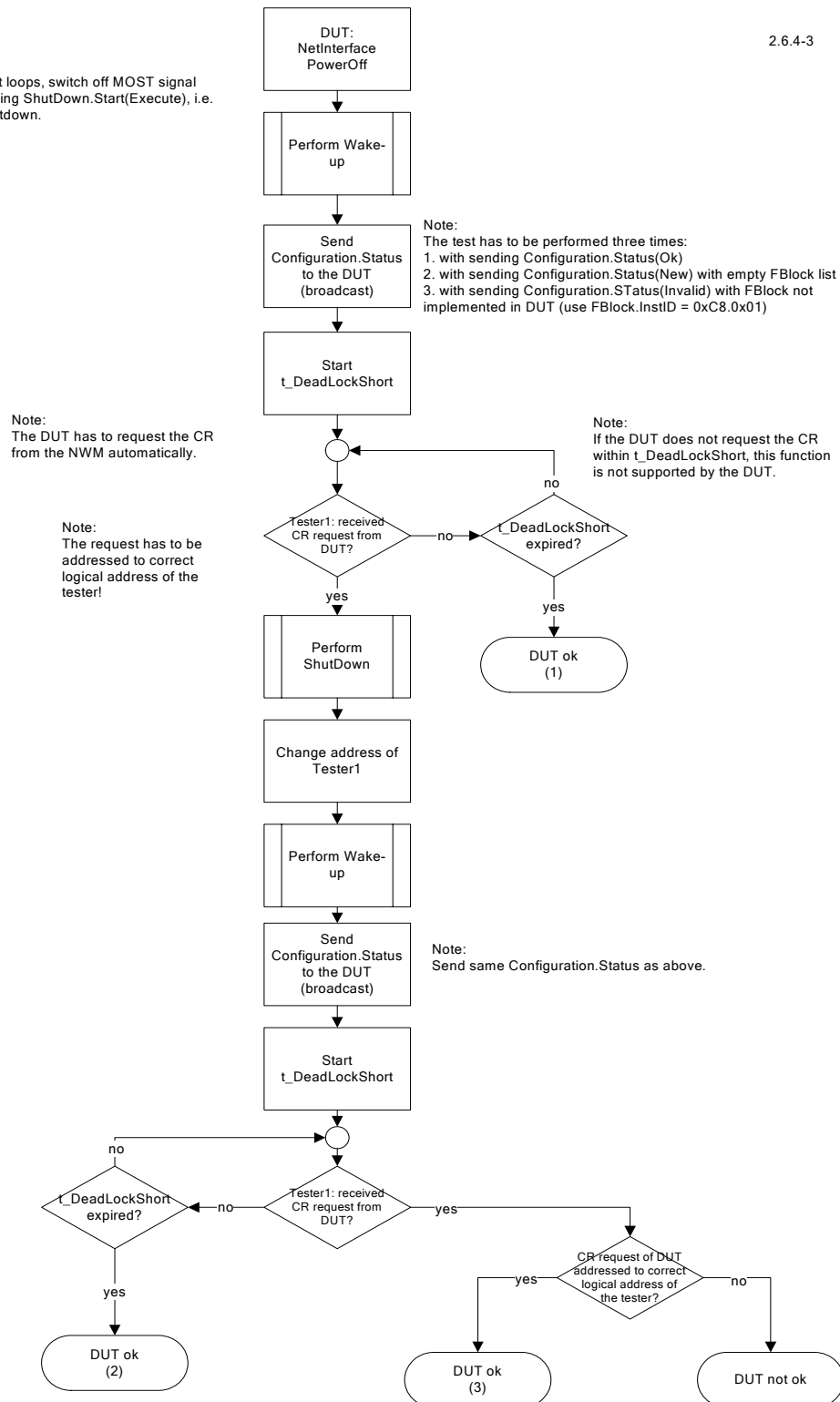


Figure 3-48 Flow of NWM Address Storage test.

3.9.2.3 Address Re-Initialization test (2.6.4-4)

Name of test	Address Re-Initialization test 2.6.4-4
Reference to MOST Specification	Rev. 2.4: 3.3.4.2 Specific Startup Behavior Rev. 2.5: 3.3.4.2 Specific Startup Behavior
Value of Interest	Address of DUT CentralRegistry.Get
Start Conditions	DUT in NormalOperation Ring closed
Test description	During runtime, the position of the DUT is changed (by switching Tester 2 from spy-mode into test-mode). Then, tester 1 sends "Configuration.Status(NotOK)". If the address of the DUT is in dynamic address range, it has to derive a new address from its ring position, else it has to continue using its old address. If the DUT supports DR, Tester 1 sends "Configuration.Status(ok)". The DUT has to send "CentralRegistry(Get)" to Tester 1 to rebuild its DR.
Experimental set-up	#2 - Tester 1 in spy-mode - Tester 2 in master mode
Device type	All devices except NWM and TM [x] MOST25 [x] MOST50 oPHY [x] MOST50 ePHY
Multi Node Device	All devices without NWM and TM
Note	
Results	DUT ok (1): The DUT has passed the test. DUT ok (2): The DUT has passed the test (does not support DR). DUT not ok (1): DUT (dyn. address) fails to change address in case of receiving Config(NotOK). DUT not ok (2): DUT (dyn address) fails to derive new address from ring position. DUT not ok (3): DUT (not dyn. address) fails to use previous address.. DUT not ok (4): DUT fails to delete / rebuild DR (if supported).

2.6.4-4

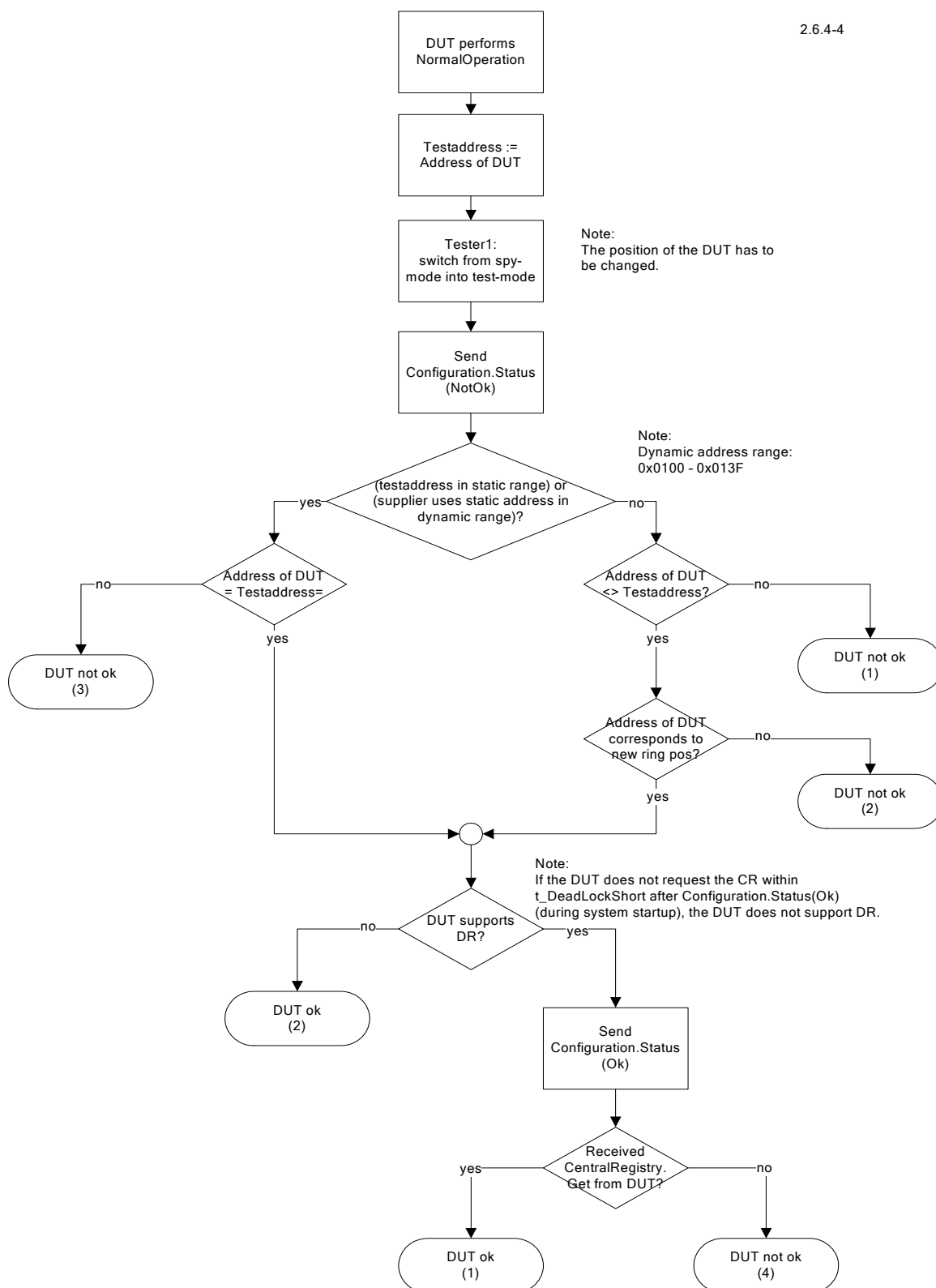


Figure 3-49 Flow of Address Re-Initialization test.

3.9.2.4 FBlock Response Time test (2.6.4-8)

Name of test	FBlock Response Time test 2.6.4-8
Reference to MOST Specification	Rev. 2.4: 3.3.4.3.3 Responding to Configuration Requests by the Network Master Rev. 2.5: 3.3.4.3.3 Responding to Configuration Requests by the Network Master
Value of Interest	t_Answer
Start Conditions	DUT: NetInterfacePowerOff Ring closed
Test description	After wake-up, the tester sends "FBlockIDs.Get" to the DUT. It has to respond within t_Answer_max.
Experimental set-up	#1 - Tester 1 in master mode or slave mode (depends on DUT)
Device type	All devices except NWM [x] MOST25 [x] MOST50 oPHY [x] MOST50 ePHY
Multi Node Device	All devices without NWM
Note	
Results	DUT ok: The DUT has passed the test. DUT not ok: DUT fails to respond within t_Answer.

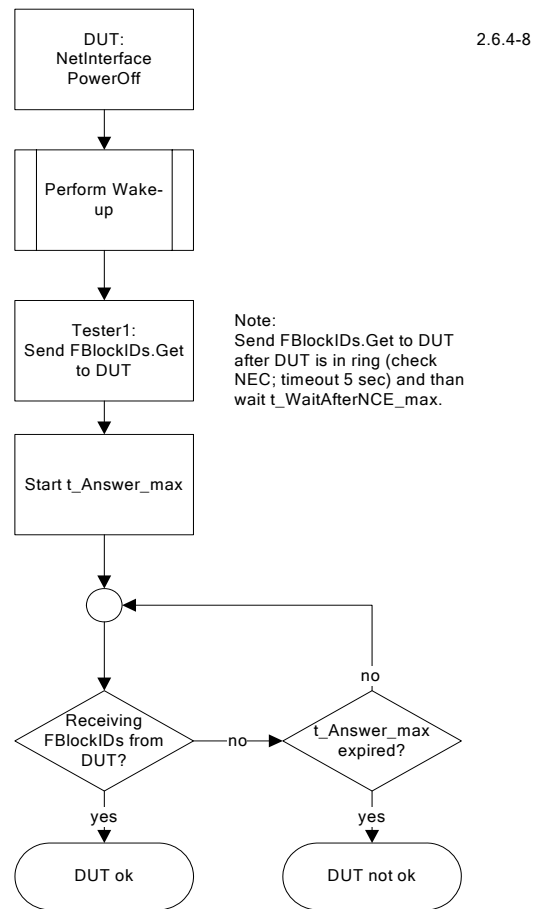


Figure 3-50 Flow of FBlock Response Time test.

3.9.2.5 Control message suppression test (2.6.4-9)

Name of test	Control message suppression test 2.6.4-9
Reference to MOST Specification	Rev. 2.4: 3.3.4.3.2 Behavior in System State NotOk Rev. 2.5: 3.3.4.3.2 Behavior in System State NotOk
Value of Interest	Control message from DUT
Start Conditions	DUT NetInterface PowerOff
Test description	The tester performs a wake-up. It does not send FBlockIDs.Get to the DUT during start-up. The DUT must not send any control message (except deallocate.all and debug messages) to the tester.
Experimental set-up	#1 - Tester 1 in master mode or slave mode (depends on DUT); NWM
Device type	All devices except NWM [x] MOST25 [x] MOST50 oPHY [x] MOST50 ePHY
Multi Node Device	All devices without NWM
Note	
Results	DUT ok: The DUT has passed the test. DUT not ok (1): DUT sends control message without receiving FBlockIDs.Get

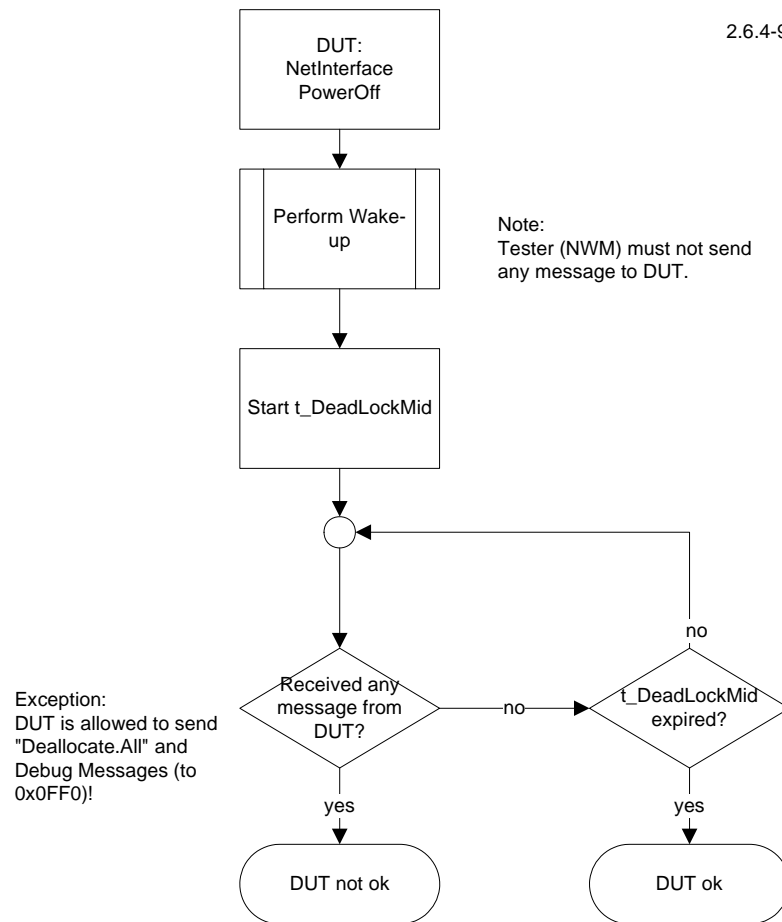


Figure 3-51 Flow of control message suppression test.

3.9.3 Configuration / System Configuration (General)

3.9.3.1 InstID Wildcard Test (2.6.4-10)

Name of test	InstID Wildcard Test 2.6.4-10
Reference to MOST Specification	Rev. 2.4: 2.3.2.3.6 InstID Wildcards Rev. 2.5: 2.3.2.3.6 InstID Wildcards
Value of Interest	FktIDs.Status InstID
Start Conditions	DUT: NormalOperation
Test description	The tester sends FktIDs.Get to the DUT, using wildcard 0x00 for InstID. The DUT has to respond FktIDs.Status. The respond of the DUT must not contain any wildcard as InstID. The tester sends FktIDs.Get to the DUT, using wildcard 0xFF for InstID. The DUT has to respond FktIDs.Status of all implemented instances of the current FBlock. The respond of the DUT must not contain any wildcard as InstID.
Experimental set-up	# 1 - Tester in master mode or slave mode (depends on DUT)
Device type	All devices [x] MOST25 [x] MOST50 oPHY [x] MOST50 ePHY
Multi Node Device	All devices
Note	The test has to be performed with every single registered FBlock of the DUT except Netblock and ET. All implemented instances of an FBlock of the DUT can be detected by the tester by sending FBlockIDs.Get to the DUT.
Results	DUT ok: The DUT has passed the test. DUT not ok (1): The DUT responds with Error "InstID not available" DUT not ok (2): The DUT uses wildcard for responding FktIDs.Status. DUT not ok (3): DUT fails to respond with FktIDs.Status of all implemented instances of the current FBlock. DUT not ok (4): The DUT uses wildcard for responding FktIDs.Status.

2.6.4-10

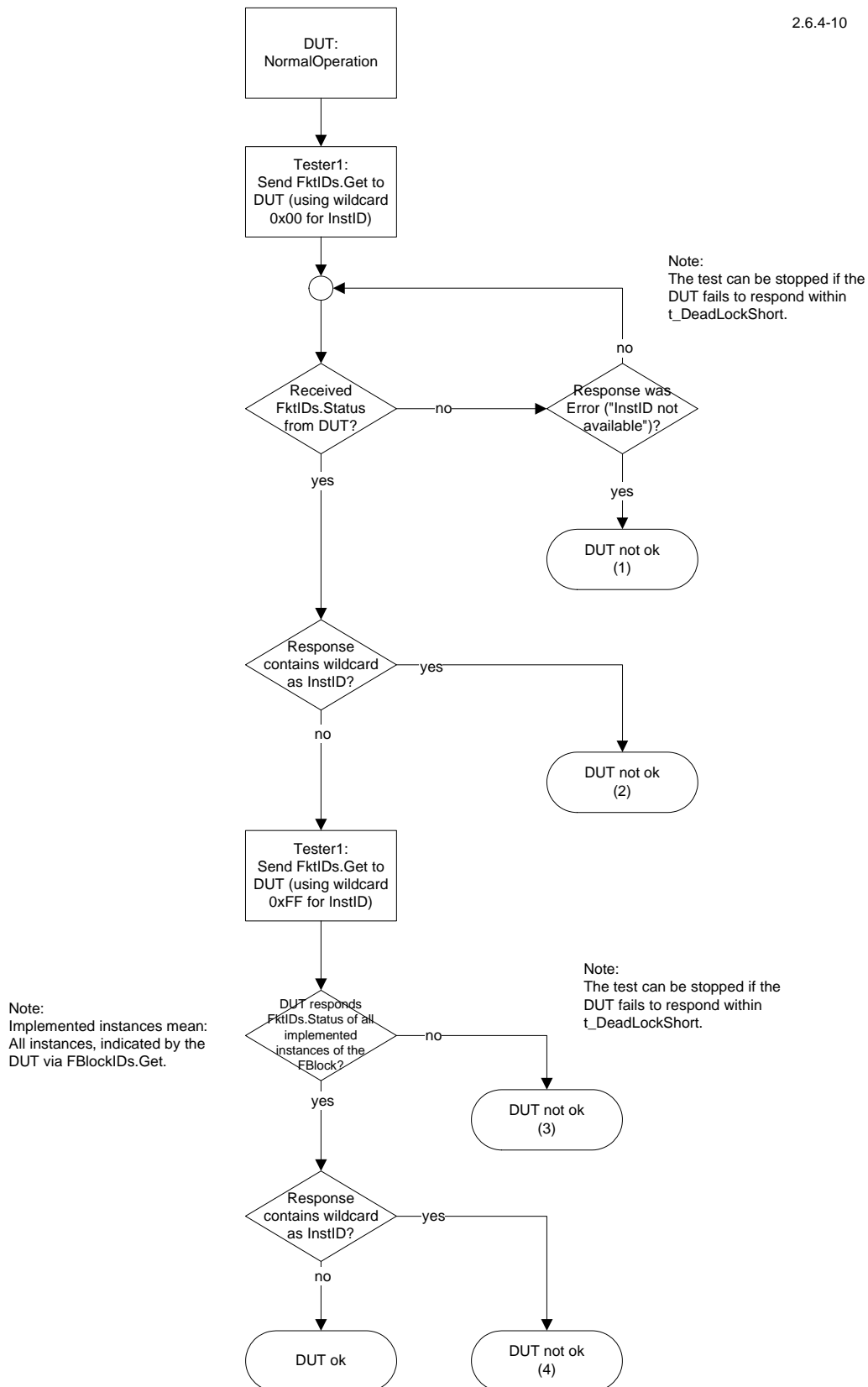


Figure 3-52 Flow of InstID Wildcard test.

3.10 Node Addressing

3.10.1.1 Node Addressing test (2.7-1)

Name of test	Node Addressing test 2.7-1
Reference to MOST Specification	Rev. 2.4: 3.4.1 Addressing Rev. 2.5: 3.4.1 Addressing
Value of Interest	Node Position Address of DUT Groupcast Address of DUT Broadcast Address of DUT
Start Conditions	DUT in NormalOperation Ring closed
Test description	At the beginning of the test, the ring is shut down and the position of the DUT is changed if DUT is not TM (Tester 1: spy mode → test mode). At the next wake-up, the DUT has to derive the new Node Position Address from its new ring-position. The Node Position Address, the Logical Node Address, the Broadcast Address and the Groupcast Address of the DUT are tested by sending a message to the corresponding address and checking the respond.
Experimental set-up	#2 - Tester 1 in spy mode; it must not respond to group cast messages. - Tester 2 in master mode or slave mode (depends on DUT).
Device type	All devices except NWM [x] MOST25 [x] MOST50 oPHY [x] MOST50 ePHY
Multi Node Device	All devices without NWM. If DUT contains TM, the ring position can only be changed of nodes in front of the TM.
Note	
Results	DUT ok (1): The DUT has passed the test. DUT not ok (1): DUT fails to respond to "Node position addressing". DUT not ok (2): DUT fails to respond to "Logical Node addressing". DUT not ok (3): DUT fails to respond to "groupcast addressing". DUT not ok (4): DUT fails to accept changes of the groupcast address. DUT not ok (5): DUT fails to respond to "broadcast addressing".

2.7-1

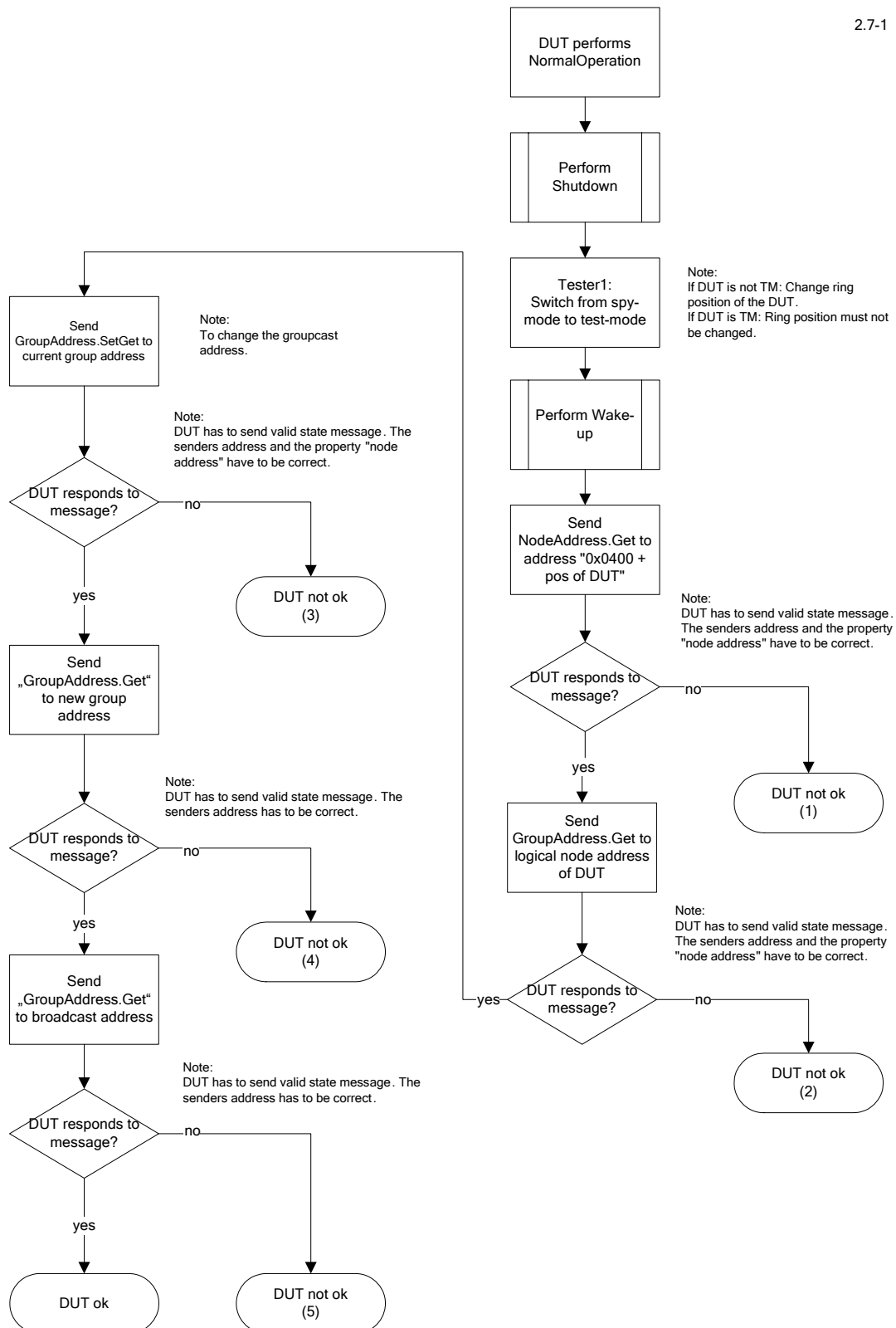


Figure 3-53 Flow of Node Addressing test.

3.10.1.2 Broadcast error test (2.8.4-9)

Name of test	Broadcast error test 2.8.4-9
Reference to MOST Specification	Rev. 2.4: 2.3.2.5.1 Error Rev. 2.5: 2.3.2.5.1 Error
Value of Interest	error respond of broadcast message
Start Conditions	DUT in NormalOperation
Test description	Tester 1 produces an error within the DUT by broadcasting message "ET.InstID(0x0).FktID(0x3c8).SetGet" to the DUT. The DUT must not return any error. The not implemented function will be addressed normally and DUT has to respond with error.
Experimental set-up	#1 - Tester 1 in master mode or slave mode (depends on DUT)
Device type	All devices [x] MOST25 [x] MOST50 oPHY [x] MOST50 ePHY
Multi Node Device	All devices
Note	
Results	DUT ok: The DUT has passed the test. DUT not ok (1): DUT responds error on broadcast messages DUT not ok (2): DUT fails to respond with error if not implemented function is addressed normally

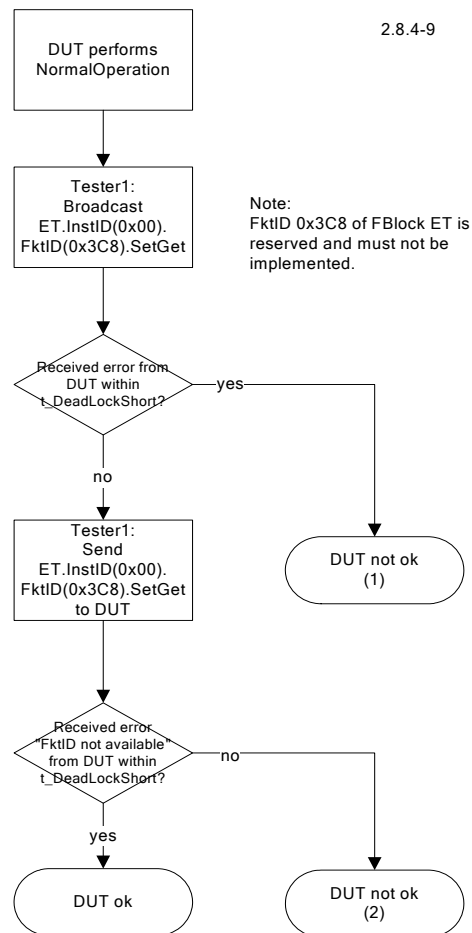


Figure 3-54 Flow of Broadcast error test.

3.11 Other Functions

3.11.1 ACK/NAK

3.11.1.1 ACK / NAK test (2.8.1-1) MOST2V5

Name of test	ACK / NAK test (MOST2V5) 2.8.1-1
Reference to MOST Specification	Rev. 2.5: 3.8 Timing Definitions
Value of Interest	ACK of DUT
Start Conditions	DUT in NormalOperation Ring closed
Test description	A pair of messages (ET.0x3C8.Result()) is sent to the DUT (using parameter RetryTime=5ms; RetryNumber=3). For every of the both messages (or retries), the tester has to receive an ACK from the DUT. This sequence has to be performed 20 times with equidistant periods of 50ms.
Experimental set-up	#1 - Tester 1 in master mode or slave mode (depends on DUT).
Device type	All devices [x] MOST25 [x] MOST50 oPHY [x] MOST50 ePHY
Multi Node Device	None
Note	Variation of exp. setup is allowed. An additionally spy (or slave spy) behind the DUT can be used to detect ACKs of DUT.
Results	DUT ok: The DUT has passed the test. DUT not ok: DUT does fails to acknowledge every message.

2.8.1-1

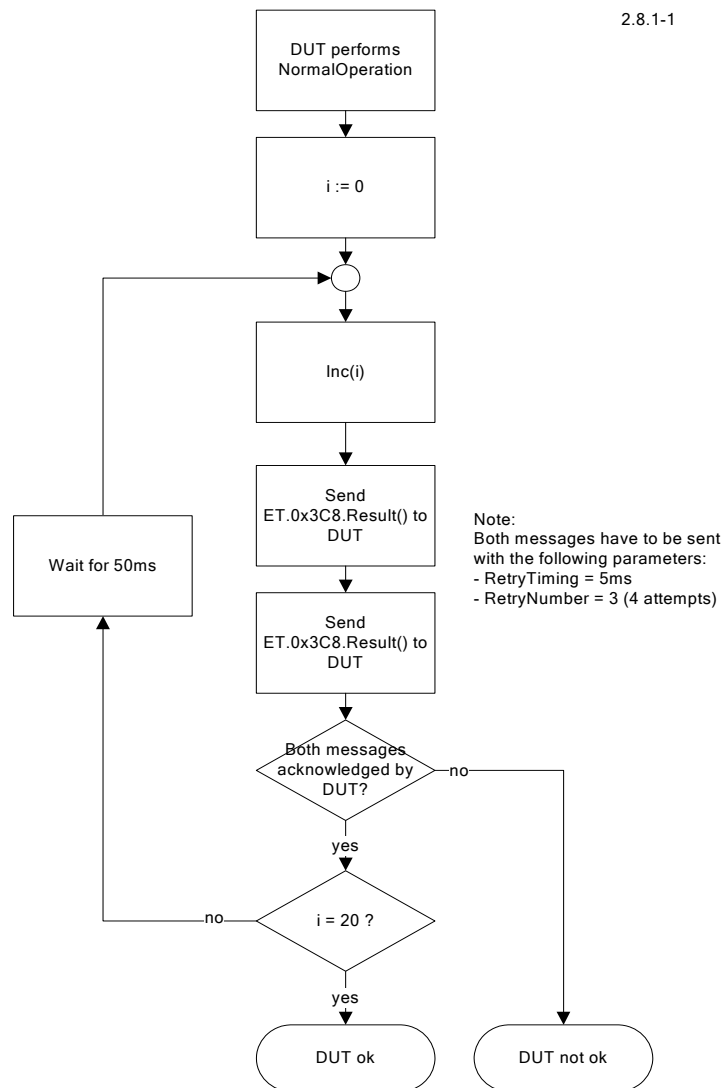


Figure 3-55 Flow of ACK / NAK test MOST2V5.

3.11.2 Notification Matrix

3.11.2.1 Notification Matrix Storage test (NWM) (2.8.3-1a)

Name of test	Notification Matrix Storage test (NWM) 2.8.3-1a
Reference to MOST Specification	Rev. 2.4: 2.3.12 Handling Message Notification Rev. 2.5: 2.3.12 Handling Message Notification
Value of Interest	Notification Matrix t_Property
Start Conditions	DUT in NormalOperation Ring closed
Test description	<p>The tester enters itself into the Notification Matrix of the DUT by using Notification.Set(All).</p> <p>If the DUT supports notification, it must not generate a notification error.</p> <p>If the tester triggers a "Configuration.Status(New)" (by sending new FBlockID lists with new FBlockIDs to the DUT), the DUT must not delete the entries from the Notification Matrix.</p> <p>Tester 2 leaves the ring by switching from test mode into spy mode. The DUT has to send "Config(Invalid)" and its Notification Matrix must not be empty.</p> <p>Tester 2 changes its address to "uninitialized_node_address" and enters the ring, again (test mode). The DUT has to broadcast "Config(NotOK)" and its Notification Matrix must not contain any entry of the tester.</p> <p>Tester 1 writes some entry to the Notification Matrix of the DUT. It triggers Config(ok). If DUT sends Config.Status(ok), the Notification Matrix will be checked again. It must contain all entries, made by the tester.</p> <p>Tester performs a ShutDown. At the next wake-up, the Notification Matrix of the DUT must not contain any entry of the tester.</p>
Experimental set-up	#2 - Tester 1 in slave mode or master mode (depends on DUT) - Tester 2 in test mode
Device type	NWM [x] MOST25 [x] MOST50 oPHY [x] MOST50 ePHY
Multi Node Device	All devices containing NWM
Note	<p>The test has to be performed with every single registered FBlock and function of the DUT except FBlock ET and FBlock PM. All FBlocks have to be tested within one testloop (no separate testloop for every FBlock) to increase stress of DUT.</p> <p>At the beginning of the test, the notification of the DUT must not contain any entry of the tester.</p> <p>Test is based on generation of NCEs so the DUT has to check the SystemConfiguration on each NCE and the tester 2 has to set the FBlocks and address accordingly.</p>
Results	<p>DUT ok (1): The DUT has passed the test. It does not support notification.</p> <p>DUT ok (2): The DUT has passed the test.</p> <p>DUT not ok (1): DUT fails to store any entry into its notification matrix</p> <p>DUT not ok (2): DUT deletes Notification Matrix in case of Config(New).</p> <p>DUT not ok (3): DUT deletes Notification Matrix in case of Config(Invalid).</p> <p>DUT not ok (4): DUT fails to delete the Notification Matrix in case of Config(NotOK).</p> <p>DUT not ok (5): DUT deletes Notification Matrix in case of Config(Ok) during SystemState(Ok).</p> <p>DUT not ok (6): DUT fails to delete the Notification Matrix after NetOn.</p>

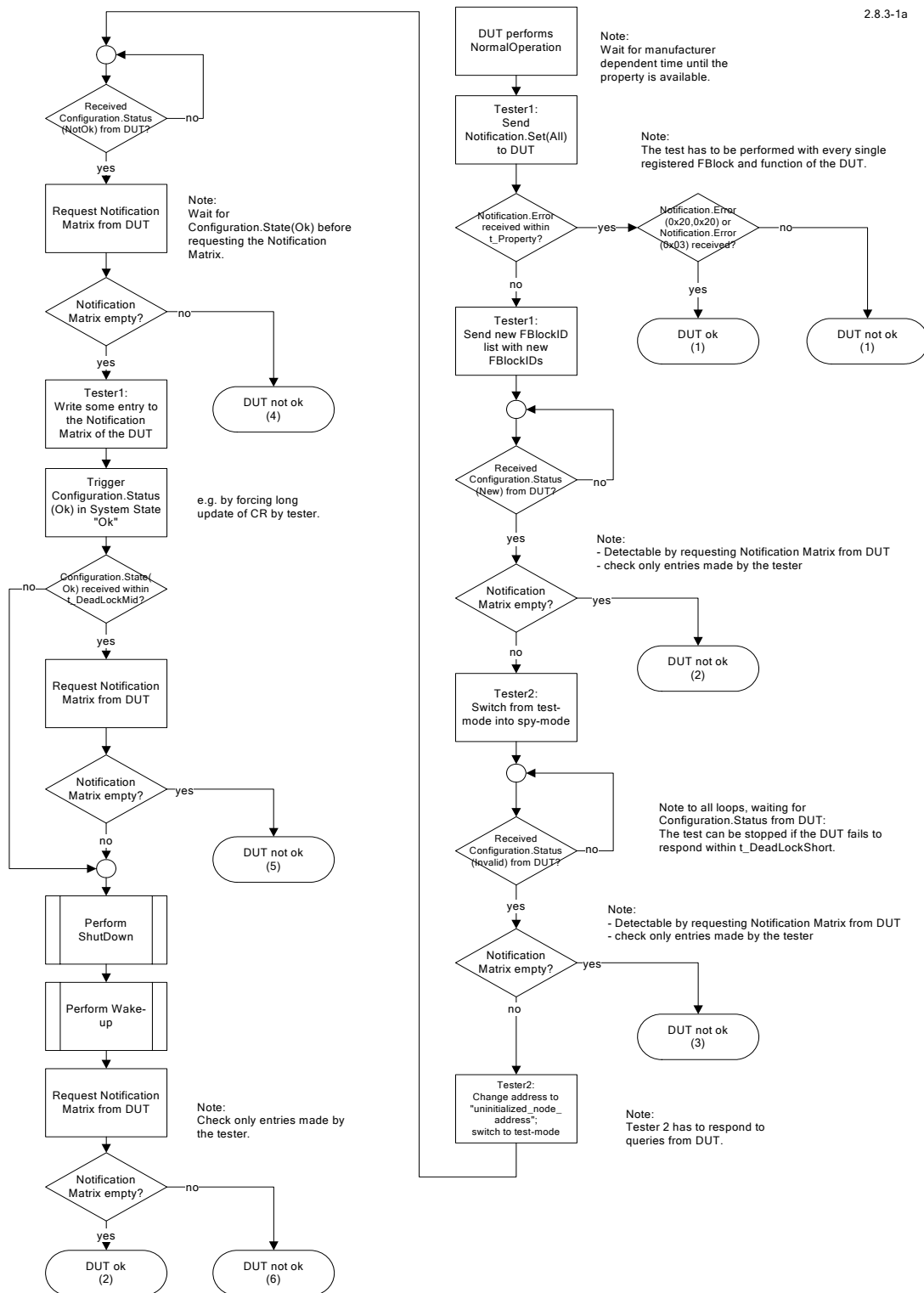


Figure 3-56 Flow of Notification Matrix Storage test (NWM).

3.11.2.2 Notification Matrix Storage test (Slave) (2.8.3-1b)

Name of test	Notification Matrix Storage test (Slave) 2.8.3-1b
Reference to MOST Specification	Rev. 2.4: 2.3.12 Handling Message Notification Rev. 2.5: 2.3.12 Handling Message Notification
Value of Interest	Notification Matrix t_Property
Start Conditions	DUT in NormalOperation Ring closed
Test description	<p>The tester enters itself into the Notification Matrix of the DUT by using Notification.Set(All).</p> <p>If the DUT supports notification, it must not generate a notification error.</p> <p>If the tester sends "Config(Ok)", the DUT must not delete the entries from the Notification Matrix.</p> <p>If the tester sends "Config(New)", the DUT must not delete the entries from the Notification Matrix.</p> <p>If the tester sends "Config(Invalid)", the DUT must not delete the entries from the Notification Matrix.</p> <p>If the tester sends "Config(NotOK)", the DUT has to delete the entries of the tester from the Notification Matrix.</p> <p>Tester 1 writes some entry to the Notification Matrix of the DUT and performs a ShutDown. At the next wake-up, the Notification Matrix of the DUT must not contain any entry of the tester.</p>
Experimental set-up	#1 - Tester 1 in master mode or slave mode (depends on DUT)
Device type	All devices except NWM [x] MOST25 [x] MOST50 oPHY [x] MOST50 ePHY
Multi Node Device	All devices without NWM
Note	<p>The test has to be performed with every single registered FBlock and function of the DUT except FBlock ET and FBlock PM. All FBlocks have to be tested within one testloop (no separate testloop for every FBlock) to increase stress of DUT.</p> <p>At the beginning of the test, the notification of the DUT must not contain any entry of the tester.</p>
Results	<p>DUT ok (1): The DUT has passed the test. It does not support notification.</p> <p>DUT ok (2): The DUT has passed the test.</p> <p>DUT not ok (1): DUT fails to store any entry into its notification matrix</p> <p>DUT not ok (2): DUT deletes Notification Matrix if Config(Ok) has been received.</p> <p>DUT not ok (3): DUT deletes Notification Matrix if Config(New) has been received.</p> <p>DUT not ok (4): DUT deletes Notification Matrix if Config(Invalid) has been received.</p> <p>DUT not ok (5): DUT fails to delete the Notification Matrix if Config(NotOK) has been received.</p> <p>DUT not ok (6): DUT fails to delete the Notification Matrix after NetOn.</p>

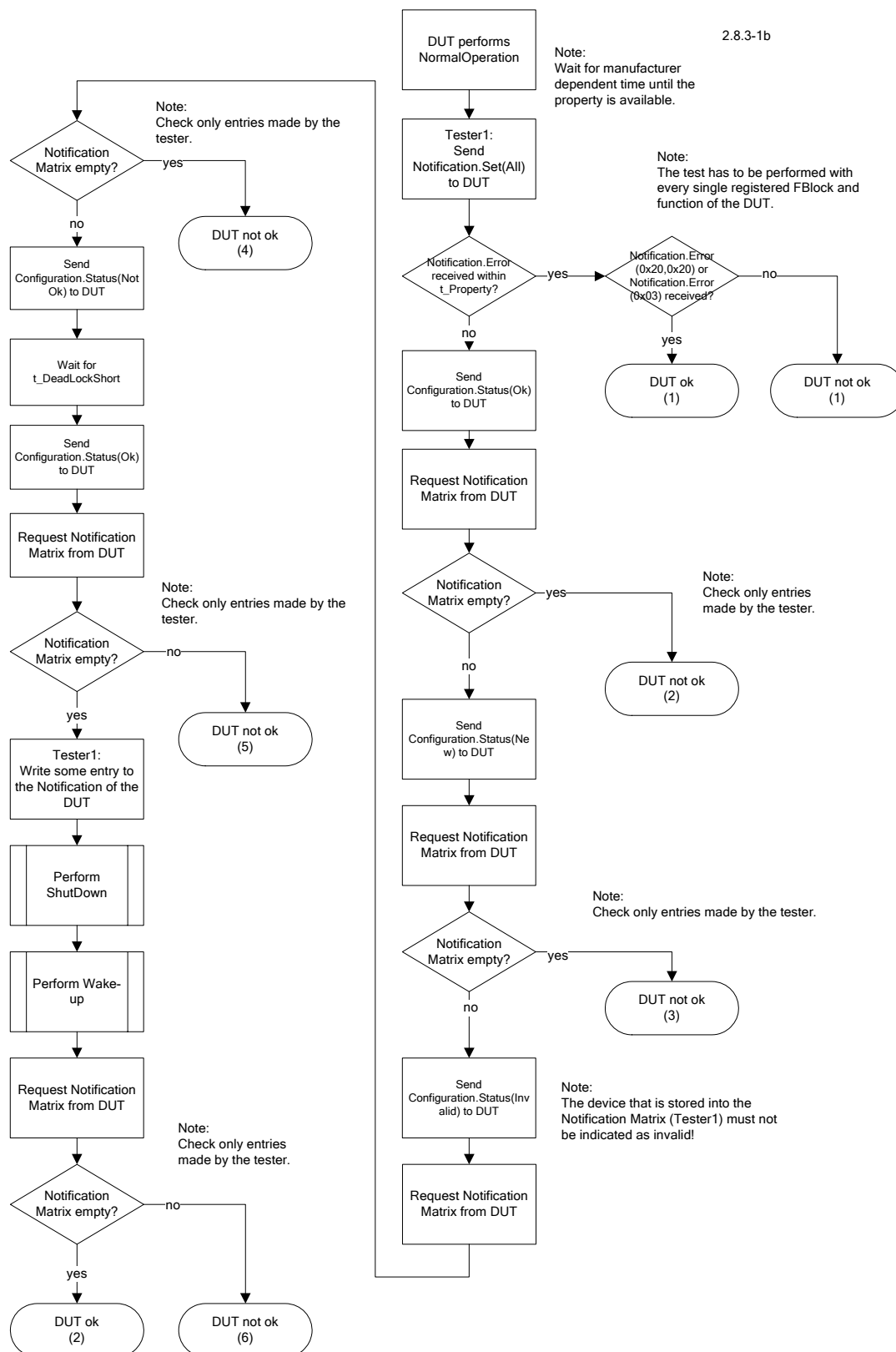


Figure 3-57 Flow of Notification Matrix Storage test (Slave).

3.11.2.3 NotificationCheck test (2.8.3-2)

Name of test	NotificationCheck test 2.8.3-2
Reference to MOST Specification	Rev. 2.4: 2.3.2.4 FktID Rev. 2.5: 2.3.2.4 FktID
Value of Interest	NotificationMatrix
Start Conditions	DUT: NormalOperation
Test description	The tester1 requests the FktID list of DUT to check whether NotificationCheck is supported. If the DUT supports NotificationCheck, the tester1 enters itself into all properties of the current FBlock to the DUT that support notification. Then the tester sends Notification.Check to the DUT. The DUT has to respond a list that contains all functions supporting notification.
Experimental set-up	# 2 - Tester 1 in master mode or slave mode (depends on DUT) - Tester 2 in slave mode
Device type	All devices [x] MOST25 [x] MOST50 oPHY [x] MOST50 ePHY
Multi Node Device	All devices
Note	The test has to be performed with every single registered FBlock of the DUT.
Results	DUT ok (1): The DUT has passed the test. DUT ok (2): The DUT has passed the test (does not support NotificationCheck). DUT not ok (1): The DUT fails to respond with list, containing all functions that support notification. DUT not ok (2): Responded list from DUT (for tester2) not empty. DUT not ok (3): DUT fails to respond to FktIDs.Get in time.

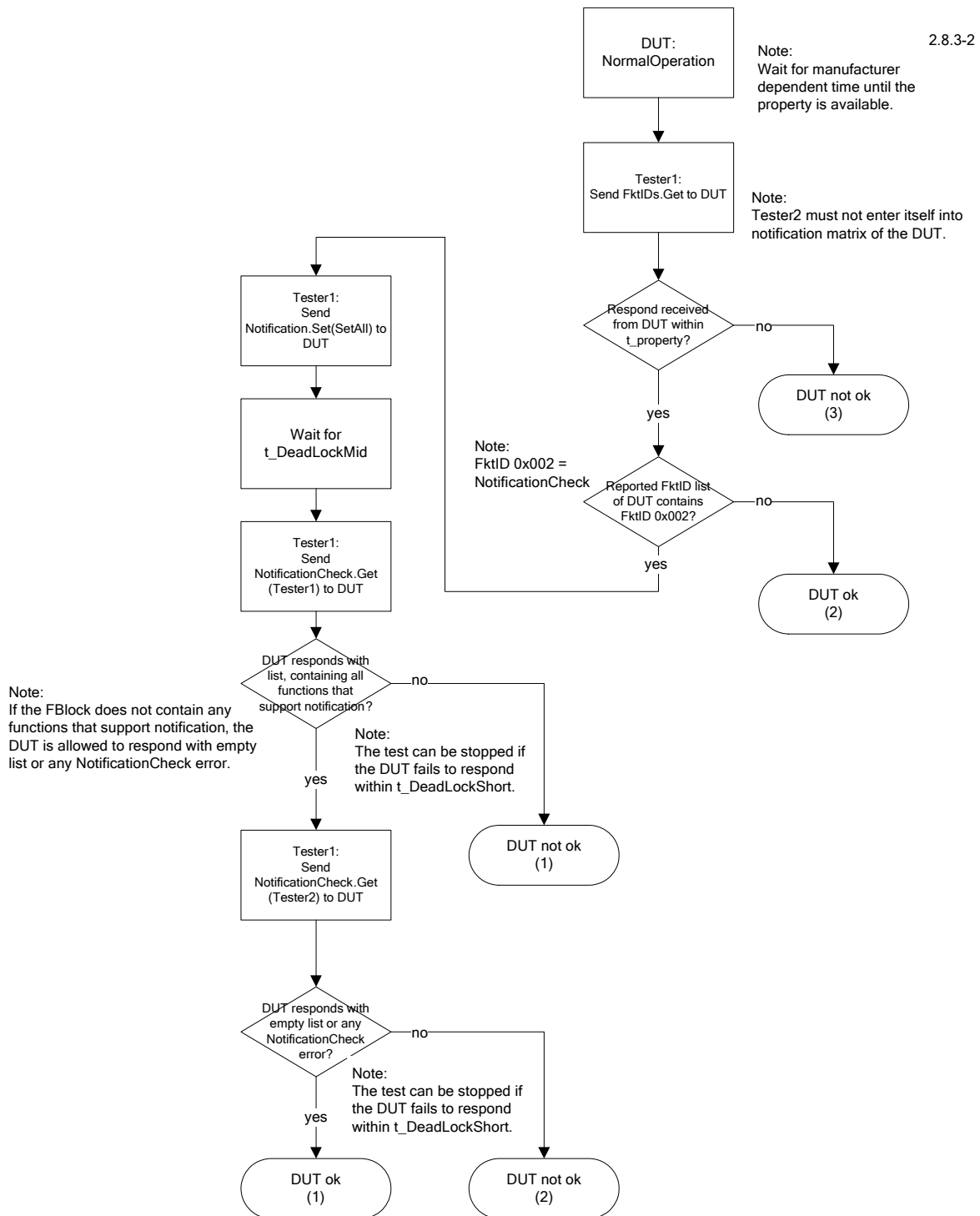


Figure 3-58 Flow of NotificationCheck test.

3.11.2.4 Notification Matrix Double Entry test (2.8.3-7)

Name of test	Notification Matrix Double Entry test 2.8.3-7
Reference to MOST Specification	Rev. 2.4: 2.3.12 Handling Message Notification Rev. 2.5: 2.3.12 Handling Message Notification
Value of Interest	Notification Matrix t_Property
Start Conditions	DUT in NormalOperation Ring closed
Test description	To get all functions that could be used for notification, a "Notification.Set(All)" is sent to the DUT. If the DUT supports notification, it must not generate a notification error. The tester sends "Notification.Set(All)" to the DUT, again (try to generate double entries). The DUT has to acknowledge the double entries normally but it must not store it into the Notification Matrix. If the tester deletes itself from the Notification Matrix, the Notification Matrix must not contain any entry of the tester.
Experimental set-up	#1 - Tester 1 in master mode or slave mode (depends on DUT).
Device type	All devices [x] MOST25 [x] MOST50 oPHY [x] MOST50 ePHY
Multi Node Device	All devices
Note	The test has to be performed with every single registered FBlock and function of the DUT except FBlock ET and FBlock PM. All FBlocks have to be tested within one testloop (no separate testloop for every FBlock) to increase stress of DUT. To check whether the DUT supports notification, the FBlock ET could be used to check the size of the notification matrix. At the beginning of the test, the notification matrix of the DUT must not contain any entry of the tester.
Results	DUT ok (1): The DUT has passed the test. It does not support notification. DUT ok (2): The DUT has passed the test. DUT not ok (1): DUT fails to store any entry into its notification matrix. DUT not ok (2): DUT fails to acknowledge entries to the Notification Matrix within t_Property. DUT not ok (3): DUT stores double entries into the Notification Matrix. DUT not ok (4): DUT fails to delete devices from the Notification Matrix.

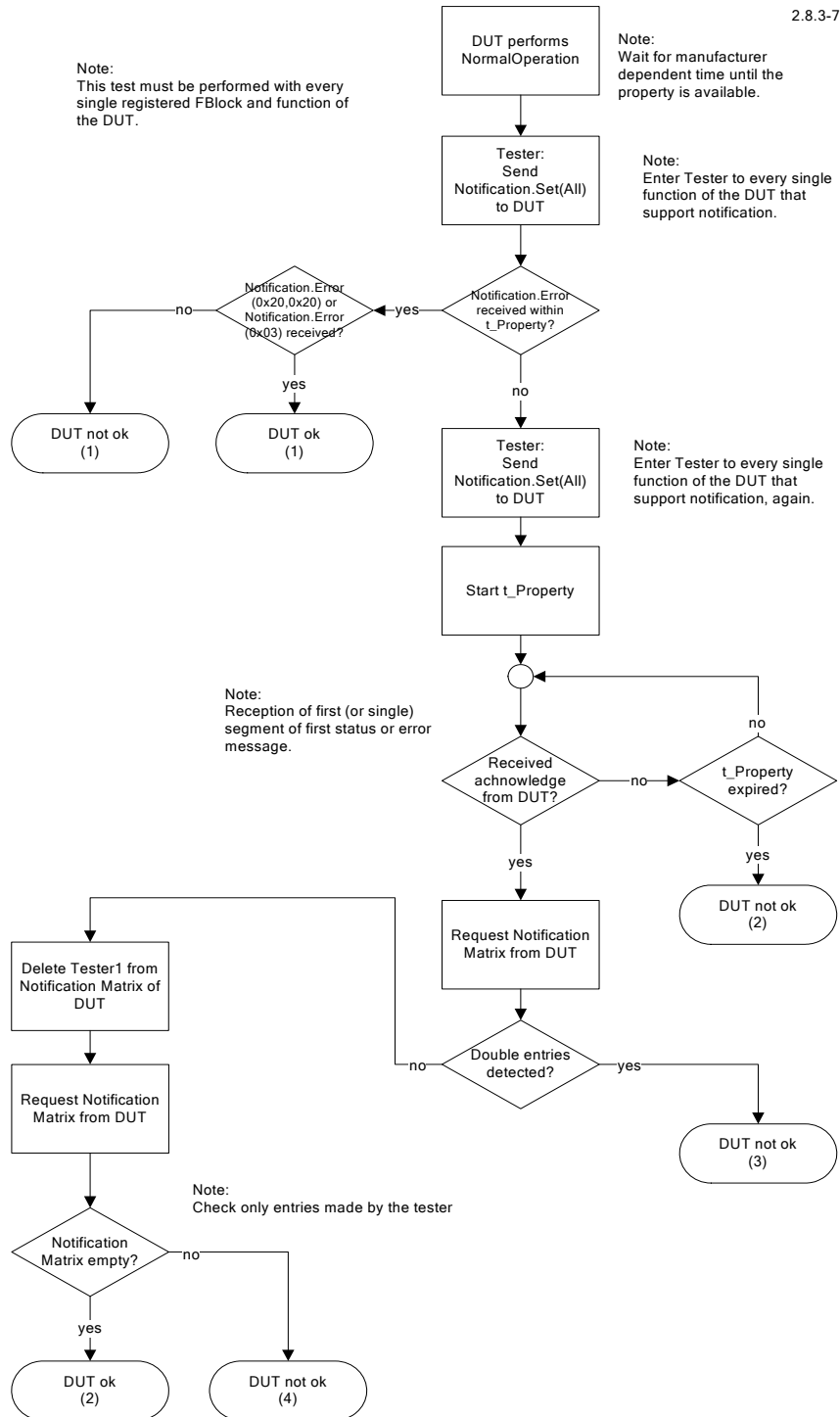


Figure 3-59 Flow of Notification Matrix Double Entry test.

3.11.2.5 Notification Error test (2.8.3-10)

Name of test	Notification Error test 2.8.3-10
Reference to MOST Specification	Rev. 2.4: 2.3.12 Handling Message Notification Rev. 2.5: 2.3.12 Handling Message Notification
Value of Interest	Notification Matrix Notification.Error
Start Conditions	DUT in NormalOperation Ring closed
Test description	<p>The tester requests a list of all function-IDs of the DUT (by means of "FktIDs.Get"). It tries to register itself to a function of the DUT that does not exist. The DUT has to respond with a "Notification Error" if the FBlock supports notification.</p> <p>The tester sends "Notification.Set(All)" to the DUT to get a list of all functions that support notification. It tries to register to a function that does not support notification. The DUT has to respond with an error.</p> <p>The tester tries to read out the "notification state" of a function that does not support notification. The DUT has to respond with an error.</p> <p>The notification matrix of the DUT will be cleared by means of "Notification.Set(ClearAll)" and the tester tries to delete an existing, not notified function from the notification matrix of the DUT. The DUT must not return any error.</p>
Experimental set-up	#1 - Tester 1 in master mode or slave mode (depends on DUT)
Device type	All devices [x] MOST25 [x] MOST50 oPHY [x] MOST50 ePHY
Multi Node Device	All devices
Note	<p>The test has to be performed with every single registered FBlock of the DUT except FBlock ET and FBlock PM.</p> <p>All FBlocks have to be tested within one testloop (no separate testloop for every FBlock) to increase stress of DUT.</p>
Results	<p>DUT ok (1): The DUT has passed the test.</p> <p>DUT ok (2): The DUT has passed the test (FB does not support notification).</p> <p>DUT not ok (1): DUT fails to send any error if function does not exist.</p> <p>DUT not ok (2): DUT fails to send "Notification Error" if function does not support notification.</p> <p>DUT not ok (3): DUT fails to send "Notification Error(0x07,0x1)" if requested function does not support notification.</p> <p>DUT not ok (4): DUT responds with error in case of deletion of a not notified function.</p>

2.8.3-10

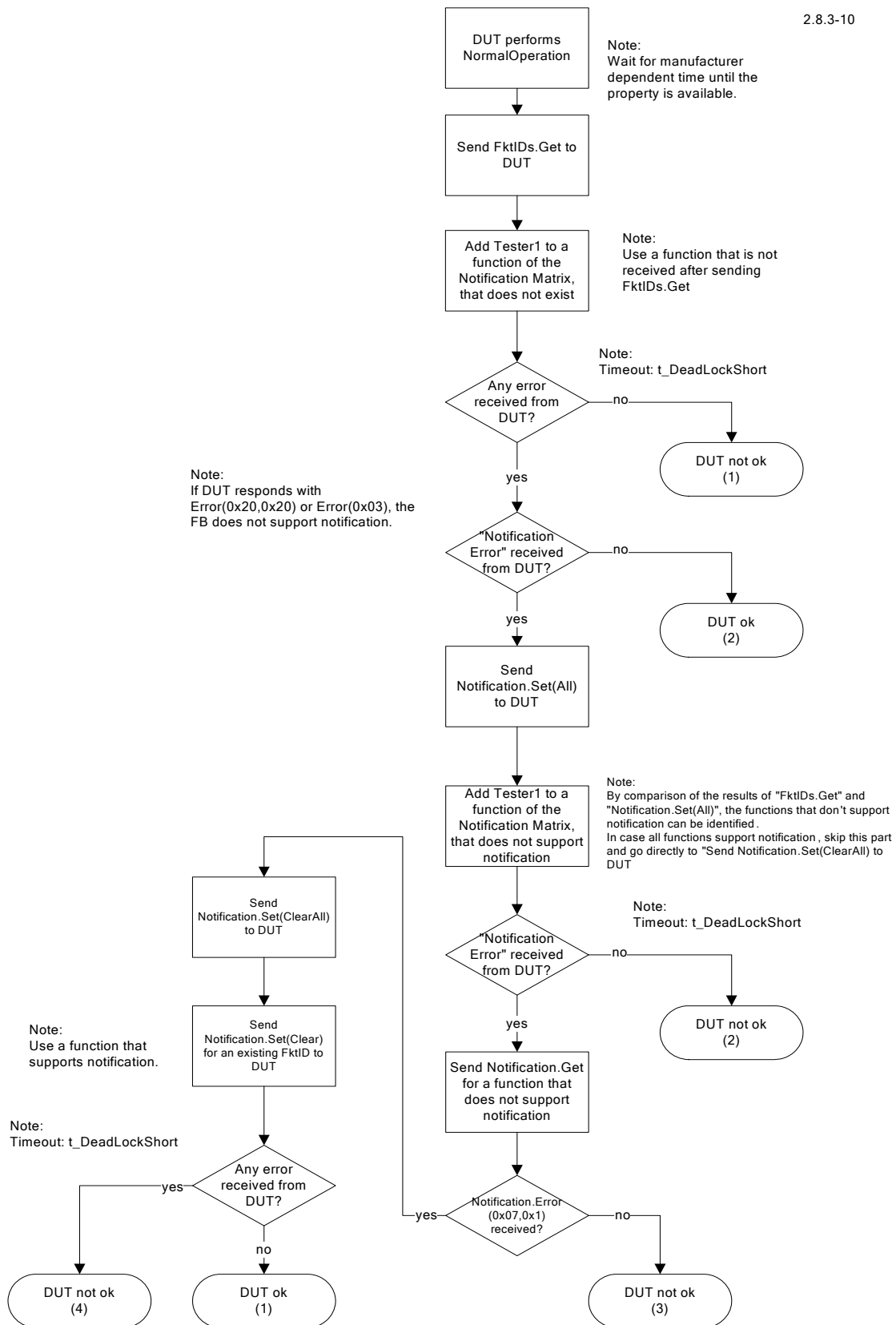


Figure 3-60 Flow of Notification Error test.

3.11.3 Message Segmentation

3.11.3.1 Segmented Message Rejection test (2.8.4-1)

Name of test	Segmented Message Rejection test 2.8.4-1
Reference to MOST Specification	Rev. 2.4: 2.3.2.5.1 Error Rev. 2.5: 2.3.2.5.1 Error
Value of Interest	Segmentation Error t_Property
Start Conditions	DUT in NormalOperation Ring closed
Test description	The tester sends the first part of a segmented message ("NetBlock.FBlockIDs.Get") to the DUT. The OP-Type "Get" is used to be sure that the DUT responds to the message. If the DUT does not support segmented messages, it has to respond with a "segmentation error". If the DUT supports segmented messages, it has to respond with "Segmentation Error(05)".
Experimental set-up	#1 - Tester 1 in master mode or slave mode (depends on DUT).
Device type	All devices [x] MOST25 [x] MOST50 oPHY [x] MOST50 ePHY
Multi Node Device	All devices
Note	
Results	DUT ok (1): The DUT has passed the test (DUT does not support segmented messages). DUT ok (2): The DUT has passed the test (DUT supports segmented messages). DUT not ok(1): DUT fails to send "Segmentation Error" in time.

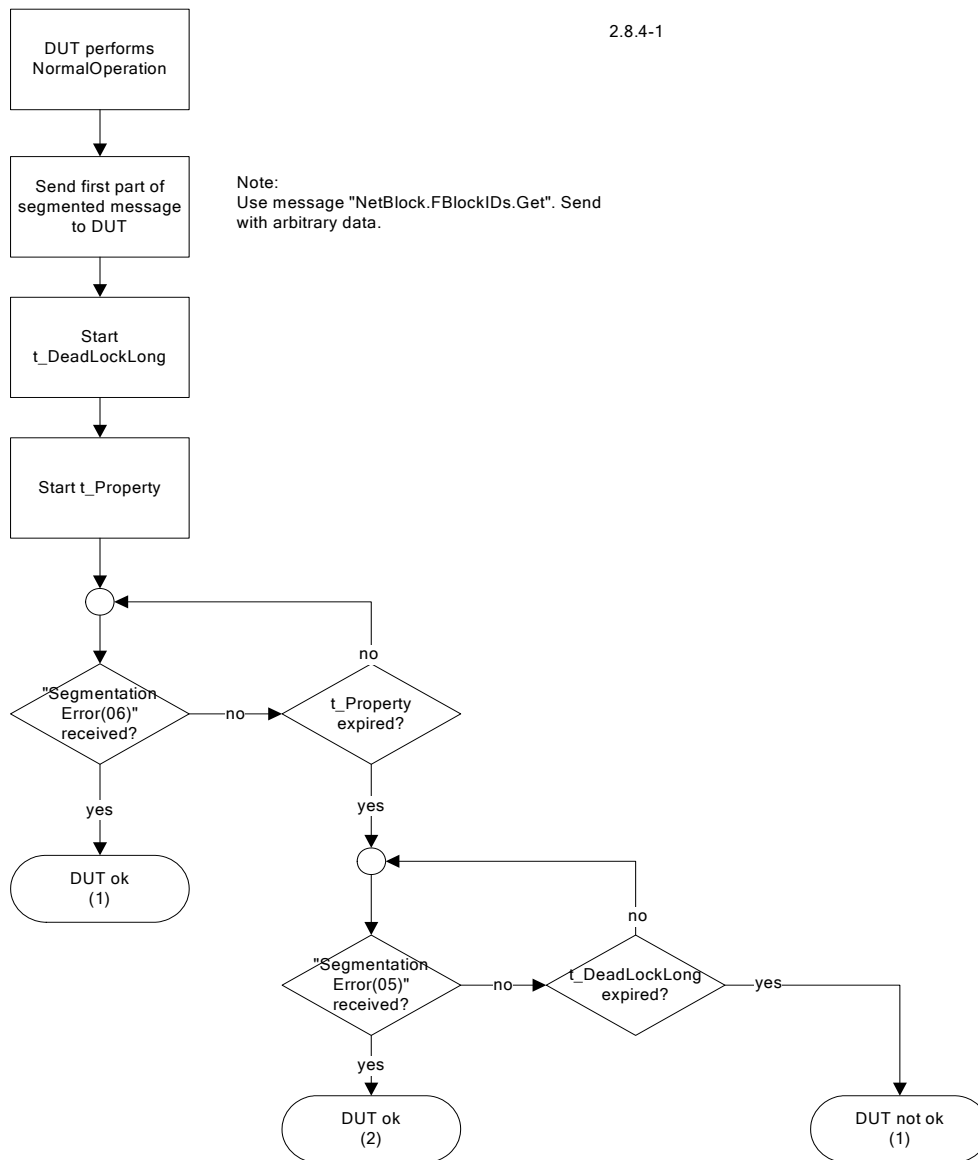


Figure 3-61 Flow of Segmented Message Rejection test.

3.11.3.2 Segmented Message Sending test (2.8.4-2)

Name of test	Segmented Message Sending test 2.8.4-2
Reference to MOST Specification	Rev. 2.4: 3.4.7.2 Application Message Service (AMS) and Application Protocols Rev. 2.5: 3.4.6.2 Application Message Service (AMS) and Application Protocols
Value of Interest	Segmented message from DUT
Start Conditions	DUT in NormalOperation Ring closed
Test description	<p>The tester triggers the DUT (via "FBlock ET"; EchoMessage) to send a segmented message (at least four segments) to the tester. The DUT has to send the message correctly:</p> <ul style="list-style-type: none"> - Sequence of message has to be correct - The whole message has to be sent right in time (5 seconds x number of segments) - The telegram types have to be correct (1,2,2,3)
Experimental set-up	#1 - Tester 1 in master mode or slave mode (depends on DUT)
Device type	<p>All devices that support message segmentation.</p> <p>[x] MOST25 [x] MOST50 oPHY [x] MOST50 ePHY</p>
Multi Node Device	All devices with at least one message segmentation supporting node
Note	Possibly, some devices are able to send segmented messages but not able to receive.
Results	<p>DUT ok: The DUT has passed the test.</p> <p>DUT not ok: The DUT fails to send a segmented message correctly.</p>

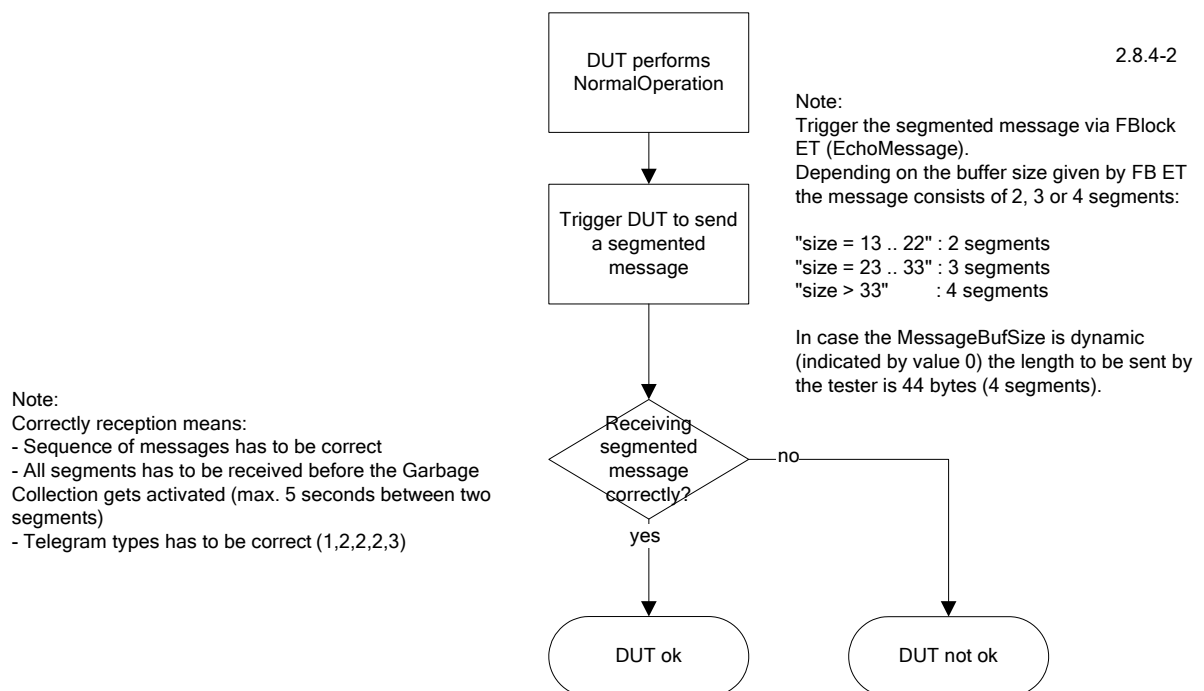


Figure 3-62 Flow of Segmented Message Sending test.

3.11.3.3 Message Segmentation Error test (2.8.4-3)

Name of test	Message Segmentation Error test 2.8.4-3
Reference to MOST Specification	Rev. 2.4: 2.3.2.5.1 Error Rev. 2.5: 2.3.2.5.1 Error
Value of Interest	Segmentation Error t_Property
Start Conditions	DUT in NormalOperation Ring closed
Test description	<p>The tester sends a second part of a segmented message ("NetBlock.FBlockIDs.Get") to the DUT without sending the first part. The DUT has to respond with "Segmentation Error(01)".</p> <p>The tester sends the first and the third part of a segmented message without sending the second part. The DUT has to respond with "Segmentation Error(03)".</p> <p>The tester sends the first part of a segmented message twice. The DUT has to respond with "Segmentation Error(07)".</p>
Experimental set-up	#1 - Tester 1 in master mode or slave mode (depends on DUT)
Device type	<p>All devices that support message segmentation.</p> <p>[x] MOST25 [x] MOST50 oPHY [x] MOST50 ePHY</p>
Multi Node Device	All devices with at least one message segmentation supporting node
Note	
Results	<p>DUT ok: The DUT has passed the test.</p> <p>DUT not ok (1): DUT fails to detect/indicate missing first part of segm. message.</p> <p>DUT not ok (2): DUT fails to detect/indicate wrong order of parts of segm. message.</p> <p>DUT not ok (3): DUT fails to detect/indicate parts of segm. message that are sent twice.</p>

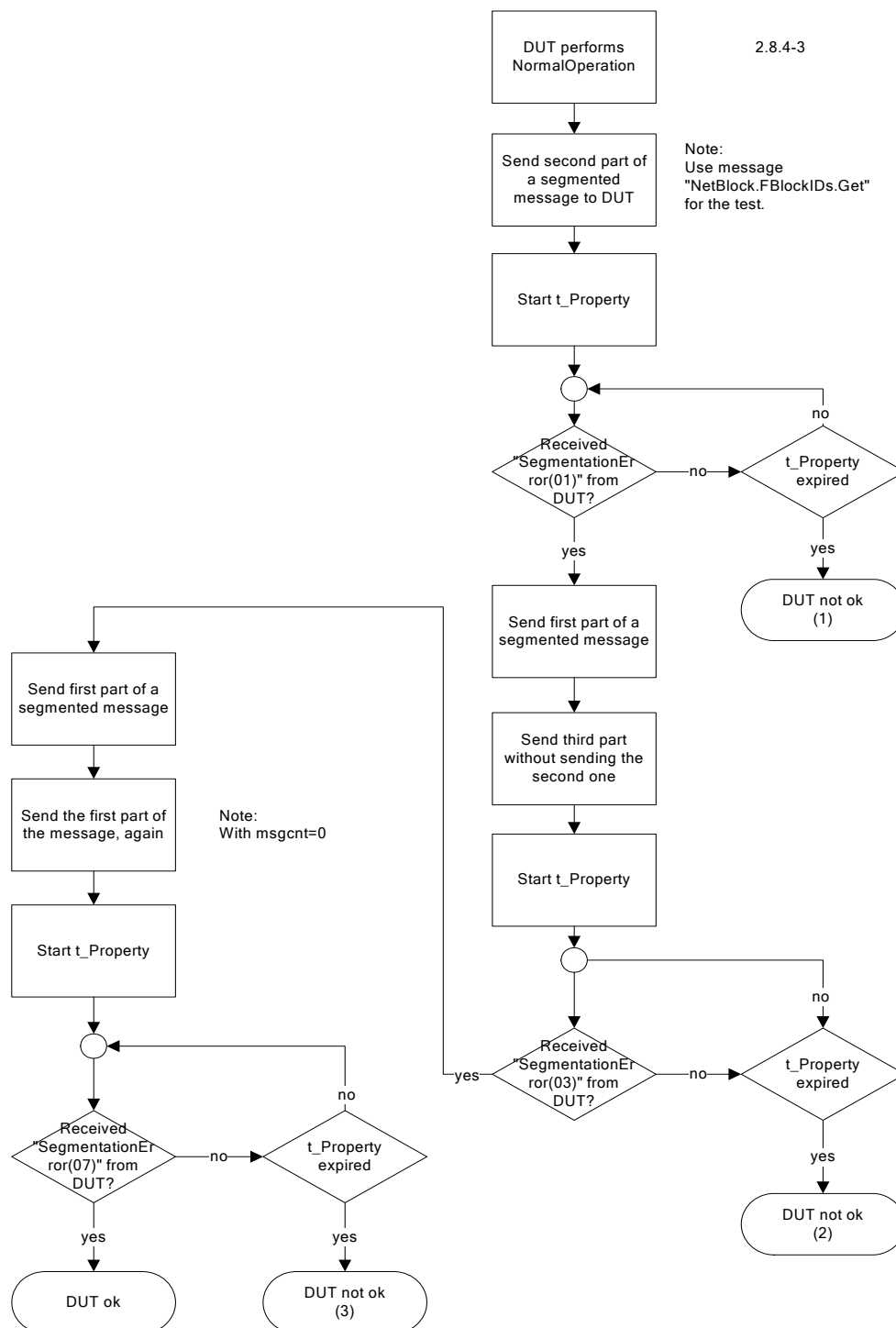


Figure 3-63 Flow of Message Segmentation Error test.

3.11.3.4 Message Segmentation Buffer test (2.8.4-7)

Name of test	Message Segmentation Buffer test 2.8.4-7
Reference to MOST Specification	Rev. 2.4: 3.4.7.2 Application Message Service (AMS) and Application Protocols Rev. 2.5: 3.4.6.2 Application Message Service (AMS) and Application Protocols
Value of Interest	Segmentation Error
Start Conditions	DUT in NormalOperation Ring closed
Test description	<p>If DUT does not support dynamic buffer size, the tester sends a segmented message ("ET.EchoMessage(0x208).Data" with correct InstID for the DUT) to the DUT that exceeds the supported buffer size. The DUT has to respond with "Segmentation Error(0x02)".</p> <p>The tester sends a segmented message with more than 256 segments (but smaller than the supported buffer size). The DUT must not return any segmentation error.</p> <p>If DUT supports dynamic buffer size, the first part of the test (trigger of "Segmentation Error (0x02)") will be skipped.</p>
Experimental set-up	#1 - Tester 1 in master mode or slave mode (depends on DUT).
Device type	All devices that support message segmentation. [x] MOST25 [x] MOST50 oPHY [x] MOST50 ePHY
Multi Node Device	All devices with at least one message segmentation supporting node
Note	
Results	<p>DUT ok: The DUT has passed the test.</p> <p>DUT not ok (1): DUT fails to return a "buffer size overflow" error.</p> <p>DUT not ok (2): DUT fails to handle more than 256 segments per message.</p>

2.8.4-7

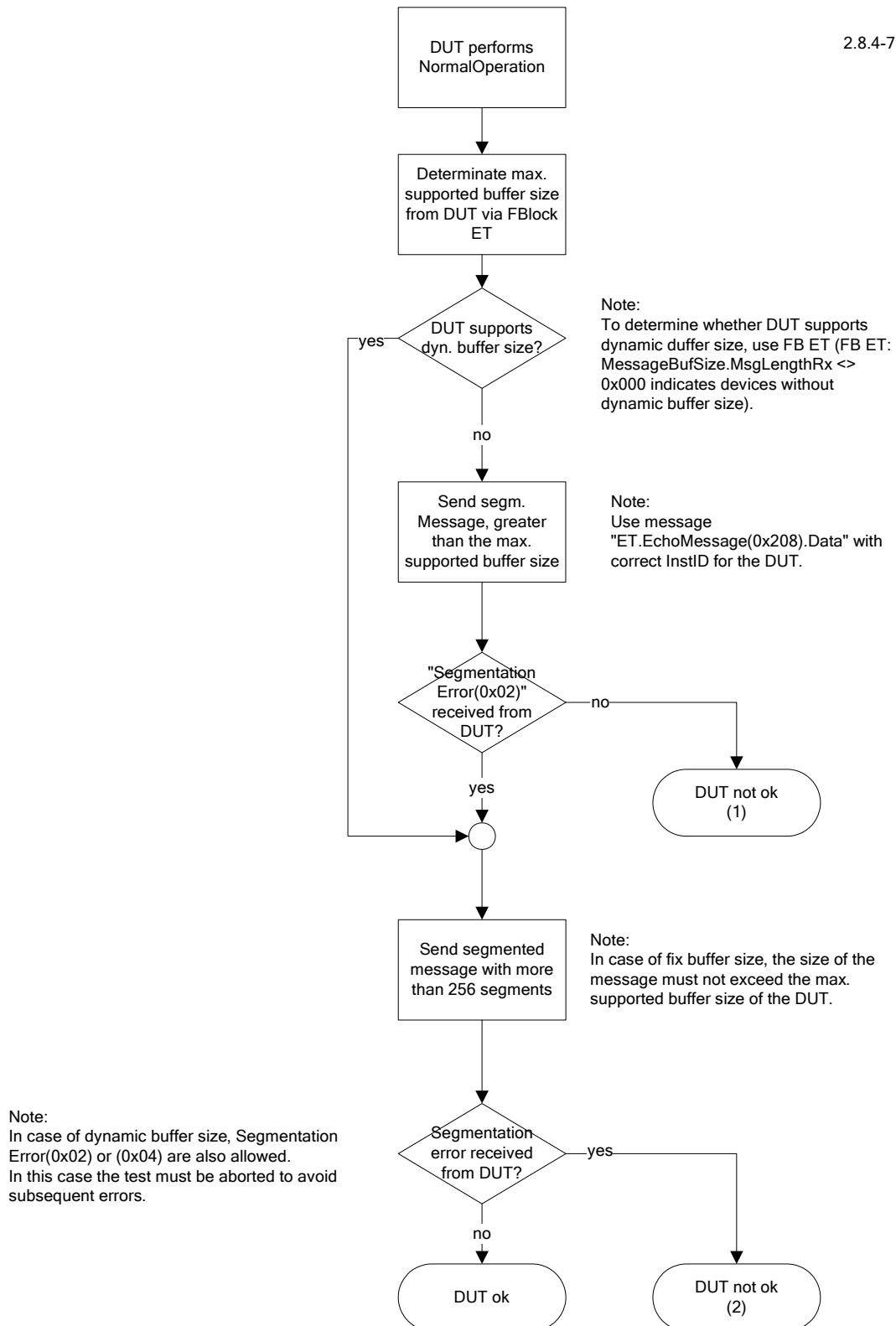


Figure 3-64 Flow of Message Segmentation Buffer test.

3.11.3.5 Parallel Segmented Message Reception test (2.8.4-8)

Name of test	Parallel Segmented Message Reception test 2.8.4-8
Reference to MOST Specification	Rev. 2.4: 3.4.7.2 Application Message Service (AMS) and Application Protocols Rev. 2.5: 3.4.6.2 Application Message Service (AMS) and Application Protocols
Value of Interest	Pool overflow error
Start Conditions	DUT in NormalOperation Ring closed
Test description	The tester determines the max. number of simultaneous segmented messages that are supported by the DUT (e.g. by means of the "FBlock ET"). The tester sends as much first parts of segmented messages ("NetBlock.FBlockIDs.Get") as supported by the DUT (every message is sent by a unique ID). The DUT must not return any error. If the tester sends one more message, the DUT has to return a "pool overflow" error (ErrorCode 0x0C, ErrorInfo 0x04). The whole test has to be finished before the garbage collection is active.
Experimental set-up	#1 - Tester 1 in master mode or slave mode (depends on DUT)
Device type	All devices that support message segmentation [x] MOST25 [x] MOST50 oPHY [x] MOST50 ePHY
Multi Node Device	All devices with at least one message segmentation supporting node
Note	Test only applicable for devices without dynamic parallel message buffers (FBlock ET: MessageBufSize.BufferCountRx <> 0x000)
Results	DUT ok: The DUT has passed the test. DUT not ok (1): DUT fails to handle enough segmented message simultaneously. DUT not ok (2): DUT fails to send "pool overflow" error (ErrorInfo 0x04).

Note:
The whole test has to be performed, before
the Garbage Collection of the DUT gets
active.

2.8.4-8

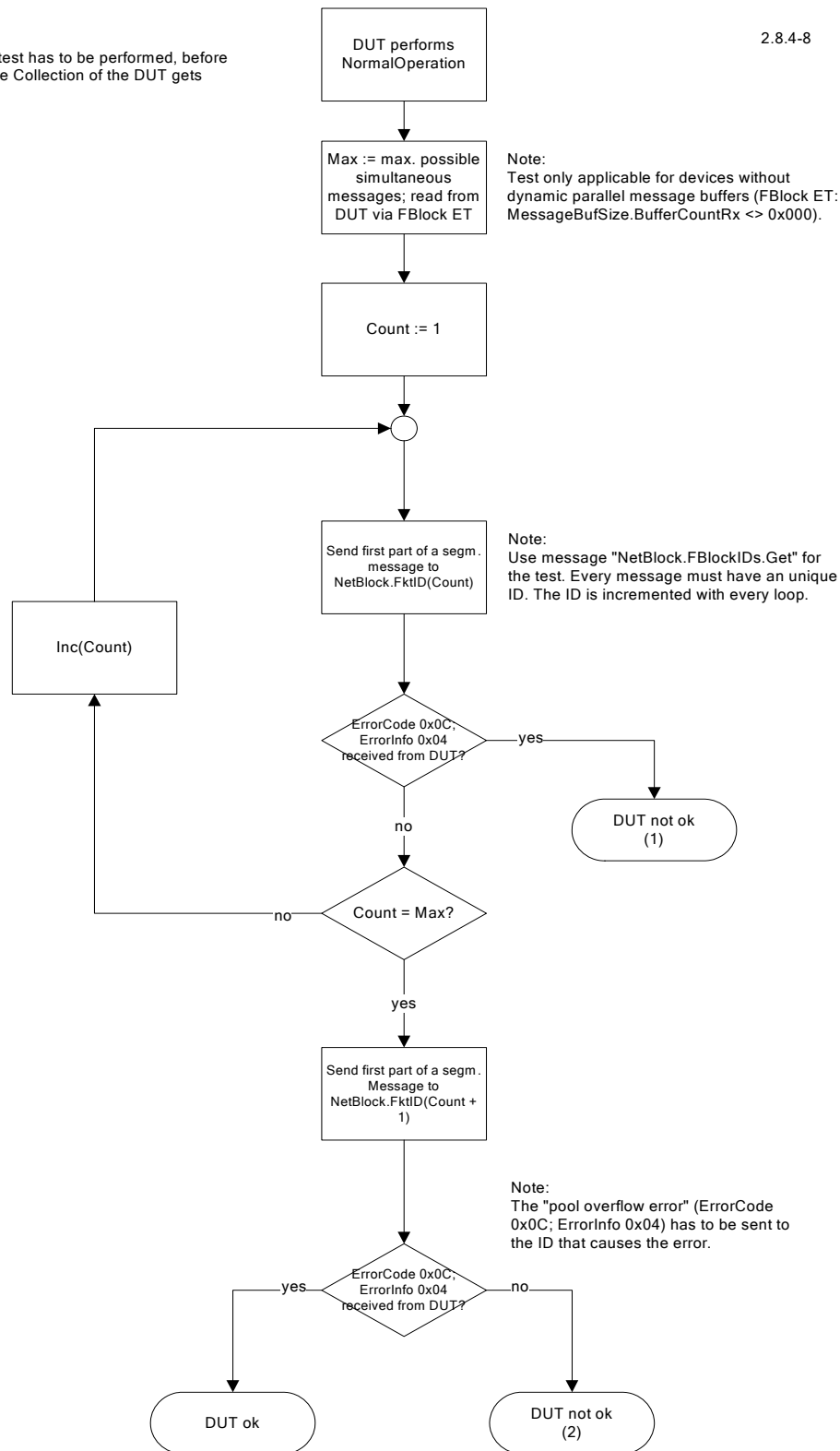


Figure 3-65 Flow of Parallel Segmented Message Reception test.

3.12 Source / Sink Identification

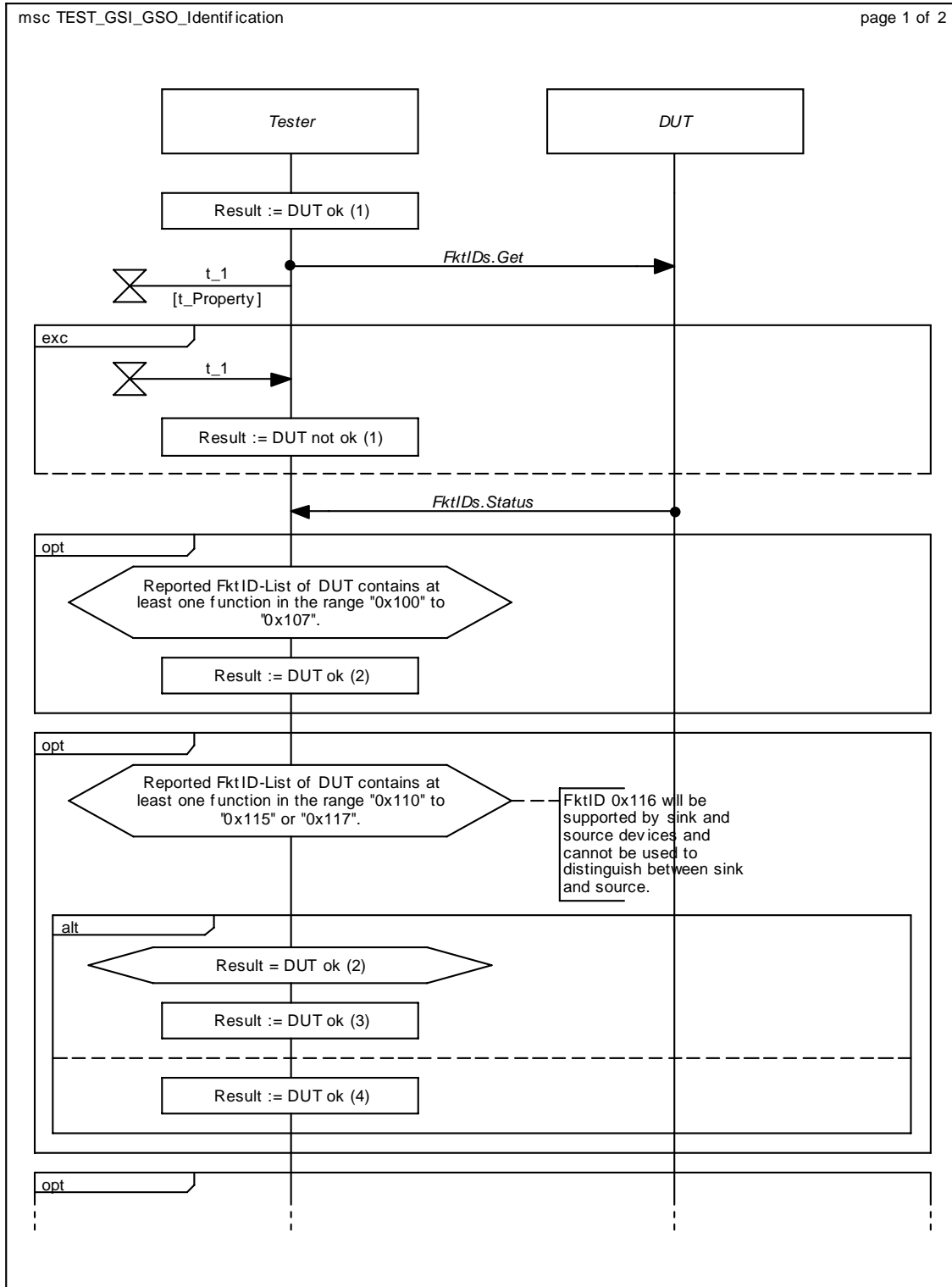
3.12.1.1 TEST_GSI_GSO_Identification (3.0-1)

Name of test	TEST_GSI_GSO_Identification 3.0-1
Reference to MOST Specification	Rev. 2.4: [GFB] para 2.1 Rev. 2.5: [GFB] 2.1 GeneralFBlock
Value of Interest	t_Property Reported FktIDs
Preconditions	DUT: NormalOperation
Test focus	The tester tries to identify FBlocks of the DUT that contain sink or source functionalities.
Device type	All devices [x] MOST25 [x] MOST50 oPHY [x] MOST50 ePHY
Multi Node Devices	All devices
Note	<p>The test has to be performed to identify whether the DUT contains sink or source.</p> <p>Test has to be performed with every single FBlock, reported by the DUT by means of FBlockID.Status.</p> <p>The manufacturer has to provide list with all FBlocks of the DUT, containing sink and / or source functionality.</p> <p>DUT with result "DUT ok (1)" must not run through any source or sink related test</p> <p>DUT with result "DUT ok (2)" has to run through all source related tests (relevant FBlocks only)</p> <p>DUT with result "DUT ok (3)" has to run through all source and sink related tests (relevant FBlocks only)</p> <p>DUT with result "DUT ok (4)" has to run through all sink related tests (relevant FBlocks only)</p>
Results	<p>DUT ok (1): FBlock neither contains sink nor source</p> <p>DUT ok (2): FBlock contains source</p> <p>DUT ok (3): FBlock contains source and sink</p> <p>DUT ok (4): FBlock contains sink</p> <p>DUT not ok (1): DUT fails to send FktIDs.Status</p> <p>DUT not ok (2): FBlocks of DUT, containing sinks / sources do not match to manufacturer list</p> <p>Exception: If DUT reports sink / source relevant FktIDs, the manufacturer has to indicate that the relevant FBlock contains sink /source (and vice versa) with the following exception: In case functions are only implemented in principle and source resp. sink count is zero the result is set correspondingly</p> <p>DUT ok (1): FBlock neither contains sink nor source according to manufacturer info or</p> <p>DUT ok (2): FBlock contains source according to manufacturer info or</p> <p>DUT ok (4): FBlock contains sink according to manufacturer info</p>

CORE_GSI_GSO

msc TEST_GSI_GSO_Identification

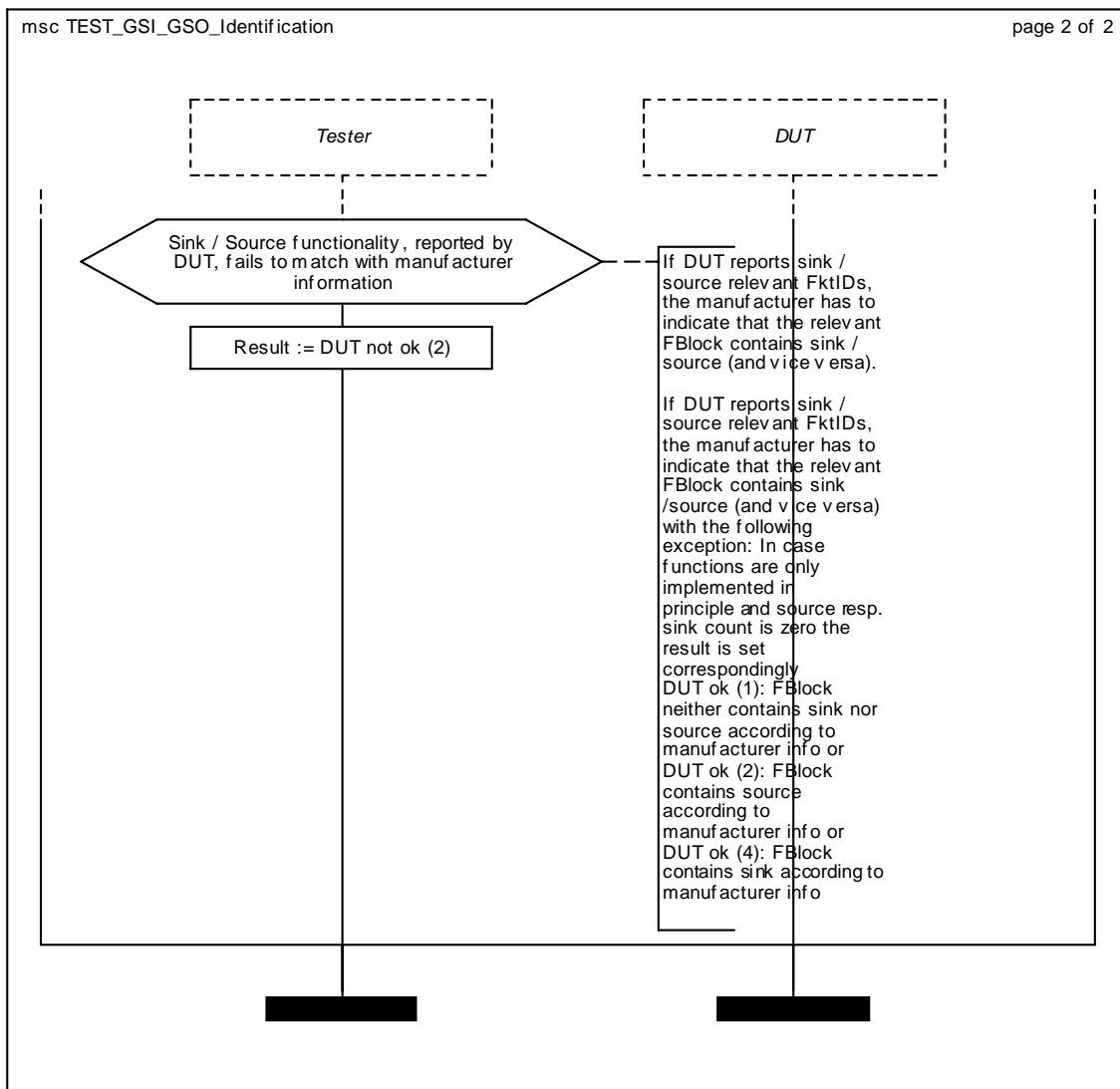
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CORE_GSI_GSO

msc TEST_GSI_GSO_Identification

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3.13 Obligatory tests for sink and source devices

3.13.1 Sink Devices

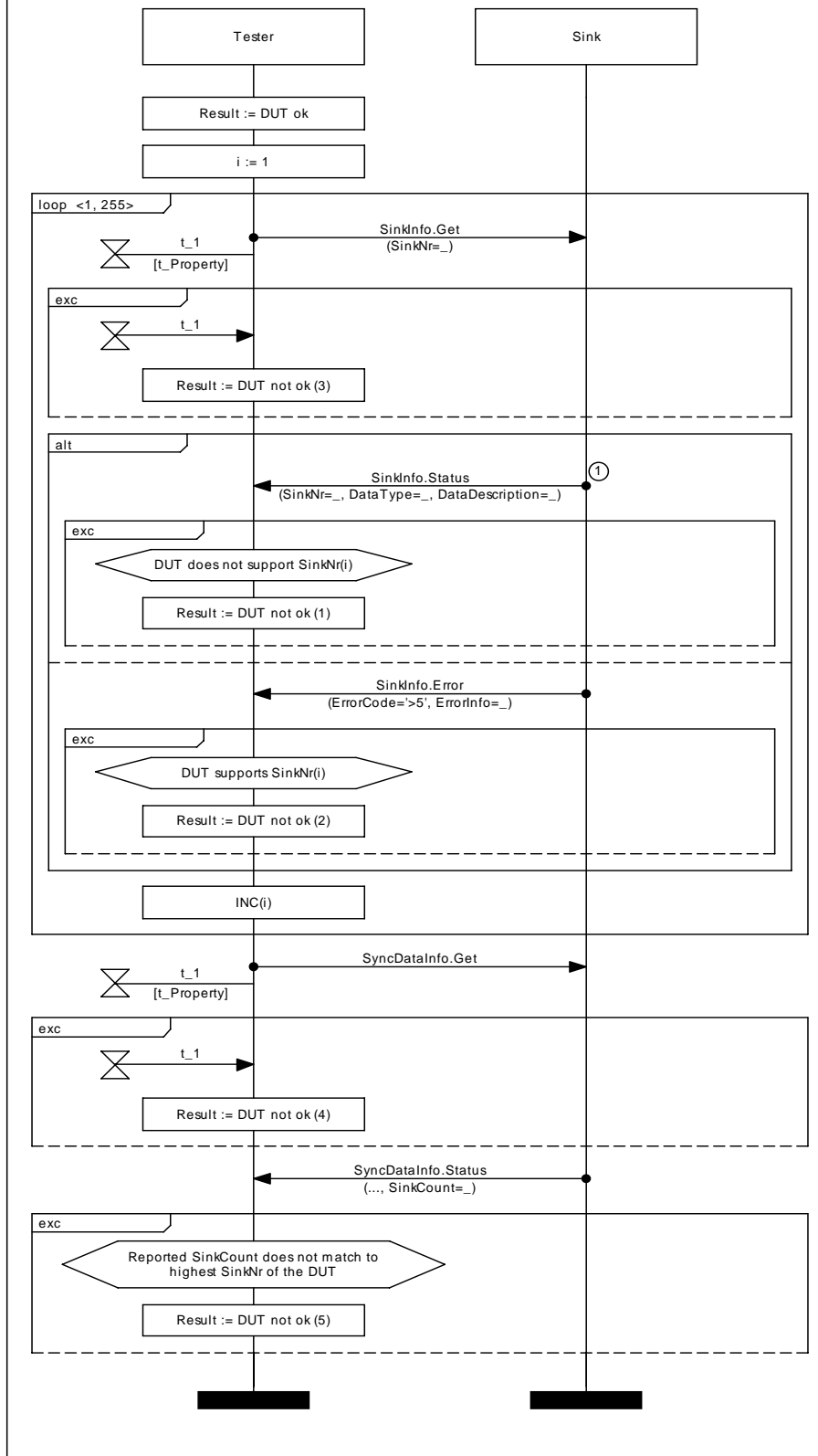
3.13.1.1 TEST_GSI_SinkInfo (3.1-1)

Name of test	TEST_GSI_SinkInfo 3.1-1
Reference to MOST Specification	Rev. 2.4: [MS] para 3.5.2.2.2 [GFB] para 2.1.19 Rev. 2.5: [MS] para 3.5.2.2.2 [GFB] 2.1.25 SinkInfo [GFB] 2.1.31 SyncDataInfo
Value of Interest	SinkInfo; SyncDataInfo
Preconditions	DUT: NormalOperation
Test focus	DUT has to respond to SinkInfo.Get correct status information (or Error, if SinkNr not supported). DUT has to respond to SyncDataInfo.Get with correct status information.
Device type	All sink devices [x] MOST25 [x] MOST50 oPHY [x] MOST50 ePHY
Multi Node Devices	All devices containing at least one sink
Note	Test has to be performed with every single SinkNumber (1..255). If DUT supports sink number (input by device manufacturer), the DUT has to respond the correct DataType and DataDescription (both input by device manufacturer). If DUT does not support sink number, it has to respond with error.
Results	DUT ok (1): The DUT has passed the test DUT not ok (1): The DUT responds status although SinkNr not supported DUT not ok (2): The DUT responds with Error although SinkNr supported DUT not ok (3): The DUT fails to respond within t_Property to SinkInfo.Get DUT not ok (4): The DUT fails to respond within t_Property to SyncDataInfo.Get DUT not ok (5): The DUT responds wrong status information with SyncDataInfo

CORE_GSI_GSO

msc TEST_GSI_SinkInfo

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1 DataType and DataDescription has to match with manufacturer information.

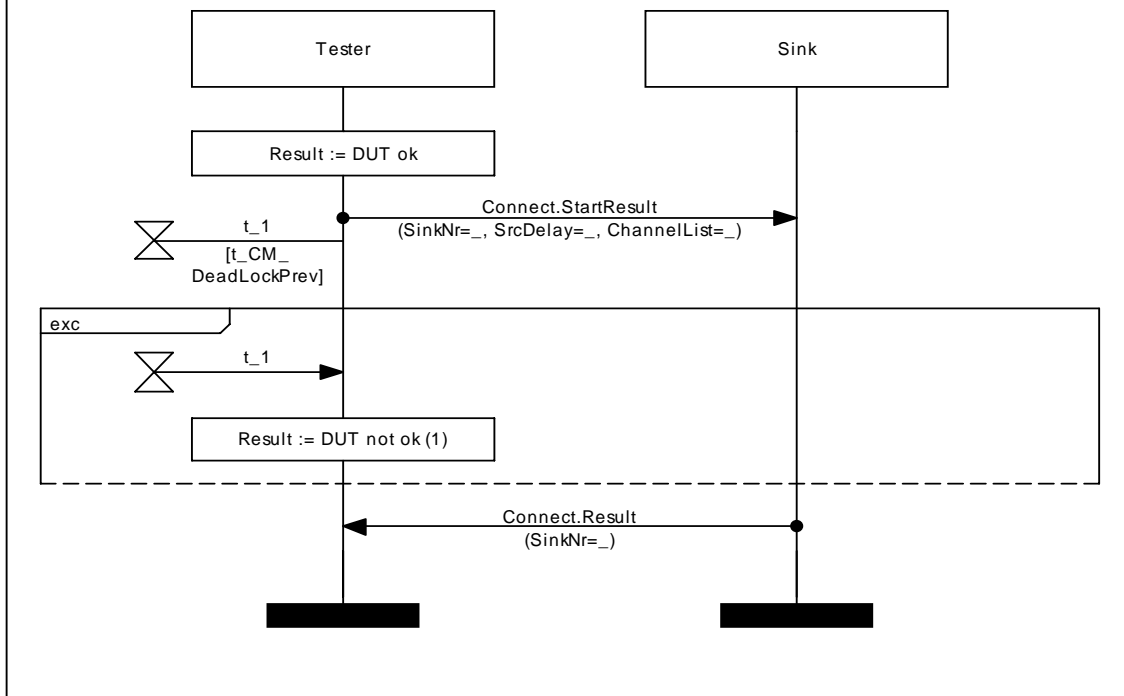
3.13.1.2 TEST_GSI_Connect (3.1-3)

Name of test	TEST_GSI_Connect 3.1-3
Reference to MOST Specification	Rev. 2.4: [MDS] para 4.8.1 Rev. 2.5: [MDS] 4.8.1 Connect
Value of Interest	t_CM_DeadLockPrev Connect.StartResult
Preconditions	DUT: NormalOperation
Test focus	DUT has to process Connect.StartResult correctly
Device type	All sink devices [x] MOST25 [x] MOST50 oPHY [x] MOST50 ePHY
Multi Node Devices	All devices containing at least one sink
Note	<ul style="list-style-type: none"> - Test only applicable if DUT does not contain CM - Test to be performed with every single SinkNumber, supported by the DUT but Disconnect before in order to avoid resource overflow. - The tester has to ensure that the channels to which a sink has to connect to are in use by a source. (Reason: In some test cases the tester sends the message "Connect.StartResult" with a ChannelList to the DUT (sink device) to trigger the DUT to connect to the channel. Practice has shown that some sink devices check the usage of the channels they have to connect to before performing the connection. Only if the TM indicates the channels as allocated by a source, the sink device will connect. This behaviour is neither prescribed nor forbidden by the MOST Specification and has to be tolerated. - Timeout "t_CM_DeadLockPrev" is valid for whole MSC. - In cases where the bandwidth of sink cannot be determined because of the data type, the test case is skipped for this sink.
Results	DUT ok: The DUT has passed the test DUT not ok (1): The DUT fails to respond within t_CM_DeadLockPrev

CORE_GSI_GSO

msc TEST_GSI_Connect

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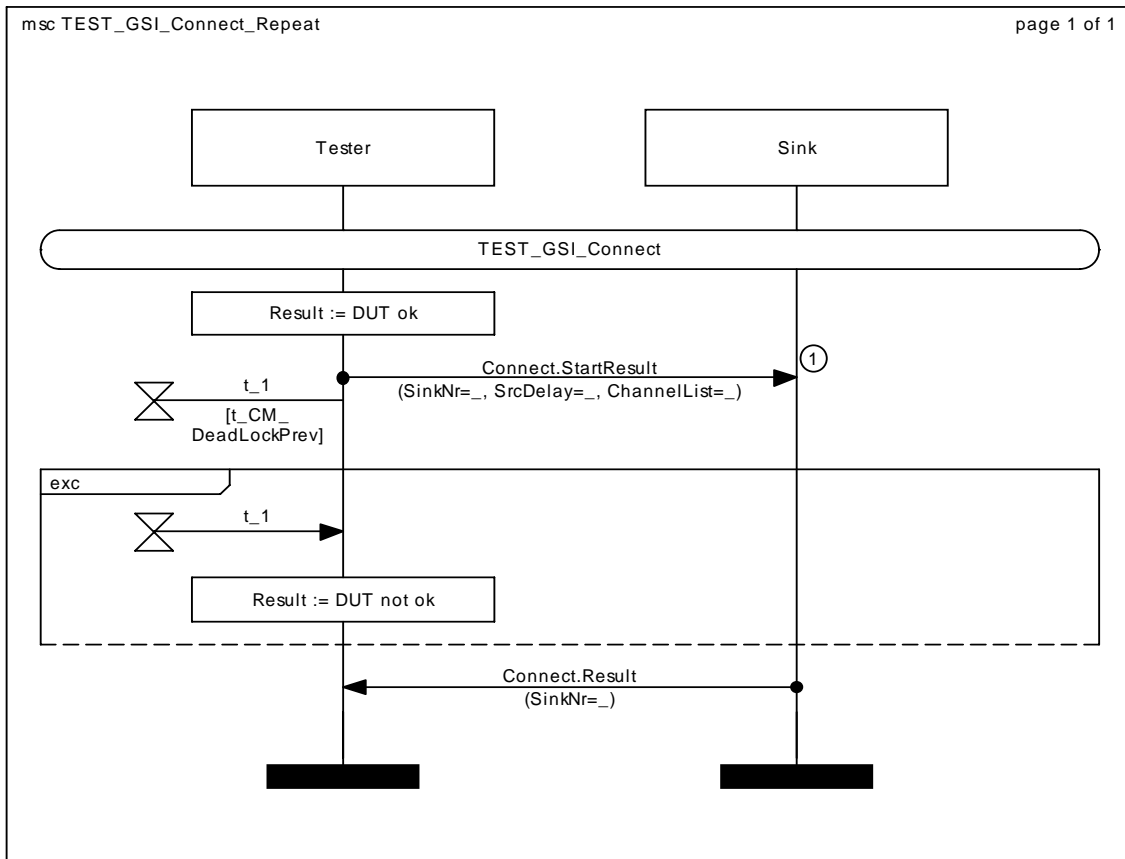
3.13.1.3 TEST_GSI_Connect_Repeat (3.1-4)

Name of test	TEST_GSI_Connect_Repeat 3.1-4
Reference to MOST Specification	Rev. 2.4: [MDS] para 4.8.1 [MS] para 3.5.2.2.3 Rev. 2.5: [MDS] 4.8.1 Connect [MS] 3.5.2.2.3 Handling of Double Commands
Value of Interest	t_CM_DeadLockPrev Connect.StartResult
Preconditions	DUT: NormalOperation
Test focus	DUT has to process repeated Connect.StartResult correctly
Device type	All sink devices [x] MOST25 [x] MOST50 oPHY [x] MOST50 ePHY
Multi Node Devices	All devices containing at least one sink
Note	<ul style="list-style-type: none"> - Test only applicable if DUT does not contain CM - Test to be performed with every single SinkNumber, supported by the DUT but Disconnect before in order to avoid resource overflow. - Timeout "t_CM_DeadLockPrev" is valid for whole MSC. - In cases where the bandwidth of sink cannot be determined because of the data type, the test case is skipped for this sink.
Results	DUT ok: The DUT has passed the test DUT not ok: The DUT fails to respond within t_CM_DeadLockPrev

CORE_GSI_GSO

msc TEST_GSI_Connect_Repeat

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- 1 Use same parameters (including SinkNr, SrcDelay and ChannelList) as for the first connection

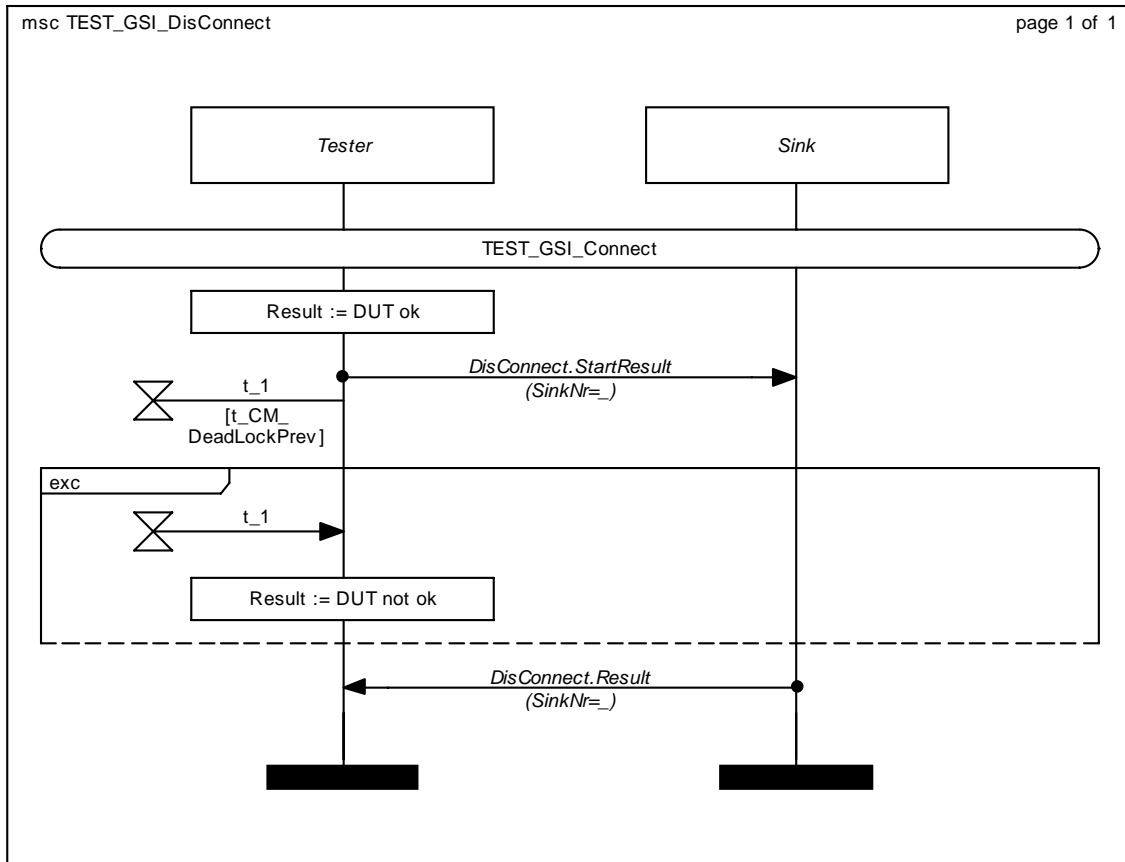
3.13.1.4 TEST_GSI_DisConnect (3.1-5)

Name of test	TEST_GSI_DisConnect 3.1-5
Reference to MOST Specification	Rev. 2.4: [MDS] para 4.8.2 Rev. 2.5: [MDS] 4.8.2 Disconnect
Value of Interest	t_CM_DeadLockPrev DisConnect.StartResult
Preconditions	- DUT: NormalOperation - DUT already connected to a source
Test focus	DUT has to disconnect correctly
Device type	All sink devices [x] MOST25 [x] MOST50 oPHY [x] MOST50 ePHY
Multi Node Devices	All devices containing at least one sink
Note	- Test only applicable if DUT does not contain CM - Test to be performed with every single SinkNumber, supported by the DUT. - Timeout "t_CM_DeadLockPrev" is valid for whole MSC. - In cases where the bandwidth of sink cannot be determined because of the data type, the test case is skipped for this sink.
Results	DUT ok: The DUT has passed the test DUT not ok: The DUT fails to respond to DisConnect.StartResult within t_CM_DeadLockPrev

CORE_GSI_GSO

msc TEST_GSI_DisConnect

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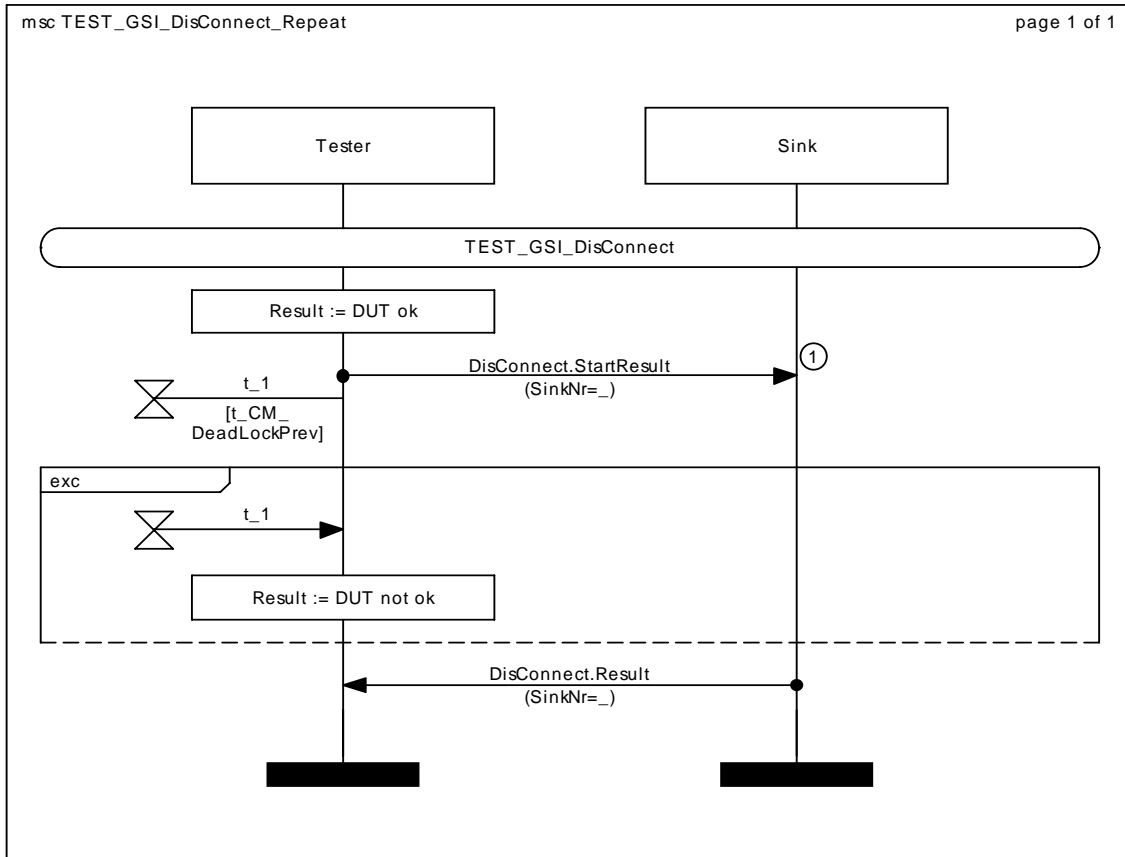
3.13.1.5 TEST_GSI_DisConnect_Repeat (3.1-6)

Name of test	TEST_GSI_DisConnect_Repeat 3.1-6
Reference to MOST Specification	Rev. 2.4: [MDS] para 4.8.2 [MS] para 3.5.2.2.3 Rev. 2.5: [MDS] 4.8.2 Disconnect [MS] 3.5.2.2.3 Handling of Double Commands
Value of Interest	t_CM_DeadLockPrev DisConnect.StartResult
Preconditions	- DUT: NormalOperation - DUT already connected to a source
Test focus	DUT must not generate any error in case of repeated disconnect command
Device type	All sink devices [x] MOST25 [x] MOST50 oPHY [x] MOST50 ePHY
Multi Node Device	All devices containing at least one sink
Note	- Test only applicable if DUT does not contain CM - Test to be performed with every single SinkNumber, supported by the DUT. - Timeout "t_CM_DeadLockPrev" is valid for whole MSC. - In cases where the bandwidth of sink cannot be determined because of the data type, the test case is skipped for this sink.
Results	DUT ok: The DUT has passed the test DUT not ok: The DUT fails to respond to repeated DisConnect.StartResult within t_CM_DeadLockPrev

CORE_GSI_GSO

msc TEST_GSI_DisConnect_Repeat

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1 Use same SinkNr as for the first disconnection

3.13.2 Source Devices

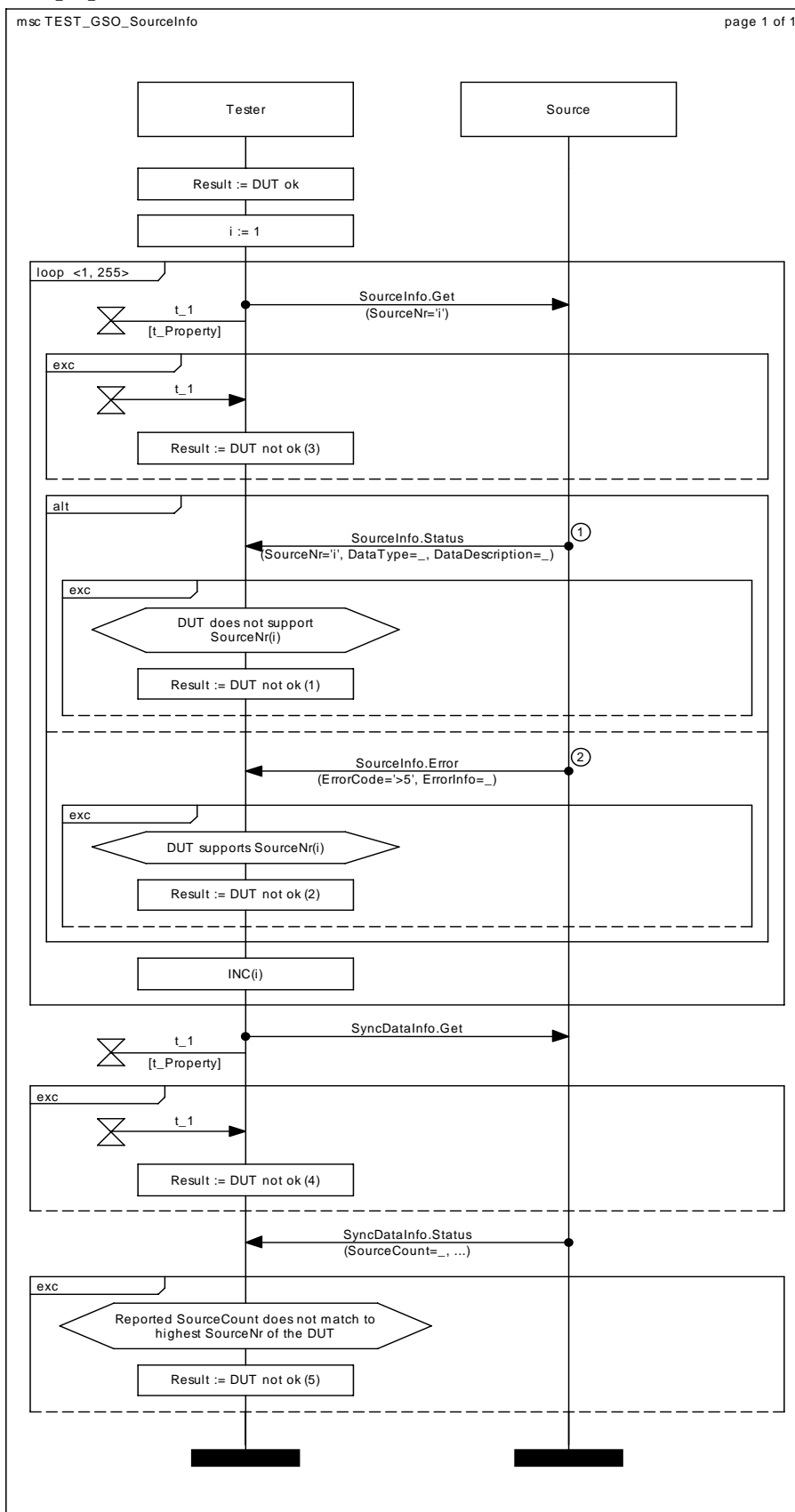
3.13.2.1 TEST_GSO_SourceInfo (3.2-1)

Name of test	TEST_GSO_SourceInfo 3.2-1
Reference to MOST Specification	Rev. 2.4: [MS] para 3.5.2.2.1 [MDS] para 4.4.1.1.1 Rev. 2.5: [MS] 3.5.2.2.1 Streaming Source [MDS] 4.4.1.1.1 Retrieving SourceInfo
Value of Interest	t_Property SourceInfo.Status SyncDataInfo.Status
Preconditions	- DUT: NormalOperation
Test focus	DUT has to respond correct information to SourceInfo.Get. DUT has to respond to SyncDataInfo.Get with correct status information.
Device type	All source devices [x] MOST25 [x] MOST50 oPHY [x] MOST50 ePHY
Multi Node Device	All devices containing at least one source
Note	Test has to be performed with every single SourceNumber (1..255). If DUT supports source number (input by device manufacturer), the DUT has to respond the correct DataType and DataDescription (both input by device manufacturer). If DUT does not support source number, it has to respond with error.
Results	DUT ok: The DUT has passed the test DUT not ok (1): The DUT responds SourceInfo.Status although source info not supported. DUT not ok (2): The DUT responds with error. DUT not ok (3): The DUT fails to respond within t_Property DUT not ok (4): The DUT fails to respond within t_Property to SyncDataInfo.Get DUT not ok (5): The DUT responds wrong status information with SyncDataInfo

CORE_GSI_GSO

msc TEST_GSO_SourceInfo

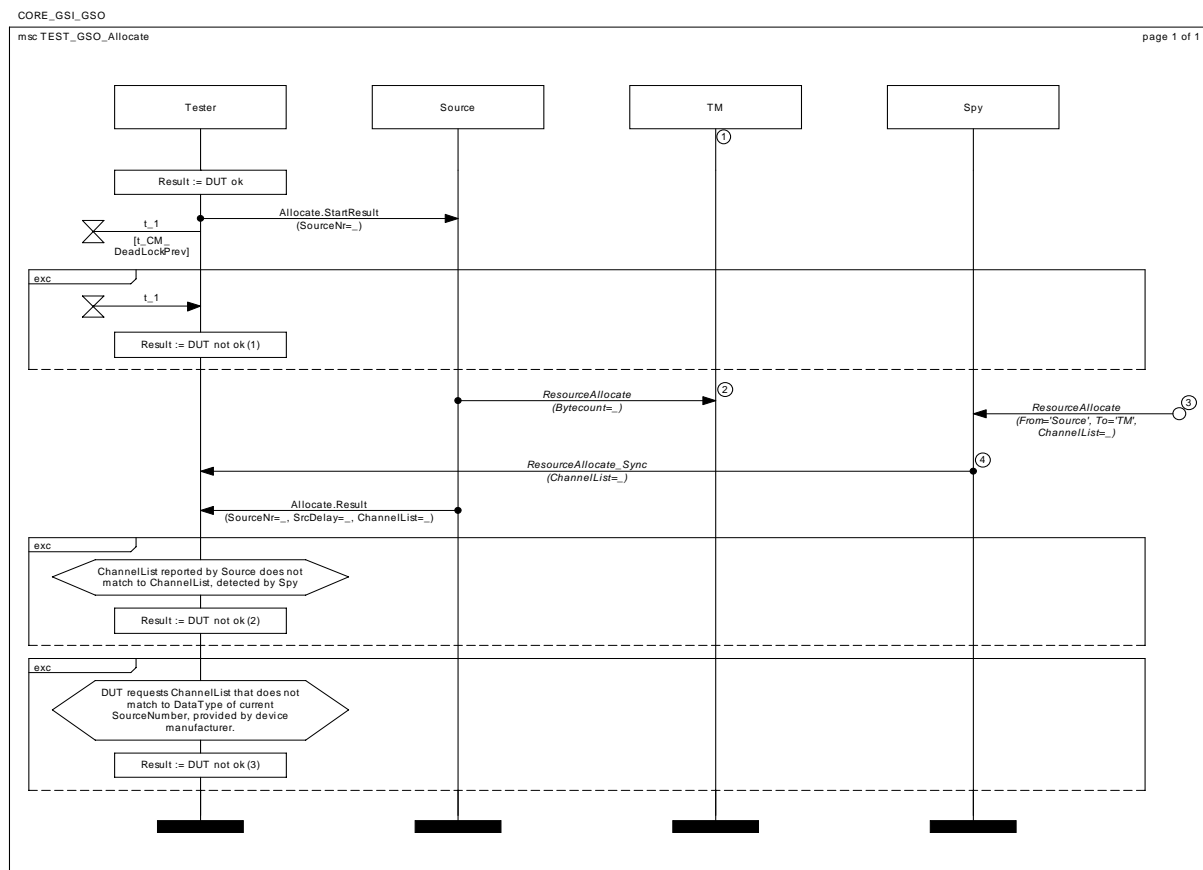
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1 DataType and DataDescription has to match with manufacturer information.

3.13.2.2 TEST_GSO_Allocate (3.2-3)

Name of test	TEST_GSO_Allocate 3.2-3
Reference to MOST Specification	Rev. 2.4: [MDS] para 4.9.1 Rev. 2.5: [MDS] 4.9.1 Allocate
Value of Interest	t_CM_DeadLockPrev Allocate.Result
Preconditions	- DUT: NormalOperation
Test focus	DUT has to process Allocate.StartResult correctly
Device type	All source devices, supporting allocate mechanism [x] MOST25 [x] MOST50 oPHY [x] MOST50 ePHY
Multi Node Device	All devices containing at least one source that supports allocate mechanism
Note	<ul style="list-style-type: none"> - Test only applicable if DUT does not contain CM - Test to be performed with every single SourceNumber, supported by the DUT. - ChannelList of DUT has to be provided by device manufacturer - Timeout "t_CM_DeadLockPrev" is valid for whole MSC. <p>Bandwidth constraints must be considered when beginning with next source number.</p> <p>For MOST25 devices, use MSC "TEST_GSO_Allocate". For MOST50 devices, use MSC "TEST_GSO_Allocate_MOST50".</p> <p>Test result "DUT not ok (2)" only applicable for MOST25 devices.</p> <p>Note to "Spy/Resource(De)Allocate with Grantinfo + ChannelList for DUT = TM": In some cases the Spy device is not able to evaluate ChannelList, Status and Label from the ResourceAllocate message of DUT (e.g. DUT = TM). In that case the Spy and the Tester has to assume correct values to ensure the DUT does not fail the test due to technical restrictions of test setup. The test result has to be marked accordingly. Nevertheless, the DUT will fail the test if it fails to send any Resource(De)Allocate message to TM.</p> <p>Note to monitoring ResourceAllocate resp. ResourceDeAllocate: ResourceAllocates resp. ResourceDeAllocates can occur several times. The evaluation/check must regard all Resource(De)Allocates, not just the first one.</p>
Results	<p>DUT ok: The DUT has passed the test</p> <p>DUT not ok (1): The DUT fails to respond within t_CM_DeadLockPrev</p> <p>DUT not ok (2): The DUT responds wrong ChannelList.</p> <p>DUT not ok (3): Requested ChannelList of DUT does not match to information of DataType of manufacturer.</p>

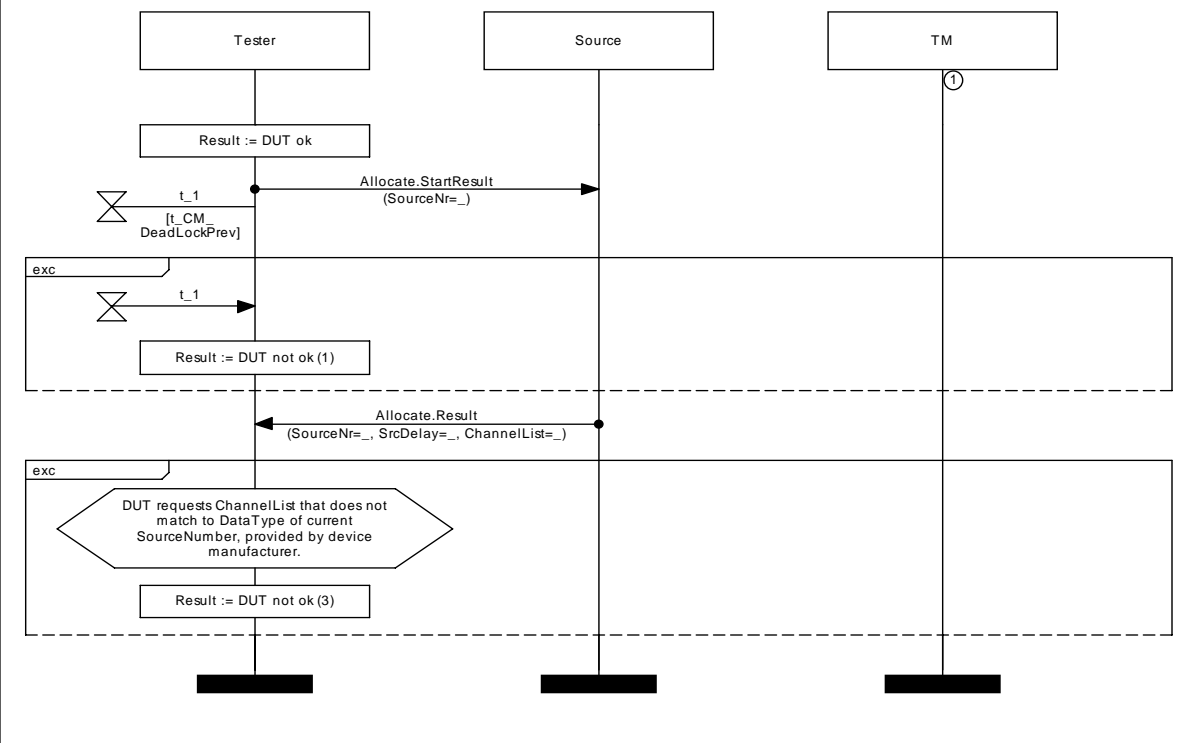


- 1 TM could be located in DUT or in Tester.
- 2 Message monitored by Spy-Device .
Additionally, the Spy has to monitor the respond of the TM to the Source (same control frame as ResourceAllocate).
From this respond, the Spy is able to get information about ChannelList.
- 3 Message "ResourceAllocate" from DUT to TM monitored by Spy-Device .
Additionally, the Spy has to monitor the respond of the TM to the Source (same control frame as ResourceAllocate).
From this respond, the Spy is able to get information about ChannelList.
- 4 Synchronisation between Spy and Tester can be performed by any kind of mechanism.

CORE_GSL_GSO

msc TEST_GSO_Allocate_MOST50

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1 TM could be located in DUT or in Tester.

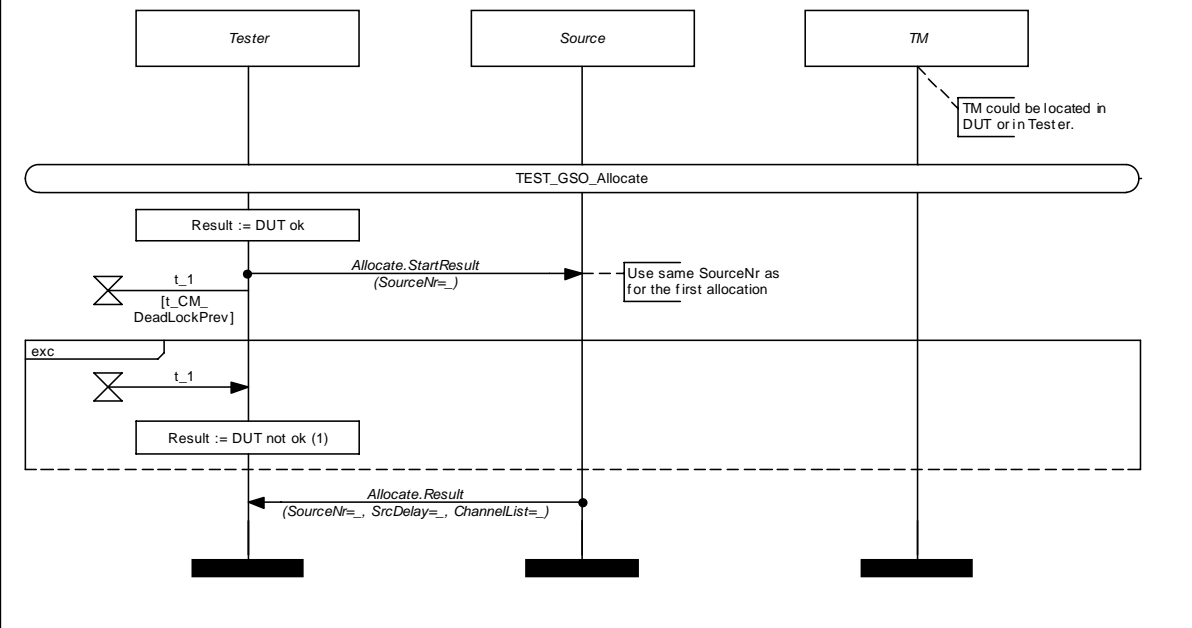
3.13.2.3 TEST_GSO_Allocate_Repeat (3.2-4)

Name of test	TEST_GSO_Allocate_Repeat 3.2-4
Reference to MOST Specification	Rev. 2.4: [MDS] para 4.9.1 [MS] para 3.5.2.2.3 Rev. 2.5: [MDS] 4.9.1 Allocate [MS] 3.5.2.2.3 Handling of Double Commands
Value of Interest	t_CM_DeadLockPrev Allocate.Result
Preconditions	- DUT: NormalOperation
Test focus	DUT has to process repeated Allocate.StartResult correctly. It has to respond status without new allocation of channels.
Device type	All source devices, supporting allocate mechanism [x] MOST25 [x] MOST50 oPHY [x] MOST50 ePHY
Multi Node Device	All devices containing at least one source that supports allocate mechanism
Note	<ul style="list-style-type: none"> - Test only applicable if DUT does not contain CM - Test to be performed with every single SourceNumber, supported by the DUT. - Timeout "t_CM_DeadLockPrev" is valid for whole MSC. <p>Note to "Spy/Resource(De)Allocate with Grantinfo + ChannelList for DUT = TM": In some cases the Spy device is not able to evaluate ChannelList, Status and Label from the ResourceAllocate message of DUT (e.g. DUT = TM). In that case the Spy and the Tester has to assume correct values to ensure the DUT does not fail the test due to technical restrictions of test setup. The test result has to be marked accordingly. Nevertheless, the DUT will fail the test if it fails to send any Resource(De)Allocate message to TM.</p>
Results	DUT ok: The DUT has passed the test DUT not ok (1): The DUT fails to respond within t_CM_DeadLockPrev

CORE_GSL_GSO

msc TEST_GSO_Allocate_Repeat

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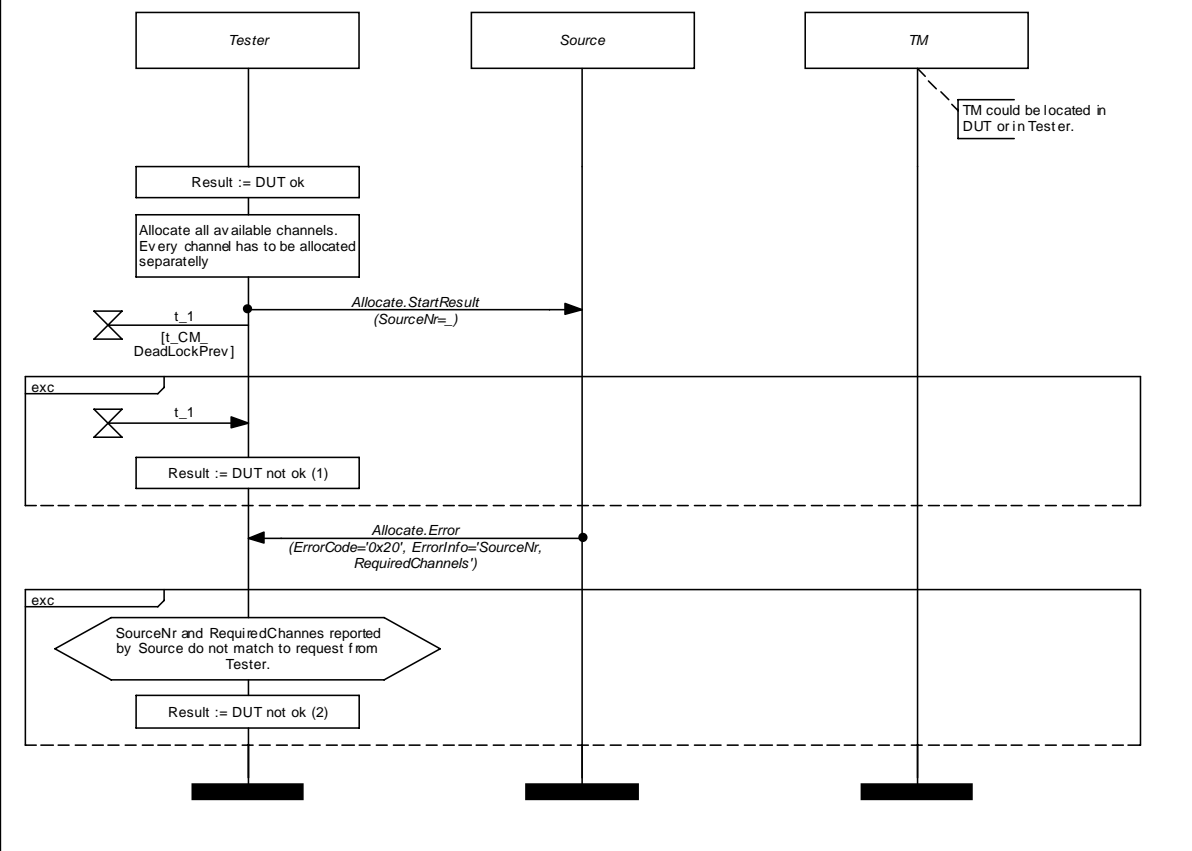
3.13.2.4 TEST_GSO_Allocate_NoChAvail (3.2-5)

Name of test	TEST_GSO_Allocate_NoChAvail 3.2-5
Reference to MOST Specification	Rev. 2.4: [MDS] para 4.9.1 [MS] para 3.5.2.2.1 Rev. 2.5: [MDS] 4.9.1 Allocate [MS] 3.5.2.2.1 Streaming Source
Value of Interest	t_CM_DeadLockPrev Allocate.Error
Preconditions	- DUT: NormalOperation
Test focus	DUT has to detect no available channels during allocation and has to report correct error message.
Device type	All source devices, supporting allocate mechanism [x] MOST25 [x] MOST50 oPHY [x] MOST50 ePHY
Multi Node Device	All devices containing at least one source that supports allocate mechanism
Note	<ul style="list-style-type: none"> - Test only applicable if DUT does not contain CM - Test to be performed with every single SourceNumber, supported by the DUT. - Timeout "t_CM_DeadLockPrev" is valid for whole MSC. <p>Note to "Spy/Resource(De)Allocate with Grantinfo + ChannelList for DUT = TM": In some cases the Spy device is not able to evaluate ChannelList, Status and Label from the ResourceAllocate message of DUT (e.g. DUT = TM). In that case the Spy and the Tester has to assume correct values to ensure the DUT does not fail the test due to technical restrictions of test setup. The test result has to be marked accordingly. Nevertheless, the DUT will fail the test if it fails to send any Resource(De)Allocate message to TM.</p>
Results	<p>DUT ok: The DUT has passed the test</p> <p>DUT not ok (1): The DUT fails to respond within t_CM_DeadLockPrev</p> <p>DUT not ok (2): SourceNr and RequiredChannes reported by Source do not match to request from Tester.</p>

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msc TEST_GSO_Allocate_NoChAvail

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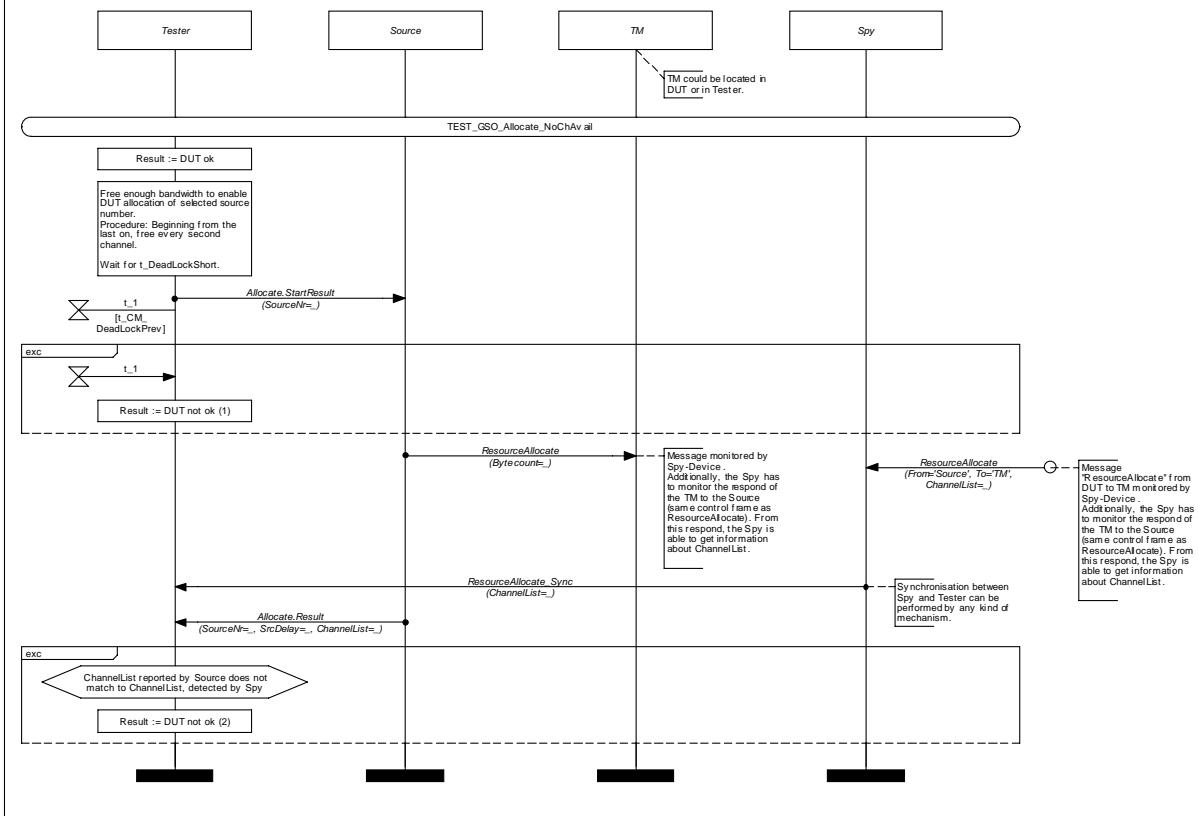
3.13.2.5 TEST_GSO_Allocate_ReqChAvail (3.2-6)

Name of test	TEST_GSO_Allocate_ReqChAvail 3.2-6
Reference to MOST Specification	Rev. 2.4: [MDS] para 4.9.1 [MS] para 3.5.2.2.1 Rev. 2.5: [MDS] 4.9.1 Allocate [MS] 3.5.2.2.1 Streaming Source
Value of Interest	t_CM_DeadLockPrev Allocate.Result
Preconditions	- DUT: NormalOperation
Test focus	DUT has to be able to allocate channels if exactly required bandwidth is available, even if available channels are fragmented..
Device type	All source devices, supporting allocate mechanism [x] MOST25 [x] MOST50 oPHY [x] MOST50 ePHY
Multi Node Device	All devices containing at least one source that supports allocate mechanism
Note	<ul style="list-style-type: none"> - Test only applicable if DUT does not contain CM - Test to be performed with every single SourceNumber, supported by the DUT. - Timeout "t_CM_DeadLockPrev" is valid for whole MSC. <p>For MOST25 devices, use MSC "TEST_GSO_Allocate_ReqChAvail". For MOST50 devices, use MSC "TEST_GSO_Allocate_ReqChAvail_MOST50".</p> <p>Test result "DUT not ok (2)" only applicable for MOST25 devices.</p> <p>Note to "Spy/Resource(De)Allocate with Grantinfo + ChannelList for DUT = TM": In some cases the Spy device is not able to evaluate ChannelList, Status and Label from the ResourceAllocate message of DUT (e.g. DUT = TM). In that case the Spy and the Tester has to assume correct values to ensure the DUT does not fail the test due to technical restrictions of test setup. The test result has to be marked accordingly. Nevertheless, the DUT will fail the test if it fails to send any Resource(De)Allocate message to TM.</p> <p>Note to monitoring ResourceAllocate resp. ResourceDeAllocate: ResourceAllocates resp. ResourceDeAllocates can occur several times. The evaluation/check must regard all Resource(De)Allocates, not just the first one.</p>
Results	<p>DUT ok: The DUT has passed the test</p> <p>DUT not ok (1): The DUT fails to respond within t_CM_DeadLockPrev</p> <p>DUT not ok (2): SourceNr and RequiredChannes reported by Source do not match to request from Tester.</p>

CORE_GSI_GSO

msc TEST_GSO_Allocate_ReqChAvail

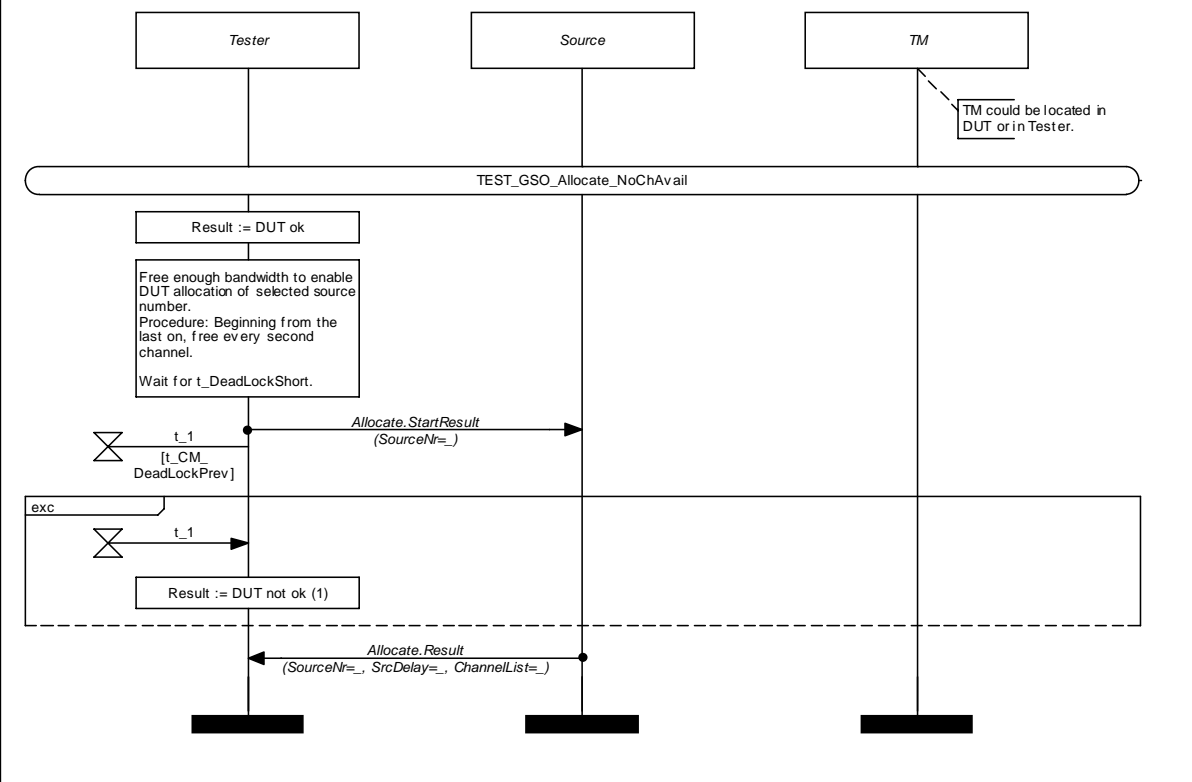
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CORE_GSL_GSO

msc TEST_GSO_Allocate_ReqChAvail_MOST50

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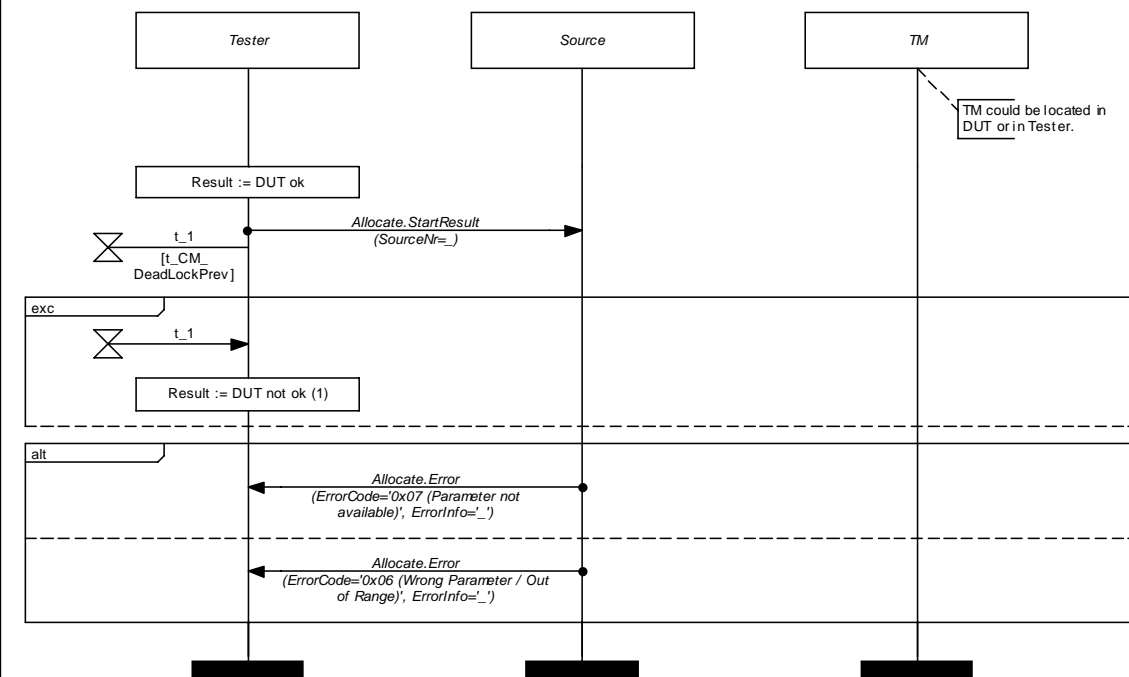
3.13.2.6 TEST_GSO_Allocate_WrongSourceNr (3.2-7)

Name of test	TEST_GSO_Allocate_WrongSourceNr 3.2-7
Reference to MOST Specification	Rev. 2.4: [MDS] para 4.9.1 [MS] para 3.5.2.2.1 Rev. 2.5: [MDS] 4.9.1 Allocate [MS] 3.5.2.2.1 Streaming Source
Value of Interest	t_CM_DeadLockPrev Allocate.Error
Preconditions	- DUT: NormalOperation
Test focus	DUT has to be able to detect wrong source number at allocation request and has to respond with error.
Device type	All source devices, supporting allocate mechanism [x] MOST25 [x] MOST50 oPHY [x] MOST50 ePHY
Multi Node Device	All devices containing at least one source that supports allocate mechanism
Note	<ul style="list-style-type: none"> - Test only applicable if DUT does not contain CM - Test to be performed with first SourceNumber, not supported by the DUT. - Timeout "t_CM_DeadLockPrev" is valid for whole MSC. <p>Note to "Spy/Resource(De)Allocate with Grantinfo + ChannelList for DUT = TM": In some cases the Spy device is not able to evaluate ChannelList, Status and Label from the ResourceAllocate message of DUT (e.g. DUT = TM). In that case the Spy and the Tester has to assume correct values to ensure the DUT does not fail the test due to technical restrictions of test setup. The test result has to be marked accordingly. Nevertheless, the DUT will fail the test if it fails to send any Resource(De)Allocate message to TM.</p>
Results	DUT ok: The DUT has passed the test DUT not ok (1): The DUT fails to respond within t_CM_DeadLockPrev

CORE_GSL_GSO

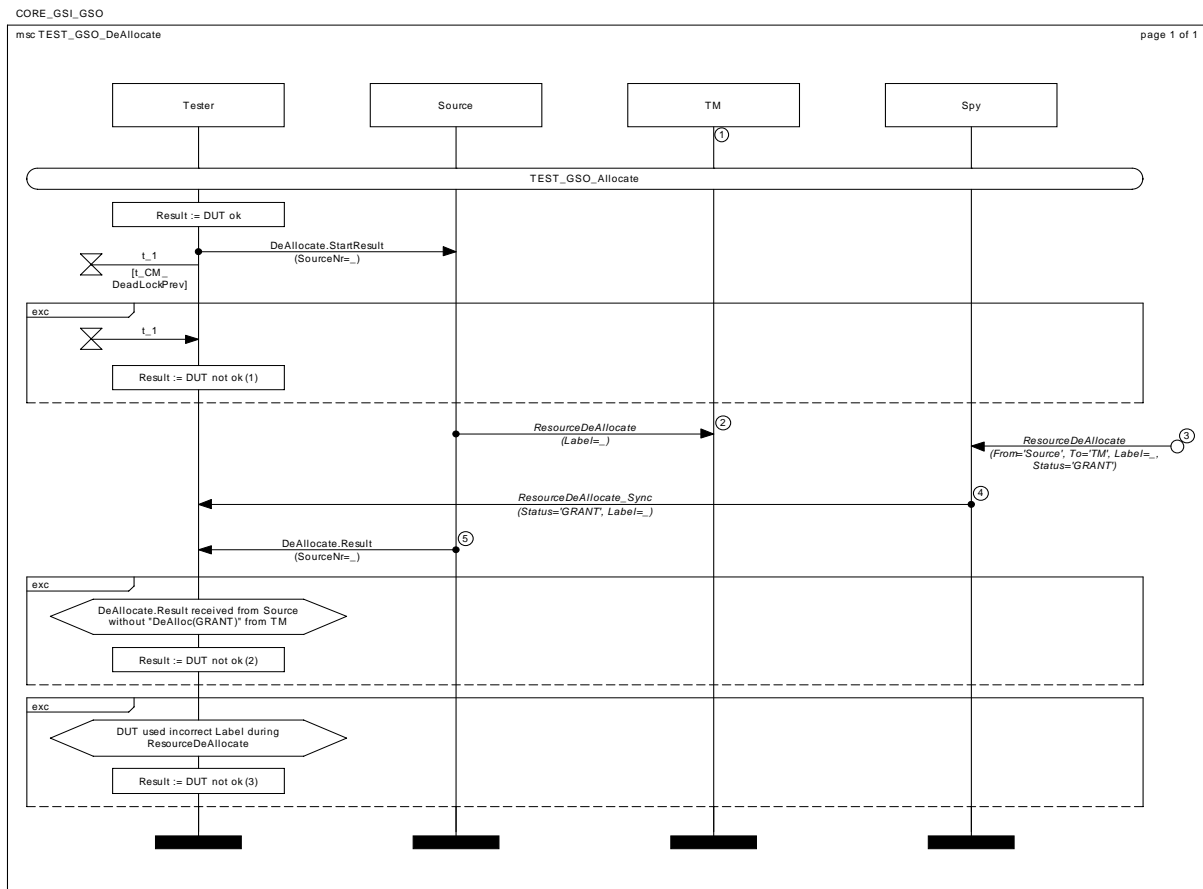
msc TEST_GSO_Allocate_WrongSourceNr

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3.13.2.7 TEST_GSO_DeAllocate (3.2-8)

Name of test	TEST_GSO_DeAllocate 3.2-8
Reference to MOST Specification	Rev. 2.4: [MDS] para 4.9.2 [MS] para 3.5.2.2.1 Rev. 2.5: [MDS] 4.9.2 Deallocate [MS] 3.5.2.2.1 Streaming Source
Value of Interest	t_CM_DeadLockPrev DeAllocate.Result
Preconditions	- DUT: NormalOperation
Test focus	DUT has to process DeAllocate.StartResult correctly
Device type	All source devices, supporting allocate mechanism [x] MOST25 [x] MOST50 oPHY [x] MOST50 ePHY
Multi Node Device	All devices containing at least one source that supports allocate mechanism
Note	<p>- Test only applicable if DUT does not contain CM. - Timeout "t_CM_DeadLockPrev" is valid for whole MSC.</p> <p>For MOST25 devices, use MSC "TEST_GSO_DeAllocate". For MOST50 devices, use MSC "TEST_GSO_DeAllocate_MOST50".</p> <p>Test result "DUT not ok (2)" only applicable for MOST25 devices.</p> <p>Note to "Spy/Resource(De)Allocate with Grantinfo + ChannelList for DUT = TM": In some cases the Spy device is not able to evaluate ChannelList, Status and Label from the ResourceAllocate message of DUT (e.g. DUT = TM). In that case the Spy and the Tester has to assume correct values to ensure the DUT does not fail the test due to technical restrictions of test setup. The test result has to be marked accordingly. Nevertheless, the DUT will fail the test if it fails to send any Resource(De)Allocate message to TM.</p> <p>Note to monitoring ResourceAllocate resp. ResourceDeAllocate: ResourceAllocates resp. ResourceDeAllocates can occur several times. The evaluation/check must regard all Resource(De)Allocates, not just the first one.</p>
Results	<p>DUT ok: The DUT has passed the test</p> <p>DUT not ok (1): The DUT fails to respond within t_CM_DeadLockPrev</p> <p>DUT not ok (2): The DUT responds DeAllocate.Result without getting "DeAlloc(GRANT)" from TM</p> <p>DUT not ok (3): The DUT uses wrong Label for ResourceDeAllocate</p>

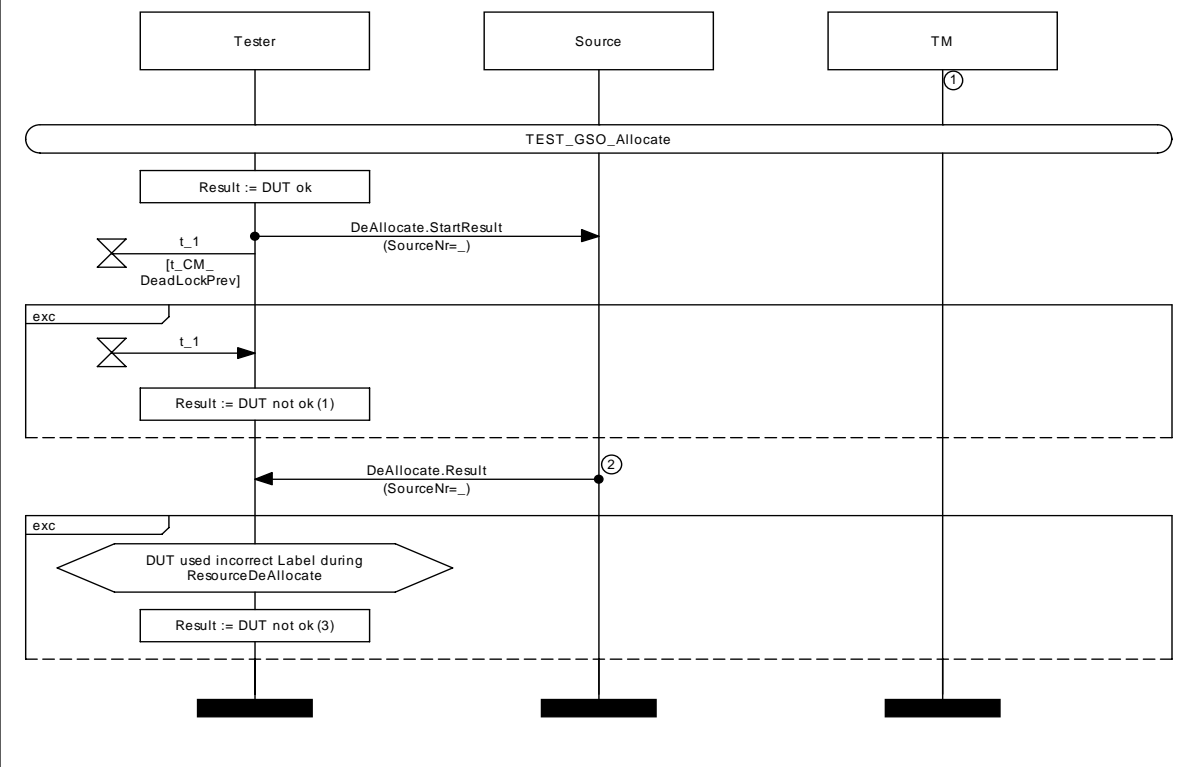


- 1 Note: TM could be located in DUT or in Tester.
- 2 Message monitored by Spy-Device .
Additionally, the Spy has to monitor the respond of the TM to the Source (same control frame as ResourceDeAllocate). From this respond, the Spy is able to get information about ChannelList.
- 3 Message "ResourceDeAllocate" from DUT to TM monitored by Spy-Device .
Additionally, the Spy has to monitor the respond of the TM to the Source (same control frame as ResourceDeAllocate). From this respond, the Spy is able to get information about the Status.
- 4 Synchronisation between Spy and Tester can be performed by any kind of mechanism.
- 5 If DUT responds with DeAllocate.Processing, the tester has to repeat DeAllocate.StartResult after t_CM_DeadLockPrev (max 10 retries)

CORE_GSL_GSO

msc TEST_GSO_DeAllocate_MOST50

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- 1 Note: TM could be located in DUT or in Tester.
- 2 If DUT responds with DeAllocate.Processing, the tester has to repeat DeAllocate.StartResult after t_CM_DeadLockPrev (max 10 retries)

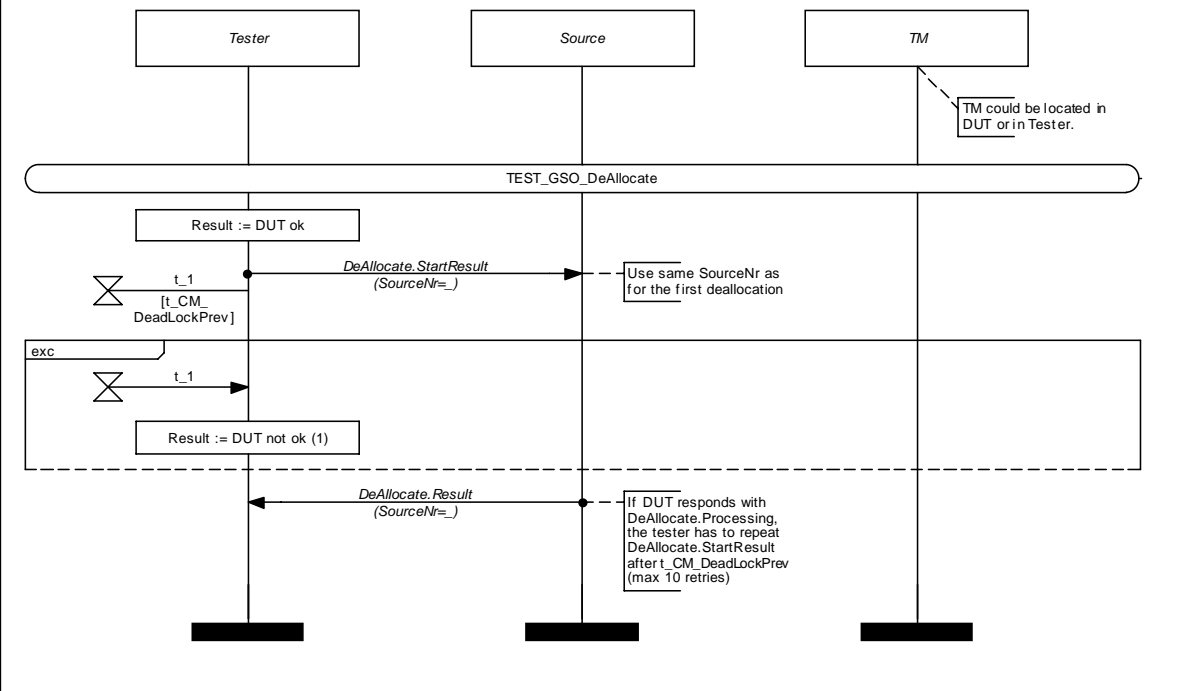
3.13.2.8 TEST_GSO_DeAllocate_Repeat (3.2-9)

Name of test	TEST_GSO_DeAllocate_Repeat 3.2-9
Reference to MOST Specification	Rev. 2.4: [MDS] para 4.9.2 [MS] para 3.5.2.2.3 Rev. 2.5: [MDS] 4.9.2 Deallocate [MS] 3.5.2.2.3 Handling of Double Commands
Value of Interest	t_CM_DeadLockPrev DeAllocate.Status
Preconditions	- DUT: NormalOperation
Test focus	DUT has to process repeated DeAllocate.StartResult correctly
Device type	All source devices, supporting allocate mechanism [x] MOST25 [x] MOST50 oPHY [x] MOST50 ePHY
Multi Node Device	All devices containing at least one source that supports allocate mechanism
Note	- Test only applicable if DUT does not contain CM. - Timeout "t_CM_DeadLockPrev" is valid for whole MSC. Note to "Spy/Resource(De)Allocate with Grantinfo + ChannelList for DUT = TM": In some cases the Spy device is not able to evaluate ChannelList, Status and Label from the ResourceAllocate message of DUT (e.g. DUT = TM). In that case the Spy and the Tester has to assume correct values to ensure the DUT does not fail the test due to technical restrictions of test setup. The test result has to be marked accordingly. Nevertheless, the DUT will fail the test if it fails to send any Resource(De)Allocate message to TM.
Results	DUT ok: The DUT has passed the test DUT not ok (1): The DUT fails to respond within t_CM_DeadLockPrev

CORE_GSL_GSO

msc TEST_GSO_DeAllocate_Repeat

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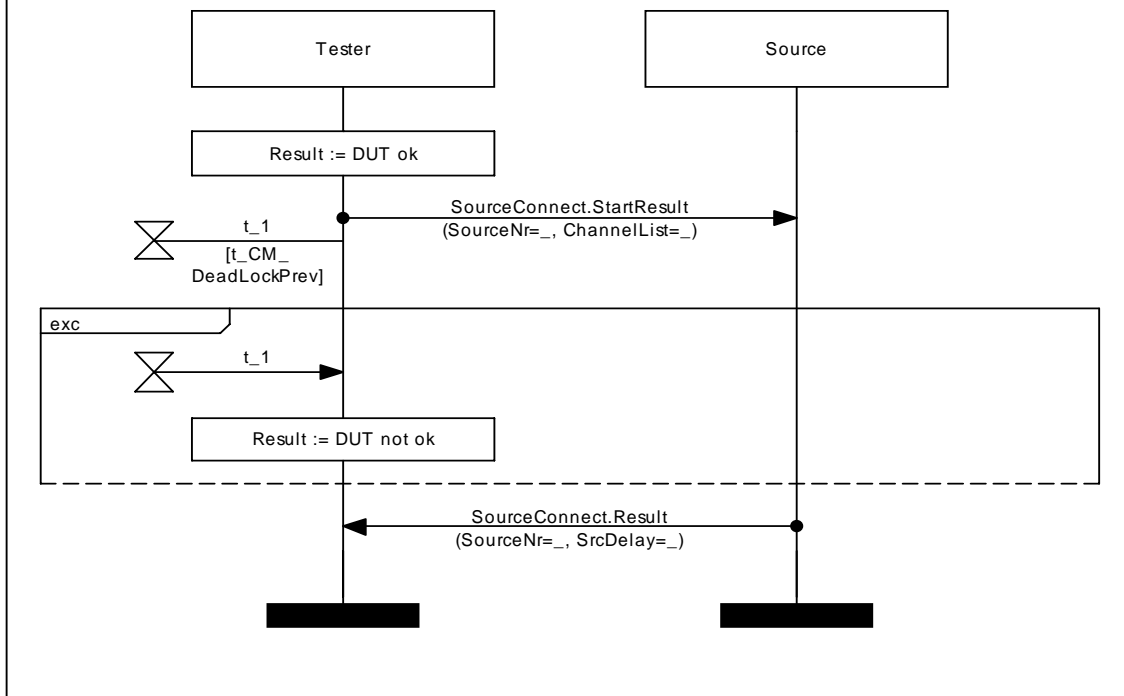
3.13.2.9 TEST_GSO_SourceConnect (3.2-10)

Name of test	TEST_GSO_SourceConnect 3.2-10
Reference to MOST Specification	Rev. 2.4: [MDS] para 4.9.3 [MS] para 3.5.2.2.1 Rev. 2.5: [MDS] 4.9.3 SourceConnect [MS] 3.5.2.2.1 Streaming Source
Value of Interest	t_CM_DeadLockPrev SourceConnect.Result
Preconditions	- DUT: NormalOperation
Test focus	DUT has to process SourceConnect.StartResult correctly
Device type	All source devices, supporting SourceConnect mechanism [x] MOST25 [] MOST50 oPHY [] MOST50 ePHY
Multi Node Device	All devices containing at least one source that supports SourceConnect mechanism
Note	- Test only applicable if DUT does not contain CM - Test to be performed with every single SourceNumber, supported by the DUT. - Timeout "t_CM_DeadLockPrev" is valid for whole MSC.
Results	DUT ok: The DUT has passed the test DUT not ok: The DUT fails to respond within t_CM_DeadLockPrev

CORE_GSI_GSO

msc TEST_GSO_SourceConnect

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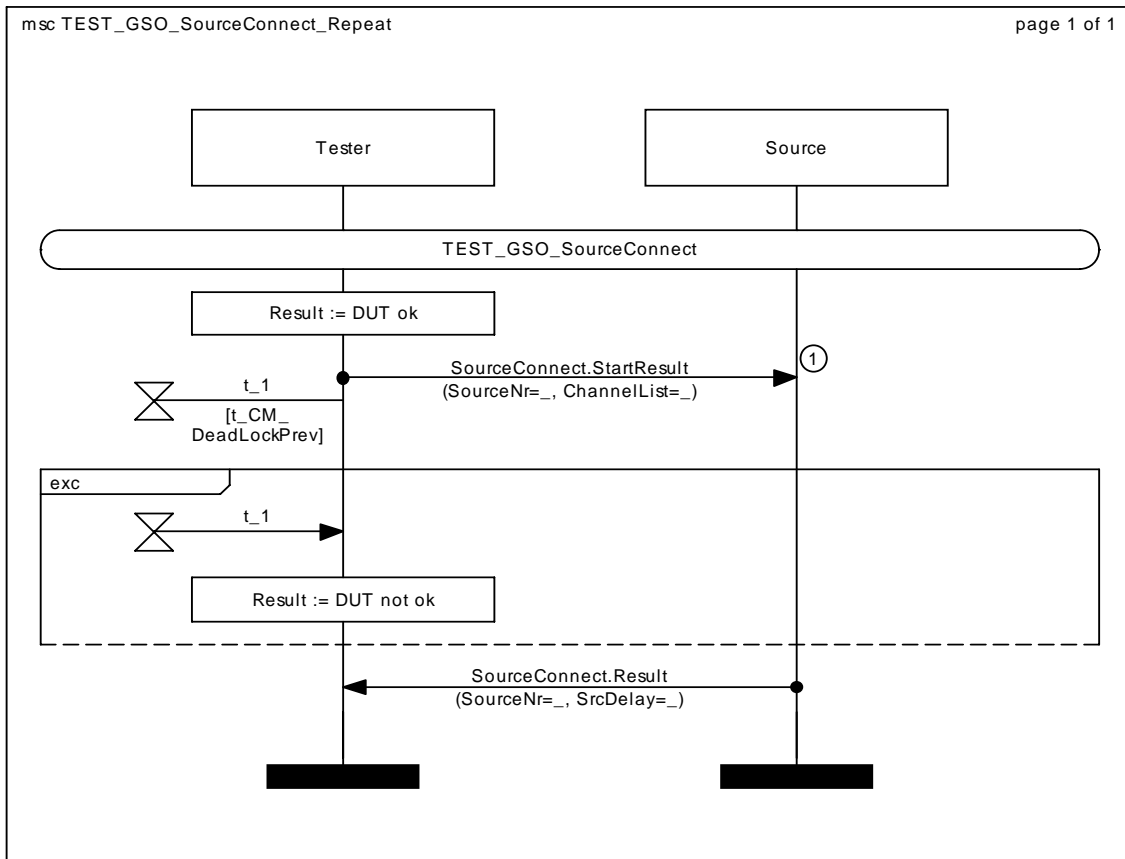
3.13.2.10 TEST_GSO_SourceConnect_Repeat (3.2-11)

Name of test	TEST_GSO_SourceConnect_Repeat 3.2-11
Reference to MOST Specification	Rev. 2.4: [MDS] para 4.9.3 [MS] para 3.5.2.2.3 Rev. 2.5: [MDS] 4.9.3 SourceConnect [MS] 3.5.2.2.3 Handling of Double Commands
Value of Interest	t_CM_DeadLockPrev SourceConnect.Result
Preconditions	- DUT: NormalOperation
Test focus	DUT has to process repeated SourceConnect.StartResult correctly
Device type	All source devices, supporting SourceConnect mechanism [x] MOST25 [] MOST50 oPHY [] MOST50 ePHY
Multi Node Device	All devices containing at least one source that supports SourceConnect mechanism
Note	- Test only applicable if DUT does not contain CM - Test to be performed with every single SourceNumber, supported by the DUT. - Timeout "t_CM_DeadLockPrev" is valid for whole MSC.
Results	DUT ok: The DUT has passed the test DUT not ok: The DUT fails to respond within t_CM_DeadLockPrev

CORE_GSI_GSO

msc TEST_GSO_SourceConnect_Repeat

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1 Use same SourceNr as for the first SourceConnect

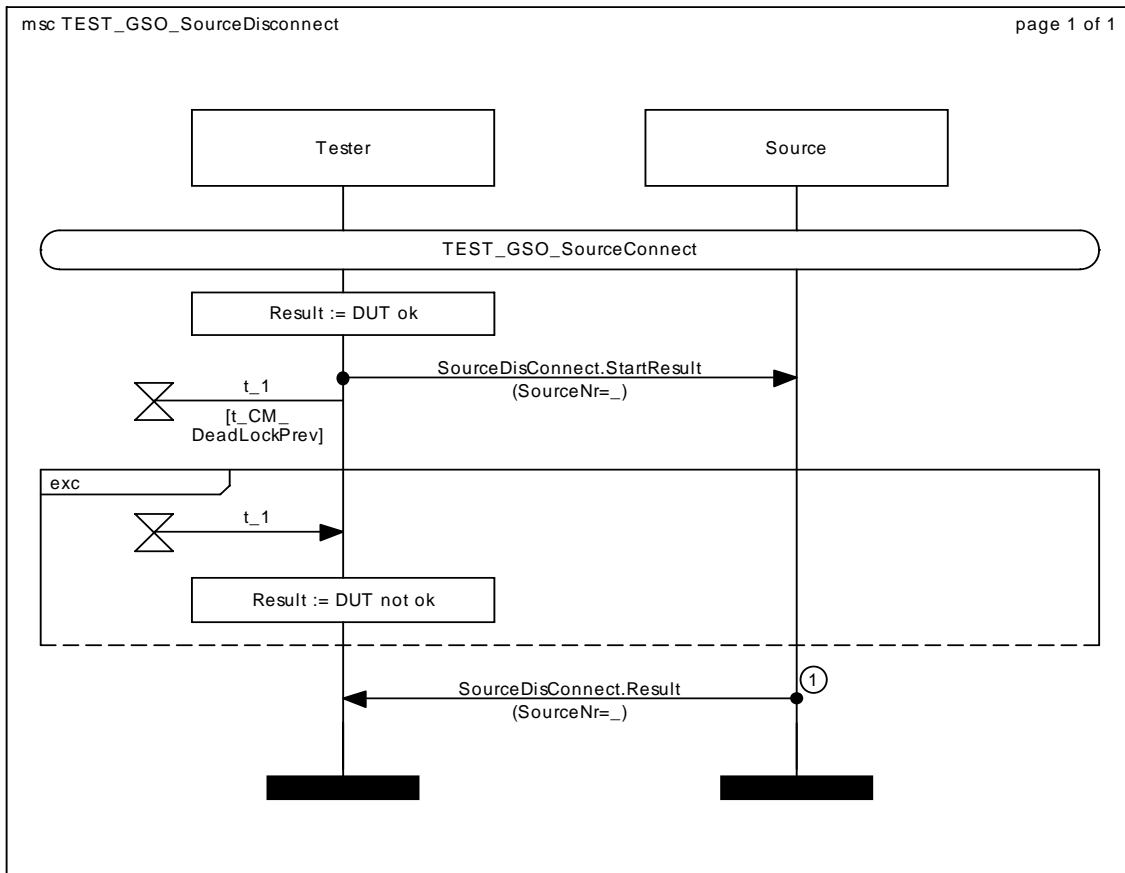
3.13.2.11 TEST_GSO_SourceDisconnect (3.2-12)

Name of test	TEST_GSO_SourceDisconnect 3.2-12
Reference to MOST Specification	Rev. 2.4: [MDS] para 4.9.4 [MS] para 3.5.2.2.2 Rev. 2.5: [MDS] 4.9.4 SourceDisconnect [MS] 3.5.2.2.2 Streaming Sink
Value of Interest	t_CM_DeadLockPrev SourceDisconnect.Result
Preconditions	- DUT: NormalOperation
Test focus	DUT has to process SourceDisconnect.StartResult correctly
Device type	All source devices, supporting SourceConnect mechanism [x] MOST25 [] MOST50 oPHY [] MOST50 ePHY
Multi Node Device	All devices containing at least one source that supports SourceConnect mechanism
Note	- Test only applicable if DUT does not contain CM. - Timeout "t_CM_DeadLockPrev" is valid for whole MSC.
Results	DUT ok: The DUT has passed the test DUT not ok: The DUT fails to respond within t_CM_DeadLockPrev

CORE_GSI_GSO

msc TEST_GSO_SourceDisconnect

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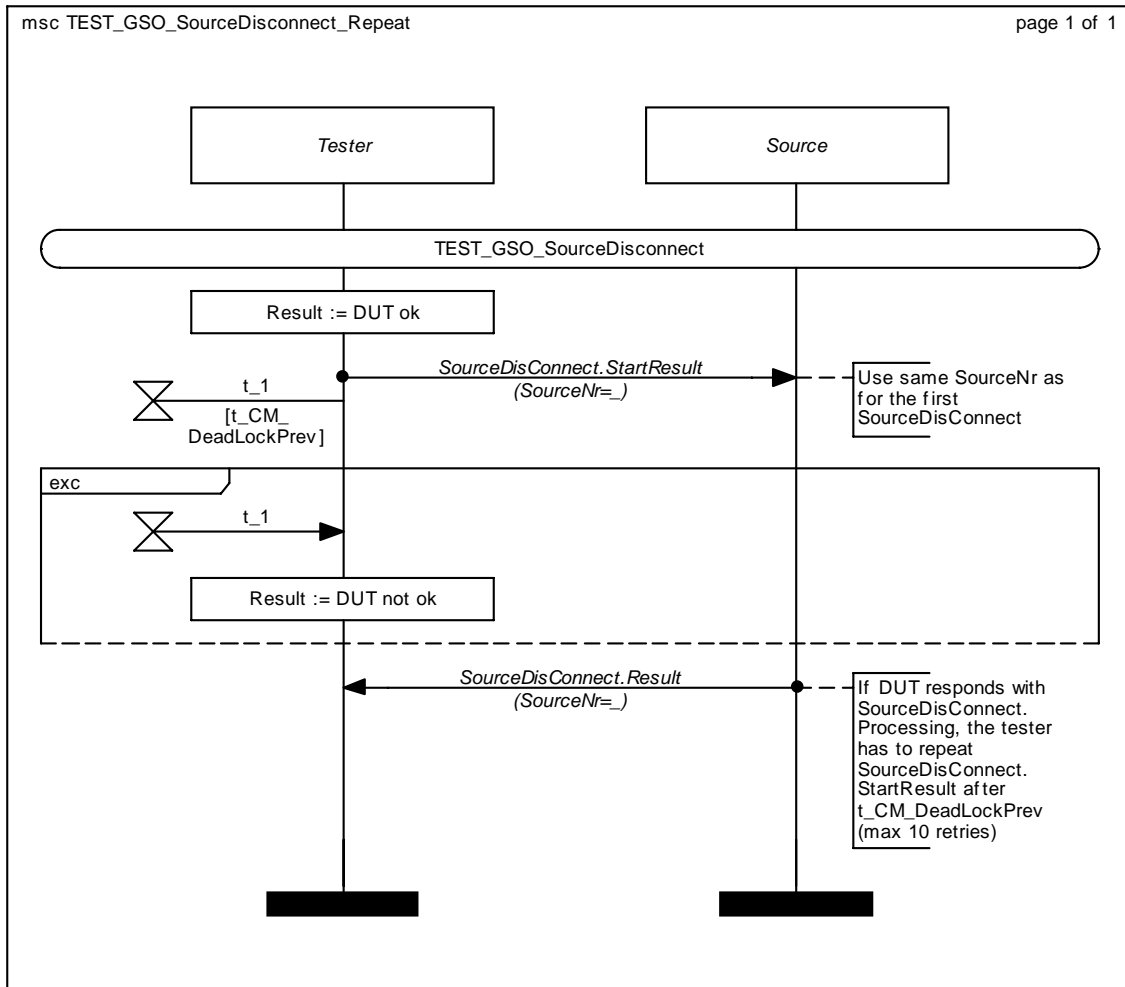


- 1 If DUT responds with `SourceDisconnect.Processing`, the tester has to repeat `SourceDisconnect.StartResult` after `t_CM_DeadLockPrev` (max 10 retries)

3.13.2.12 TEST_GSO_SourceDisconnect_Repeat (3.2-13)

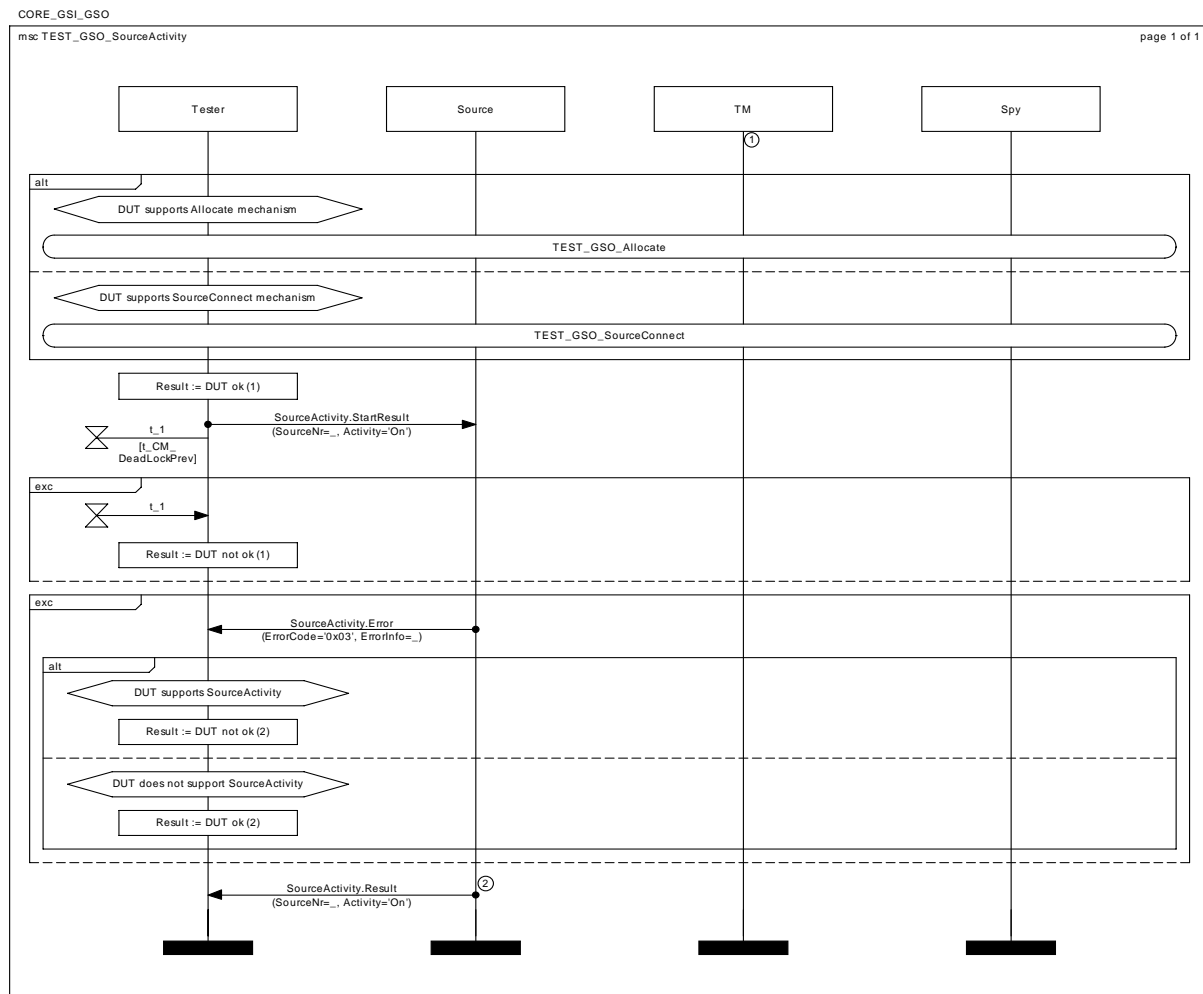
Name of test	TEST_GSO_SourceDisconnect_Repeat 3.2-13
Reference to MOST Specification	Rev. 2.4: [MDS] para 4.9.4 [MS] para 3.5.2.2.3 Rev. 2.5: [MDS] 4.9.4 SourceDisconnect [MS] 3.5.2.2.3 Handling of Double Commands
Value of Interest	t_CM_DeadLockPrev SourceDisconnect.Result
Preconditions	- DUT: NormalOperation
Test focus	DUT has to process repeated SourceDisconnect.StartResult correctly
Device type	All source devices, supporting SourceConnect mechanism [x] MOST25 [] MOST50 oPHY [] MOST50 ePHY
Multi Node Device	All devices containing at least one source that supports SourceConnect mechanism
Note	- Test only applicable if DUT does not contain CM. - Timeout "t_CM_DeadLockPrev" is valid for whole MSC.
Results	DUT ok: The DUT has passed the test DUT not ok: The DUT fails to respond within t_CM_DeadLockPrev

CORE_GSI_GSO



3.13.2.13 TEST_GSO_SourceActivity (3.2-14)

Name of test	TEST_GSO_SourceActivity 3.2-14
Reference to MOST Specification	Rev. 2.4: [MDS] para 4.4.1.1.4 [MS] para 3.5.2.2.1 Rev. 2.5: [MDS] 4.4.1.1.4 SourceActivity turned on [MS] 3.5.2.2.1 Streaming Source
Value of Interest	t_CM_DeadLockPrev SourceActivity.Result
Preconditions	- DUT: NormalOperation
Test focus	DUT has to set SourceActivity to "On" if commanded.
Device type	All source devices [x] MOST25 [x] MOST50 oPHY [x] MOST50 ePHY
Multi Node Device	All devices containing at least one source
Note	- Test only applicable if DUT does not contain CM - Test to be performed with every SourceNumber, supported by the DUT. - Input by device manufacturer whether DUT supports SourceActivity - Timeout "t_CM_DeadLockPrev" is valid for whole MSC.
Results	DUT ok (1): The DUT has passed the test DUT ok (2): The DUT does not support SourceActivity DUT not ok (1): The DUT fails to respond within t_CM_DeadLockPrev DUT not ok (2): The DUT responds with ErrorCode "0x03" although SourceActivity should be supported.



- 1 TM could be located in DUT or in Tester.
- 2 If DUT responds with SourceActivity.Processing, the tester has to repeat SourceActivity.StartResult after t_CM_DeadLockPrev (max 10 retries)

Notes:

Notes:

4 Appendix 1: Requirements and measurement uncertainty for test equipment and setup

Test Case Nr.	Designation	Measured value	Measurement uncertainty
2.1.0-1	Generic FBlock Property test	t_Property t_ProcessingDefault1	+20ms +20ms
2.1.0-2	Generic FBlock Method test	t_Property t_ProcessingDefault1	+20ms +20ms
2.1.1-1	Signal On Test	t_WakeUp	0,5ms
2.1.1-2	SBC register test	t_Config	+10ms
2.1.1-4	Wakeup/Shutdown test	t_Config	+10ms
2.1.1-5	Bypass test (All bypass)	t_test (t_Lock + t_WaitNodes)	+10ms
2.1.2-4	SBC register check test	t_Boundary	Not required
2.1.2-5	Slave lock detection test	t_Config + t_WaitNodes	+20ms
2.1.2-6	Signal off test	t_ShutDown	0,5ms
2.1.3-1	Slave wakeup	t_ShutDown t_Restart	0,5ms +5ms
2.1.3-4	Waking slave timeout	t_Restart t_Config	+5ms +5ms
2.2.1-3	short unlock test	t_Unlock t_Lock	+1ms, (Verification of setup)
2.2.1-4	accumulated short unlock test	t_test_min t_test_max	+1ms, (Verification of setup)
2.2.1-5	critical unlock test	t_Unlock t_Restart (MOST signal case)	+5ms +5ms
2.3.1-3 2.3.1-6	Timeout execute test Timeout suspend test	t_Suspend t_ShutDownWait t_RetryShutDown	+5ms +5ms +25ms
2.3.2-2	Shutdown.Start(Query) test	t_Suspend t_ShutDownWait	+5ms Not required
2.3.2-3	Shutdown.Start(Execute) test	t_ShutDownWait t_ShutDown t_PwrSwitchOffDelay	Not required 0,5ms +500ms
2.3.3-1	Over-/undervoltage test	t_test	Not required
2.3.3-4	Over-temperature signal off test	-	-
2.3.3-5	Temperature ShutDown Reaction test	t_WaitAfterOvertempShutDown	+500ms
2.4.1-1	Restart stop test	t_Config t_Restart	+10ms +5ms
2.4.1-2	Restart continue test	t_Config t_Restart	+10ms +5ms

Test Case Nr.	Designation	Measured value	Measurement uncertainty
2.4.1-9	Reaction of NCE test	t_WaitAfterNCE	+/-30 ms
2.4.1-11	Re-enter delay test	t_Bypass	+/- 5 ms
2.4.2-1	Critical Voltage test	<= 9V / (>=16.5V)	+/-0.1V
2.4.2-3	Low Voltage test	<= 7V	+/-0.1V
2.5-2	RBD procedure test	t_Diag_Slave t_Diag_Master t_PwrSwitchOffDelay	Not required Not required Not required
2.5-3	TM RBD procedure test	t_Diag_Slave t_Diag_Master t_PwrSwitchOffDelay	Not required Not required Not required
2.5-7	Wakeup after RBD test	t_test (t_Diag_Restart)	+/-5ms
2.5-8	RBD missing TM test	t_Diag_Slave	+/-200ms
2.6.1-1	NWM address initialization test	-	-
2.6.2-1	FBlock polling test	t_WaitForAnswer	+/-5ms
2.6.2-3a	Device ignore test (a)	-	-
2.6.2-3b	Device ignore test (b)	t_DelayCfgRequest1 t_DelayCfgRequest2	+/-20ms +/-200ms
2.6.2-4a	Device integration test (a)	-	-
2.6.2-4b	Device integration test (b)	-	-
2.6.2-5	Config(Ok) Delay test	t_WaitBeforeScan t_WaitForAnswer	+/-20ms +/-5ms
2.6.2-6	Double FBlock test	-	-
2.6.2-7	Config(New) Order test	-	-
2.6.3-1	FBlock Status Change Detection test	-	-
2.6.4-1	Address initialisation test	-	-
2.6.4-3	NWM Address Storage test	-	-
2.6.4-4	Address Re-Initialisation test	-	-
2.6.4-8	FBlock Response time test	t_Answer	+/-5ms
2.6.4-9	Control message suppression test	-	-
2.6.4-10	InstID Wildcard test	-	-
2.7-1	Node Addressing test	-	-
2.8.3-1a	Notification Matrix Storage test (NWM)	t_Property	+/-20ms
2.8.3-1b	Notification Matrix Storage test (Slave)	t_Property	+/-20ms
2.8.3-2	NotificationCheck test	-	-
2.8.3-7	Notification Matrix Double Entry test	t_Property	+/-20ms
2.8.3-10	Notification Error test	-	-
2.8.4-1	Segmented Message Rejection test	t_Property	+/-20ms

Test Case Nr.	Designation	Measured value	Measurement uncertainty
2.8.4-2	Segmented Message Sending test	-	-
2.8.4-3	Message Segmentation Error test	t_Property	+20ms
2.8.4-7	Message Segmentation Buffer test	-	-
2.8.4-8	Parallel Segmented Message Reception test	-	-
2.8.4-9	Broadcast error test	-	-
3.0-1	TEST_GSI_GSO_Identification	t_Property	+20ms
3.1-1	TEST_GSI_SinkInfo	t_Property	+20ms
3.1-3	TEST_GSI_Connect	t_CM_DeadLockPrev	Not required
3.1-4	TEST_GSI_Connect_Repeat	t_CM_DeadLockPrev	Not required
3.1-5	TEST_GSI_DisConnect	t_Property t_CM_DeadLockPrev	+20ms
3.1-6	TEST_GSI_DisConnect_Repeat	t_CM_DeadLockPrev	Not required
3.2-1	TEST_GSO_SourceInfo	t_Property	+20ms
3.2-3	TEST_GSO_Allocate	t_CM_DeadLockPrev	Not required
3.2-4	TEST_GSO_Allocate_Repeat	t_CM_DeadLockPrev	Not required
3.2-5	TEST_GSO_Allocate_NoChAvail	t_CM_DeadLockPrev	Not required
3.2-6	TEST_GSO_Allocate_ReqChAvail	t_CM_DeadLockPrev	Not required
3.2-7	TEST_GSO_Allocate_WrongSourceNr	t_CM_DeadLockPrev	Not required
3.2-8	TEST_GSO_DeAllocate	t_CM_DeadLockPrev	Not required
3.2-9	TEST_GSO_DeAllocate_Repeat	t_CM_DeadLockPrev	Not required
3.2-10	TEST_GSO_SourceConnect	t_CM_DeadLockPrev	Not required
3.2-11	TEST_GSO_SourceConnect_Repeat	t_CM_DeadLockPrev	Not required
3.2-12	TEST_GSO_SourceDisconnect	t_CM_DeadLockPrev	Not required
3.2-13	TEST_GSO_SourceDisconnect_Repeat	t_CM_DeadLockPrev	Not required
3.2-14	TEST_GSO_SourceActivity	t_CM_DeadLockPrev	Not required

5 Appendix 2: FBlock ET Reference to Test Cases (informative)

Note:

"-" means, this function is not used by MOST Core Compliance but by other compliance tests.

FktID	Name	Test Case	Page
0x000	FktIDs	not test specific	-
0x200	Attenuation	-	-
0x201	AutoWakeup	2.1.1-4 (Wake-up / Shutdown test (MOST2V5)) 2.1.3-1 (Slave wake-up (MOST2V5)) 2.1.3-4 (Waking slave timeout (MOST2V5)) 2.4.1-1 (Restart stop test (MOST2V5)) 2.4.1-2 (Restart continue test (MOST2V5)) 2.5-2 (RBD procedure test) 2.5-3 (TM RBD procedure test) 2.5-7 (Wakeup after RBD test) 2.5-8 (RBD missing TM test)	43 51 55 79 83 93 95 97 99
0x202	DiagTimeout	2.5-2 (RBD procedure test) 2.5-3 (TM RBD procedure test)	93 95
0x203	DiagResult	2.5-2 (RBD procedure test) 2.5-3 (TM RBD procedure test) 2.5-8 (RBD missing TM test)	93 95 99
0x204	Shutdown	2.3.1-3 (Timeout execute / Timeout suspend) 2.3.3-4 (Over-temperature signal off test)	63 75
0x205	ShutdownSuspendMode	2.3.2-2 (ShutDown.Start(Query) test)	69
0x206	NetInterfaceState	2.4.2-1 (Critical Voltage test)	89
0x207	SendMessage	-	-
0x208	EchoMessage	2.8.4-2 (Segmented Message Sending test) 2.8.4-7 (Message Segmentation Buffer test)	151 155
0x209	MessageBufSize	2.8.4-7 (Message Segmentation Buffer test) 2.8.4-8 (Parallel Segmented Message Reception test)	155 157
0x20A	SendViaMHP	-	-
0x20B	EchoViaMHP	-	-
0x20C	MHPData	-	-
0x20D	MamacPing	-	-
0x20E	ResetTests	not test specific	-
0x20F	CodingErrors	-	-
0x210	VoltageLevels	2.3.3-1 (Over-/ undervoltage test) 2.4.2-1 (Critical Voltage test) 2.4.2-3 (Low Voltage test)	73 89 91
0x211	Reset	2.4.1-11 (Re-enter delay test)	87
0x212	CentralRegistrySize	2.6.2-4b (Device Integration test (b))	111

FktID	Name	Test Case	Page
0x213	NotificationMatrixSize	2.8.3-1a (Notification Matrix Storage test (NWM))	139
		2.8.3-1b (Notification Matrix Storage test (Slave))	141
		2.8.3-7 (Notification Matrix Double Entry test)	145
0x214	ManufacturerTimings	2.1.1-4 (Wake-up / Shutdown test (MOST2V5))	43
		2.1.3-1 (Slave wake-up (MOST2V5))	51
		2.1.3-4 (Waking slave timeout (MOST2V5))	55
0x3C8	void ¹	2.8.4-9 (Broadcast error test)	135
0xF00	Version	-	-

¹ Function reserved; must not be implemented

6 Appendix 3: MOST Core Compliance Test Plan List (informative)

This table shows the relevant test cases, which are applicable for MOST25 and MOST50 devices.

TestCase No.	Name	MOST Spec 2V5 (MOST25)	MOST Spec 2V5 (MOST50)
	Chapter Static FBlock behaviour		
2.1.0-1	Generic FBlock Property test	YES	YES oPhy YES ePhy
2.1.0-2	Generic FBlock Method test	YES	YES oPhy YES ePhy
	Chapter Wake-Up		
	Sub Chapter Wake-Up – General		
2.1.1-1	Signal On Test	YES	YES oPhy NO ePhy
2.1.1-5	Bypass test	YES	YES oPhy NO ePhy
2.1.2-6	Signal off test	YES	YES oPhy NO ePhy
	Sub Chapter Wake-Up - Timing Master		
2.1.1-2	SBC Register Test	YES	NO oPhy NO ePhy
2.1.1-4	WakeUp/Shutdown Test	YES	YES oPhy NO ePhy
	Sub Chapter Wake-Up – Timing Slave		
2.1.2-4	SBC register check test	YES	NO oPhy NO ePhy
2.1.2-5	Slave lock detection test	YES	NO oPhy NO ePhy
2.1.3-1	Slave wake-up	YES	YES oPhy NO ePhy
2.1.3-4	Waking slave timeout	YES	YES oPhy NO ePhy
	Chapter Normal Operation		
	Sub Chapter Normal Operation - Unlock		
2.2.1-3	Short unlock test	YES	YES oPhy YES ePhy
2.2.1-4	Accumulated short unlock test	YES	YES oPhy YES ePhy
2.2.1-5	Critical unlock test	YES	YES oPhy YES ePhy
	Chapter Power Management		
	Sub Chapter Power Management – Power Master		
2.3.1-3	Timeout execute timeout suspend	YES	YES oPhy YES ePhy

TestCase No.	Name	MOST Spec 2V5 (MOST25)	MOST Spec 2V5 (MOST50)
2.3.1-6	Timeout suspend	YES	YES oPhy YES ePhy
2.3.3-5	Temperature ShutDown Reaction Test	YES	YES oPhy YES ePhy
	Sub Chapter Power Management - Power Slave		
2.3.2-2	Shutdown.Start(Query) test	YES	YES oPhy YES ePhy
2.3.2-3	Shutdown.Start(Execute) test	YES	YES oPhy YES ePhy
	Sub Chapter Power Management – General		
2.3.3-1	Over-/undervoltage test	YES	YES oPhy YES ePhy
2.3.3-3	AbilityToWake-Flag test	---	---
2.3.3-4	Over-temperature signal off test	YES	YES oPhy YES ePhy
	Chapter Error Management		
	Sub Chapter Error Management – MOST Signal Path		
2.4.1-1	Restart stop test	YES	YES oPhy NO ePhy
2.4.1-2	Restart continue	YES	YES oPhy NO ePhy
2.4.1-9	Reaction of NCE test	YES	YES oPhy YES ePhy
2.4.1-11	Re-enter delay test	YES	YES oPhy NO ePhy
	Sub Chapter Error Management – Voltage Level		
2.4.2-1	Critical Voltage test	YES	YES oPhy YES ePhy
2.4.2-3	Low Voltage test	YES	YES oPhy YES ePhy
	Chapter Ring Break Diagnosis		
2.5-2	RBD procedure test	YES	YES oPhy NO ePhy
2.5-3	TM RBD procedure test	YES	YES oPhy NO ePhy
2.5-7	Wakeup after RBD test	YES	YES oPhy NO ePhy
2.5-8	RBD missing TM	YES	YES oPhy NO ePhy
	Chapter System Configuration		
	Sub Chapter Configuration / System Configuration (NWM)		
2.6.1-1	NWM address initialization test	YES	YES oPhy YES ePhy
2.6.2-1	Fblock polling test	YES	YES oPhy YES ePhy
2.6.2-3a	Device Ignore test (a)	YES	YES oPhy YES ePhy
2.6.2-3b	Device Ignore test (b)	YES	YES oPhy YES ePhy

TestCase No.	Name	MOST Spec 2V5 (MOST25)	MOST Spec 2V5 (MOST50)
2.6.2-4a	Device Integration test (a)	YES	YES oPhy YES ePhy
2.6.2-4b	Device Integration test (b)	YES	YES oPhy YES ePhy
2.6.2-5	Config(Ok) Delay test	YES	YES oPhy YES ePhy
2.6.2-6	Double Fblock test	YES	YES oPhy YES ePhy
2.6.2-7	Config(New) Order test	YES	YES oPhy YES ePhy
2.6.3-1	Fblock Status Change Detection test	YES	YES oPhy YES ePhy
	Sub Chapter Configuration / System Configuration (Slave)		
2.6.4-1	Address initialization test	YES	YES oPhy YES ePhy
2.6.4-3	NWM Address Storage test	YES	YES oPhy YES ePhy
2.6.4-4	Address Re-Initialization test	YES	YES oPhy YES ePhy
2.6.4-8	Fblock Response Time test	YES	YES oPhy YES ePhy
2.6.4-9	Control Message Suppression test	YES	YES oPhy YES ePhy
	Sub Chapter Configuration / System Configuration (General)		
2.6.4-10	InstID Wildcard test	YES	YES oPhy YES ePhy
	Chapter Node Addressing		
2.7-1	Node Addressing test	YES	YES oPhy YES ePhy
2.8.4-9	Broadcast Error test	YES	YES oPhy YES ePhy
	Chapter Other Functions		
	Sub Chapter Ack/Nack		
2.8.1-1	Ack / Nack test	YES	YES oPhy YES ePhy
	Sub Chapter Notification Matrix		
2.8.3-1a	Notification Matrix Storage test (NWM)	YES	YES oPhy YES ePhy
2.8.3-1b	Notification Matrix Storage test (Slave)	YES	YES oPhy YES ePhy
2.8.3-2	NotificationCheck test	YES	YES oPhy YES ePhy
2.8.3-5	Notification Matrix Size test	---	---
2.8.3-7	Notification Matrix Double Entry test	YES	YES oPhy YES ePhy
2.8.3-10	Notification Error test	YES	YES oPhy YES ePhy
	Sub Chapter Message Segmentation		
2.8.4-1	Segmented Message Rejection test	YES	YES oPhy YES ePhy

TestCase No.	Name	MOST Spec 2V5 (MOST25)	MOST Spec 2V5 (MOST50)
2.8.4-2	Segmented Message Sending test	YES	YES oPhy YES ePhy
2.8.4-3	Message Segmentation Error test	YES	YES oPhy YES ePhy
2.8.4-7	Message Segmentation Buffer test	YES	YES oPhy YES ePhy
2.8.4-8	Parallel Segmented Message Reception test	YES	YES oPhy YES ePhy
	Sub Chapter Source / Sink Identification		
3.0-1	TEST_GSI_GSO_Identification	YES	YES oPhy YES ePhy
	Chapter Obligatory tests for sink and source devices		
	Sub Chapter Sink Devices		
3.1-1	TEST_GSI_SinkInfo	YES	YES oPhy YES ePhy
3.1-3	TEST_GSI_Connect	YES	YES oPhy YES ePhy
3.1-4	TEST_GSI_Connect_Repeat	YES	YES oPhy YES ePhy
3.1-5	TEST_GSI_DisConnect	YES	YES oPhy YES ePhy
3.1-6	TEST_GSI_DisConnect_Repeat	YES	YES oPhy YES ePhy
	Sub Chapter Source Devices		
3.2-1	TEST_GSO_SourceInfo	YES	YES oPhy YES ePhy
3.2-3	TEST_GSO_Allocate	YES	YES oPhy YES ePhy
3.2-4	TEST_GSO_Allocate_Repeat	YES	YES oPhy YES ePhy
3.2-5	TEST_GSO_Allocate_NoChAvail	YES	YES oPhy YES ePhy
3.2-6	TEST_GSO_Allocate_ReqChAvail	YES	YES oPhy YES ePhy
3.2-7	TEST_GSO_Allocate_WrongSourceNr	YES	YES oPhy YES ePhy
3.2-8	TEST_GSO_DeAllocate	YES	YES oPhy YES ePhy
3.2-9	TEST_GSO_DeAllocate_Repeat	YES	YES oPhy YES ePhy
3.2-10	TEST_GSO_SourceConnect	YES	NO oPhy NO ePhy
3.2-11	TEST_GSO_SourceConnect_Repeat	YES	NO oPhy NO ePhy
3.2-12	TEST_GSO_SourceDisconnect	YES	NO oPhy NO ePhy
3.2-13	TEST_GSO_SourceDisconnect_Repeat	YES	NO oPhy NO ePhy
3.2-14	TEST_GSO_SourceActivity	YES	YES oPhy YES ePhy

Annotations:

States of DUT have to adapted accordingly.

Procedure perform wake-up: Wait for SBC > 5 --> Wait for
NetInterfaceNormalOperation

FB ET

0x201 AutoWakeup	not used for ePhy
0x202 DiagTimeout	not used for ePhy
0x203 DiagResult	not used for ePhy
0x211 Reset	not used for ePhy

7 Appendix 4: Document History (Previous Revisions)

Initial Release Date: 2002-11-04

First version of MOST Core Compliance Test Specification 1V0-00

Change Ref.	Section	Changes
-	-	First issue

Changes MOST Core Compliance Test Specification 1V0-00 to MOST Core Compliance Test Specification 1V1-00

Change Ref.	Section	Changes
1V1_001	1.1.2	Description of TestBlock replaced by description of FBlock ET
1V1_002	2.1.1-2	Test number corrected ("2.1.1-2; 2.1.1-3" changed to "2.1.1-2")
1V1_003	2.1.2-5	Test procedure modified: Monitoring of NAL register not applicable (avoiding remote read). New timing: (t_slave + t_waitnodes)
1V1_004	2.1.3-1	Comments about Fblock ET added (t_manufacturer and AutoWakeup)
1V1_005	2.1.3-4	Comments about Fblock ET added (t_manufacturer and AutoWakeup)
1V1_006	2.1.3-4	Comments about Fblock ET added (t_manufacturer and AutoWakeup). More precise definition of "timeout"
1V1_007	2.2.1.2	Reference in core function to test procedure updated (refer to test 2.4.1-9)
1V1_008	2.2.1-2	Test deleted. Covered by 2.4.1-9.
1V1_009	2.2.1-5	Test sequence modified (t_restart will be started before generating the MOST signal). Note added to support electrical wake-up signal.
1V1_010	2.3.1-1	Comment about Fblock ET added (AutoWakeup)
1V1_011	2.3.2-2	Test 2.3.2-2: Replaced timeout t_ShutDownWait with t_RetryShutdown in test description and FlowChart. Comments about Fblock ET added (ShutDownSuspendMode)
1V1_012	2.3.3-1	Correction in square edge function diagram of the test note. Now it matches to the test procedure. Misspelling corrected.
1V1_013	2.3.3-3	Timeout (t_manufacturer) added to FlowChart. Comments about Fblock ET added (t_manufacturer)
1V1_014	2.3.3-4	Wording of temperature levels corrected Comment about Fblock ET added (Shutdown) End of test modified; "DUT not ok(5)" deleted
1V1_015	2.4.1.10	Test 2.4.1-10 (Reaction of NCE test (data sink)) deleted. Not part of core compliance. Moved to profile compliance (mute/demute)
1V1_016	2.4.1-1	Brackets removed from "external" Comment about Fblock ET added (AutoWakeup)
1V1_017	2.4.1-11	Test updated. t_bypass min and max will be tested now. Device type in test description corrected: NWM → TM
1V1_018	2.4.1-2	Brackets removed from "external" Comment about Fblock ET added (AutoWakeup)

Change Ref.	Section	Changes
1V1_019	2.4.1-9	Note corrected ("DUT <> NM" → "DUT <> TM") Note in test description about generating NCE without unlock added. Note in test description about tester1 added (Tester1 has to respond to every system configuration check of the DUT normally but with other FBlocks than tester2.) FlowChart updated (timeout t_WaitAfterNCE(max) added; t_bypass(min) added)
1V1_020	2.4.2.1	Corrections according U_Critical; U_Super
1V1_021	2.4.2-1	FlowChart and test description updated according to U_Critical, U_Super and voltage regulator ramp.
1V1_022	2.4.2-3	FlowChart and test description updated according to tolerances (U_Low) and voltage regulator ramp.
1V1_023	2.5-2	FlowChart updated according to starting of t_DiagMaster and check of "MOST signal received".
1V1_024	2.5-7	FlowChart updated according to t_DiagRestart, electrical wake-up support and avoidance of deadlocks. Test description updated according to t_DiagRestart.
1V1_025	2.6.1-1	FlowChart and test description updated: - Usage of FBlockIDs(Get) instead of remote read to get address of DUT - Test of "ConfigStatus(NotOk) within t_cfgstatus" deleted
1V1_026	2.6.2-4a	Test 2.6.2-4a: replaced "It has to act as a normal MOST device" with "It has to respond to FBlockIDs.Get from DUT like a normal MOST device" in test description.
1V1_027	2.6.2-4a	Changed timer from (devices x t_answer) to 1 second
1V1_028	2.6.2-4b	Test 2.6.2-4b: replaced "It has to act as a normal MOST device" with "It has to respond to FBlockIDs.Get from DUT like a normal MOST device" in test description.
1V1_029	2.6.2-6	"DUT ok (1)" changed to "DUT not ok (1)". FBlockID(set) changed to FBlockID(SetGet).
1V1_030	2.6.3-1	Test 2.6.3-1: "NotAvail(FBlock)" changed to "Configuration.Status(Invalid)" in test description and test results and flow chart
1V1_031	2.6.4-1	t_CfgStatus related part of test deleted
1V1_032	2.6.4-3	"Yes" and "No" at "t_test expired loop" added.
1V1_033	2.6.4-4	Correction: Tester1 in spy mode instead of tester2 "DUT ok (1)" and "DUT ok (2)" exchanged
1V1_034	2.7-1	Correction: Tester1 in spy mode instead of tester2
1V1_035	2.8.1-1	Correction: "ACT" → "ACK"
1V1_036	2.8.3.10	Reference of Core function changed to test 2.8.3-5
1V1_037	2.8.3.5	Support of at least three entries into the notification matrix deleted.
1V1_038	2.8.3-10	Test 2.8.3-10: Error code of "Notification Error" included in test results.
1V1_039	2.8.3-10	FlowChart and test description updated: - Error Code of Notification.Error corrected (0x07,0x1)
1V1_040	2.8.3-1a	FlowChart and test description updated: - Support of "no notification supporting devices" added - Notes about empty notification matrix at the beginning of the test added. - Now, the tester enters itself into the notification matrix of the DUT. - Tester1 now triggers Configuration.Status(New) by sending new FBlockID lists with new FBlockIDs to the DUT.
1V1_041	2.8.3-1b	Changed "DUT not ok(1)" to "DUT not ok(3)" in Test results table
1V1_042	2.8.3-1b	FlowChart and test description updated: - Support of "no notification supporting devices" added - Notes about empty notification matrix at the beginning of the test added. - Now, the tester enters itself into the notification matrix of the DUT.

Change Ref.	Section	Changes
1V1_043	2.8.3-5	FlowChart and test description updated: - Test of error in case of full notification matrix added - Notes about notification size added - Notes about supporting the notification matrix added - Notes about empty notification matrix at the beginning of the test added.
1V1_044	2.8.3-7	FlowChart and test description updated: - Exchanged "yes" and "no" in last "error received check" - Notes about notification size added - Notes about supporting the notification matrix added - Notes about empty notification matrix at the beginning of the test added.
1V1_045	2.8.4-1	FlowChart updated: - t_garbage starts as soon as the first part of the message has been sent
1V1_046	2.8.4-2	Test 2.8.4-2: Timing for message to send corrected in test description.
1V1_047	2.8.4-2	FlowChart and test description updated: - Max. 5 seconds between two segments - Usage of FBlock ET
1V1_048	2.8.4-3	Test description corrected: "Error(02)" replaced by "Error(03)"
1V1_049	2.8.4-7	Test 2.8.4-7: Misspelling corrected.
1V1_050	2.8.4-7	FlowChart and test description updated: - Usage of FBlock ET - "... more than 255..." → "...more than 256..." - Sequence of FlowChart
1V1_051	2.8.4-8	FlowChart and test description updated: - Usage of FBlock ET - "Pool overflow error" → ErrorInfo 0x04
1V1_052	all	All Open Items are removed from text. Now, they are listed in a table at the end of this document.
1V1_053	all	All Profile Notes are removed from text. Now, they are listed in a table at the end of this document
1V1_054	Appendix 1	Table "Requirements and tolerances for test equipment and setup" added.
1V1_055	para 2	Referencing table (Core function <-> Test procedure) at the beginning of the document removed. Now, the reference to the relevant test procedure is done at every core function. The reference to the MOST Specification at every Core function has been removed.
1V1_056	Bibliography	Reference list updated
1V1_057	2.1.1-2	Note about wake-up procedure of DUT in test description deleted.
1V1_058	2.1.1-4	Note about second test loop in test description deleted
1V1_059	2.1.2-4	Value of interest "t_config" deleted
1V1_060	2.2.1-5	Note in FlowChart about electrical wake-up corrected.
1V1_061	2.4.2-1	Note in FlowChart added to avoid physical destruction of DUT. "current voltage level => U_Super" changed to "current voltage level >= U_Super"
1V1_062	2.4.2-3	FlowChart updated: "level = U_Low?" changed to "level <= U_Low?"
1V1_063		Table of all test procedures with link to the test case added
1V1_064	all	Exchanged every "0xFFFF" and "0xFFFD" with "uninitialized_node_address" to be independent from version of MOST Specification
1V1_065	all tests	Reference to para of MOST Specification deleted. Reference to Core function will be sufficient.
1V1_066	2.8.4-1	FlowChart updated: - "yes" and "no" at "t_garbage > 5 seconds?" exchanged - Note about OP-Type corrected (OP-Type < "9")

Change Ref.	Section	Changes
1V1_067	2.8.4-7	Note added to exclude devices with dynamic buffer size
1V1_068	2.6.2-3a	FlowChart updated: - 3 Loops instead of 2 - Added note that Tester1 has to respond normally to the request of DUT
1V1_069	2.6.2-4a	FlowChart updated: - Timer starts with reception of FBlockIDs.Get Experimental setup updated: - Tester1 in slave mode or in master mode (depends on DUT)
1V1_070	2.1.3-1	Test results updated at "DUT not ok (1)": "... or: external wake-up not supported"
1V1_071	2.1.3-4	Test results updated at "DUT not ok (1)": "... or: external wake-up not supported"
1V1_072	all	Timer t_manufacturer distinguished between "0x00"; "0x01" and "0x02"; based on FBlock ET
1V1_073	para 3.1	New para added with general notes to test procedures (AutoWakeup; AbilityToWake; t_manufacturer; pre-conditions; uninitialized_node_address; NetInterface PowerOff)
1V1_074	para 2.1.1	More precise definition of wake-up (MOST-Interface and AutoWakeup)
1V1_075	2.4.1-9	Note to FlowChart added that the test has to be performed twice (a/b). Test results updated (a/b added)
1V1_076	2.6-2-3b	Note to FlowChart added that the test has to be performed twice (a/b). Test results updated (a/b added)
1V1_077	2.8.3-1a	FlowChart updated: - Note added about timeout to avoid deadlocks - Before requesting the Notification matrix, the tester has to wait for Configuration.Status() from DUT
1V1_078	2.5.7	FlowChart and test description updated: - The time between MOST signal on at the input of the DUT and MOST signal on at the output of the DUT will be measured. This period of time will be tested against t_DiagRestart(min/max). - "revice" changed to "receive"
1V1_079	2.8.3-5	Test procedure updated to consider minimum and maximum notification matrix size.
1V1_080	2.1.1-5	Note added, that test only applicable for optically wakeable devices.
1V1_081	para 1.1.2	Reference to FBlock ET V2.4 added
1V1_082	2.1.1-4 2.1.3-1 2.1.3-4 2.3.1-1 2.4.1-1 2.4.1-2 2.5-7 2.5-8	FlowCharts: Changed "DUT in SleepMode" to "DUT: NetInterface PowerOff"
1V1_083	all	Deleted all references to Open Items. The Open Item List at the end of the document will be sufficient.
1V1_084	para 1	Changed "The objective of this analysis " to "The objective of this document "
1V1_085	Appendix 1	Table "Requirements and tolerances for test equipment and setup" updated
1V1_086	para 3.1	Changed "These pre-conditions have to be provided to the DUT" to "These pre-conditions have to be provided to the DUT before start of testing. "
1V1_087	para 3.1	Deleted "This address can be changed in future versions of the MOST Specification"
1V1_088	2.3.1-3	Reference in Test description to 2.3.1-6 corrected.
1V1_089	Appendix 2	Renumbered: now Appendix 3
1V1_090	Appendix 3	Renumbered: now Appendix 4
1V1_091	-	Appendix 2 added: " FBlock ET Reference to Test Cases "
1V1_092	2.1.1 2.1.2 2.1.3	Added "electrical wake-up" list of potential wake-up events

Change Ref.	Section	Changes
1V1_093	2.1.1 2.1.2	Changed "No timings are specified for the other wake-up events (e.g. electrical wake-up). " to "No timings are specified for electrical wake-up. "
1V1_094	2.6.2.3	Changed "Under the following conditions the NWM has to ignore a device after three polling attempts:" to "Under the following conditions the NWM has to ignore a device after three polling attempts and has to send ConfigStatus(ok) anyway:"
1V1_095	2.3.3-3	Test description updated: DUT returns "Parameter not available; Error 7", if AbilityToWake is not supported.
1V1_096	2.8.4-7	Changed "more than 255 segments" to "more than 256 segments" in test description and results.
1V1_097	2.3.1-1	Test case deleted. In future it will be covered by 2.4.1-2
1V1_098	2.6.4-3	Test results: Exchanged "DUT ok (1)" and "DUT ok (2)"
1V1_099	2.2.1-4	Changed sequence in flowchart for testing of MOST signal off. Now it will be checked inside the loop
1V1_100	2.1.3-1	Modified flowchart for testing of t_shutdown
1V1_101	2.6.3-1	Modified flowchart: Tester1 has to send FBlockID(Status) with one preregistered FBlock missing.
1V1_102	2.6.2-6	Exchanged Tester1 and Tester2 (in flowchart and in test description)
1V1_103	2.8.4-1	Changed test description: OP-Type < "9" Changed test results: DUT not ok(2): exchanged "too late" with "too early" Modified flowchart: "segmentation error received" → "segmentation error(06) received"
1V1_104	2.3.1-3	Modified flowchart: If DUT sends ShutDown.Start(Query) again, t_suspend will be restarted.
1V1_105	2.1.1-5	- Changed duration of t_test: Now t_test = t_lock(max) + t_waitnodes(max) - Exchanged "stable lock" with "MOST signal on"
1V1_106	2.2.1-3 2.2.1-4	Exchanged t_unlockrecovery with t_lock
1V1_107	para 3.1	Added definition of "DUT in NormalOperation"
1V1_108	para 3.1	Added definition of "should"
1V1_109	2.3.1.3	Added note, how to check NormalOperation
1V1_110	2.6.2-4b	Note added: "If DUT supports dynamic CR (detectable via FBlock ET), the tester should have 257 FBlocks"
1V1_111	2.8.4-8	Note added: "Test only applicable for devices without dynamic parallel message buffers"
1V1_112	2.8.4-8	Note corrected: "FBlock ET: MessageBufSize.MsgLengthRx <> 0x000"
1V1_113	2.8.4-8	"ErrorInfo 0x04" changed to "ErrorCode 0x0C; ErrorInfo 0x04"
1V1_114	2.6.4-8	Added note to flowchart how to send FBlockID(get)
1V1_115	all	Changed copyright from "1999 – 2002" to "1999 – 2004"
1V1_116	2.1.2-4	Changed wording: awaked --> woken up Deleted reference to 2.1.1-1
1V1_117	2.1.2-5	Changed wording: awaked --> woken up Deleted reference to 2.1.1-1
1V1_118	2.8.3-1a	Deleted "Motification Matrix empty" as starting condition
1V1_119	2.8.3-1a 2.8.3-1b 2.8.3-5 2.8.3-7	- Added note "check only entries made by the tester" - Added note "The test has to be performed with every single, notification supporting FBlock and functon of the DUT"
1V1_120	2.8.3-1a	Test procedure changed: "Config(ok)" relevant part of test deleted.
1V1_121	2.8.3.10	Added note: "Test performed with all FBlocks except NetBlock and FBlock ET"
1V1_122	2.8.3-1a	Added note: "Test is based on generation of NCEs so the DUT has to check the SystemConfiguration on each NCE and the tester 2 has to set the FBlocks and address accordingly."

Change Ref.	Section	Changes
1V1_123	2.4.1-9	Changed test procedure: Deleted part with ConfigStateOk Value of intrest: t_WaitAfterNCE(max)
1V1_124	2.1.1-4	Changed "DUT not ok (1)": --> "No signal from DUT received"
1V1_125	2.3.1-3	Added "too late" to "DUT not ok (3)" Introduced timeout with 1 minute to avoid deadlock in case of ShutDown(Query) is continuously sent by the DUT
1V1_126	2.1.1-1	Timeout t_wakeup(min) deleted

Changes MOST Core Compliance Test Specification 1V1-00 to MOST Core Compliance Test Specification 1V1-01

Change Ref.	Section	Changes
1V1.1_001	2.1.1.3	Changed "write 0x05 into the SBC register" into ""write a value greater than 0x05 into the SBC register""
1V1.1_002	2.1.1-1	Test also applicable for slave devices
1V1.1_003	2.1.1-4	Usage of t_manufacturer_0x00 instead of t_manufacturer_0x01
1V1.1_004	2.1.1-4 2.1.3-1 2.1.3-4 2.4.1-1 2.4.1-2	Tests only applicable if DUT is able to perform active wakeups. Extened test procedure: <ol style="list-style-type: none"> 1. Check whether DUT is able to perform active wakeup 2. Set AbilityToWake=TRUE 3. Trigger AutoWakeup via FB ET
1V1.1_005	2.1.2-5	Note in FlowChart updated regarding value of SBC register (it must not change to value greater than "0x05")
1V1.1_006	2.1.3-1	Note in FlowChart regarding to t_manufacturer updated (t_manufacturer changed to t_manufacturer_0x00)
1V1.1_007	2.2.1-3	FlowChart updated: Exchange t_unlock with t_unlock_max
1V1.1_008	2.2.1-4	FlowChart updated: Exchange t_unlock with t_unlock_min
1V1.1_009	2.2.1-5	Note added that the second part of test (t_restart) not applicable if DUT supports neither wake-up via MOST nor electrical wake-up.
1V1.1_010	2.3.1.5	Exchanged t_ShutDown with t_ShutDownWait
1V1.1_011	2.3.1-3	FlowChart updated: A permanently prevented shutdown results in "DUT not ok" as a started shutdown has to be finished in any case.
1V1.1_012	2.3.1-6	Test updated: t_RetryShutDown_min and t_RetryShutDown_max will be checked
1V1.1_013	2.3.2-2	<ul style="list-style-type: none"> - Typo in test description corrected - Test will be performed with any device except PM
1V1.1_014	2.3.3-3	Test case deleted from Core Compliance Test Spec. It will be considered again if new behaviour of AbilityToWake" will be considered by MOST Spec and FB ET
1V1.1_015	3.1	General introduction of deadlock prevention timeouts
1V1.1_016	2.3.3-4	FlowChart updated: Timeout t_DeadLockMid for "MOST signal off" and "MOST signal on".
1V1.1_017	2.4.1-1	Added "(4) wait for t_restart" to note for wake-up attempt in FlowChart
1V1.1_018	2.4.1-11	Test description updated: <ul style="list-style-type: none"> - Exchanged "period of time of 200ms" with "t_bypass" - Added note how to detect whether DUT opens / closes bypass
1V1.1_019	2.4.1-2	<ul style="list-style-type: none"> - Added "(4) wait for t_restart" to note for wake-up attempt in FlowChart - Deleted "t_restart" from value of interest

Change Ref.	Section	Changes
1V1.1_020	2.8.4-2	Using FB ET "EchoMessage" to trigger segmented message from DUT
1V1.1_021	2.6.1-1	Added note to disconnect DUT for at least 1 minute from power
1V1.1_022	2.6.2-4b	Added Note for testing: There have to be two FBlockID.Status Messages provided to the DUT. The content of the first has to fit into the CR. The second has to add some Fblocks which exceed the size of the CR. Added Note in FlowChart, that DUT has to keep existing entries and ignore new devices. Added Note in FlowChart, that tester has to take into account Fblocks of DUT, already stored into the CR.
1V1.1_023	2.6.2-3a	Note in FlowChart updated, according to usage of "uninitialized_node_address".
1V1.1_024	2.6.2-3b	FlowChart updated: Testprocedure changed (testloop: timeout instead of counter)
1V1.1_025	2.6.2-6	FlowChart and Test description updated: Tester 1 and tester 2 have to use the same Fblock-ID and Inst-ID. Tester 1 in master mode or slave mode; depends on DUT DUT could send FBlockID(SetGet) to tester 1 or tester 2
1V1.1_026	2.6.3-1	FlowChart updated: Config.Status(Invalid) has to be received within 1s
1V1.1_027	3.1	Definition of general procedures
1V1.1_028	2.6.4-1	At the beginning of the test, the address of the DUT will be set.
1V1.1_029	2.6.4-3	Test has to be performed three times (using "Ok", "New" and "Invalid") Test checks, whether DUT uses correct logical address of tester.
1V1.1_030	2.6.4-4	- Changed note in FlowChart: "The position of the DUT has to be changed". - Now, test case takes into account that suppliers could use static addresses in dynamic address range. - Dynamic address range changed to "0x0100 - 0x013F"
1V1.1_031	2.6.4-8	Changed t_answer(50ms) to t_answer
1V1.1_032	2.8.1-1	FlowChart changed: SendShutDownSuspend(Set) exchanged with ShutDown.Result(Suspend)
1V1.1_033	2.8.4-1	FlowChart: Stated last query more clearly.
1V1.1_034	2.8.4-3	Added note that if sending the first part of the message again, msgcnt has to be "0".
1V1.1_035	2.3.3-1 2.6.1-1 2.6.2-1 2.6.2-3a 2.6.2-3b 2.6.3-1	Exp. Setup: Tester 1 in slave mode or in master mode (depends on DUT)
1V1.1_036	2.6.3-2	New testcase added (InstID test)
1V1.1_037	2.6.4-9	New testcase added (control message suppression test)
1V1.1_038	2.8.1-2	New testcase added (Stress test)
1V1.1_039	2.8.4-9	New testcase added (Broadcast error test)
1V1.1_040	all	Added test case number to field "Name of test" of any test case.
1V1.1_041	all relevant	Application of deadlock timeouts into FlowCharts
1V1.1_042	2.5-2 2.5-7 2.5-8	Exchanged "PowerOff mode" and "RBD finished" with "MOST signal off".

Change Ref.	Section	Changes
1V1.1_043	2.5-2	FlowChart: Changed "start timer (t_DiagSlave / t_DiagMaster)" to "start timer (t_DiagSlave / t_DiagMaster) + t_PowerSwitchOffDelay(max)". Deleted hint that test case 2.5-7 could be performed during the test Deleted "lock" in second to last query
1V1.1_044	2.5-7	Added t_DiagMaster to value of interest. Deleted note that test could be performed during test 2.5-2 Added note that test is not applicable if DUT does not support wake-up via MOST interface or electrical wake-up
1V1.1_045	2.7-1	Added note, that DUT has to send valid message. The senders address and the property "node address" have to be correct.
1V1.1_046	2.8.3-1a	Test updated: Now, Config(ok) during SystemState(ok) will be tested, too. DUT not ok(5) changed to DUT not ok(6); new DUT ok(5) introduced. Timeout for "error": t_response
1V1.1_047	2.8.3-1b	FlowChart updated: Timeout for "error": t_response
1V1.1_048	2.8.3-1a 2.8.3-1b 2.8.3-7	Added note: All FBlocks have to be tested within one testloop (no separate testloop for every FBlock) to increase stress of DUT.
1V1.1_049	2.8.3-5	Test case deleted from test spec. Not applicable.
1V1.1_050	2.8.3-7	FlowChart updated: Timeout for reception of acknowledge: t_property. Last part (last query; DUT not ok(6)) of test deleted. Note added to "receive acknowledge": Reception of first (or single) segment of first status or error message)
1V1.1_051	2.8.3-10	Changed "Notification.All" to "Notification.Set(All)
1V1.1_052	2.1.1-1	Note added: Autowakeup not tested by this test
1V1.1_053	2.5-2	Added "t_DiagMaster" and "t_PowerSwitchOffDelay" to value of interest
1V1.1_054	2.5-8	FlowChart updated: New procedure to trigger RBD.
1V1.1_055	2.5-7	FlowChart updated: New procedure to trigger RBD. At the beginning of the test, the ring is closed.
1V1.1_056	2.6.2-6	FlowChart Updated: Changed "Tester 1: send ... as Tester 1" to "Tester 1: send ... as Tester 2" Test description updated: Changed "...try to change the Fblock-ID..." to "try to change the InstID of the Fblock..." Test updated: Additionally, the test now checks whether DUT stores tester 1 or tester 2 into the CR.
1V1.1_057	2.5-3	New test case added (TM RBD procedure test)
1V1.1_058	2.4.1-9	Test updated. Now t_WaitAfterNCE(min) will be checked, too.

Change Ref.	Section	Changes
1V1.1_059	2.1.1-2 2.1.2-5 2.3.3-1 2.6.2-1 2.6.2-3a 2.6.2-3b 2.6.2-4a 2.6.2-4b 2.6.2-6 2.6.4-3 2.6.4-8	Changed start condition from "DUT in SleepMode" to "DUT: NetInterface PowerOff"
1V1.1_060	2.4.2-3	More precise test description regarding to change of voltage levels.
1V1.1_061	2.6.2-1	<ul style="list-style-type: none"> - Added to Result "DUT not ok (2): "... or checks devices in wrong order" - Added note, that in case DUT is NWM but not TM, it has to check tester 1 before tester 2 (devices has to be checkt in ring position order)
1V1.1_062	2.6.4-1	Added note to "disconnect DUT from power until buffer capacitors are empty", that DUT has to be disconnected for at least one minute.
1V1.1_063	2.1.1-4	Deleted note that wake-up procedure is not dictated. Changed wording in test description: "... able to perform active wake-ups ..." → "... able to perform actively wake-ups ..."
1V1.1_064	2.1.1-5	t_wakeup(max) considered
1V1.1_065	2.1.2-5	FlowChart updated: t_DeadLockShort introduced
1V1.1_066	2.2.1-4	Test updated: Now it will be checked whether DUT switches off MOST signal too early or too late.
1V1.1_067	2.4.2-1	FlowChart updated: Now, the current voltage level will be checked.
1V1.1_068	2.6.2-3a	Added "yes" to second last query.
1V1.1_069	2.6.2-4b	FlowChart and Experimental setup changed: second tester introduced.
1V1.1_070	2.8.1-1	Added note that variation of exp. setup is allowed (regarding to additionally spy device)
1V1.1_071	2.8.1-2	Added note that variation of exp. setup is allowed (regarding to additionally spy device)
1V1.1_072	2.8.4-7	Now, DUTs with dyn buffer size will be tested, too (with reduced test procedure)
1V1.1_073	2.6.2-3a	FlowChart changed (Loop=2)
1V1.1_074	1.1.1	Changed "MOST device" into "MOST node"
1V1.1_075	2.1.1-1	Test only applicable if DUT is optical wakeable
1V1.1_076	2.1.3-4	Changed t_restart to t_restart(max)
1V1.1_077	2.2.1-3	Changed t_unlock_max to t_unlock_typical Changed t_lock to t_lock_max
1V1.1_078	2.2.1-4	Changed t_unlock_min to t_unlock_typical Changed t_lock to t_lock_min
1V1.1_079	2.3.1-3	Test procedure updated. <ul style="list-style-type: none"> - Check whether MOST signal will be switched off within t_ShutDown(min) and t_ShutDown(max). - During ShutDown.Start(Execute) loop, ShutDown.Start(Query) no longer checked as shutdown via FB ET must not be prevented from internal processes of DUT.
1V1.1_080	2.3.3-1	Changed "under voltage level" to "U_Low". Changed "over voltage level" to "U_Super". Now, the test has to be performed twice. In one loop the voltage levels will be changed via square edge function and once with ramps.
1V1.1_081	2.3.3-4	FlowChart: Timeout "t_DeadLockMid" introduced to first inquiry
1V1.1_082	2.4.1-1	FlowChart: Changed timeout for wake-up from "t_slave / t_master" to "t_slave / t_master + t_restart".
1V1.1_083	2.4.1-2	FlowChart: Changed timeout for wake-up from "t_slave / t_master" to "t_slave / t_master + t_restart".

Change Ref.	Section	Changes
1V1.1_084	2.3.2-1	Test description: Changed "... until the DUT indicates U_Critical" to "... to U_Critical". Changed "... until the DUT indicates U_Normal" to "... to U_Normal".
1V1.1_085	2.5-2	FlowChart: Deleted note in loop while waiting for RBD.
1V1.1_086	2.5-3	Test description: Now, the ring will be interrupted between tester 2 and tester 1.
1V1.1_087	2.6.2-4b	- Behavior of Tester 1 and Tester 2 described more precisely. - At the end of the test, it will only be checked that maximum number of FBlock are stored in CR of DUT. It will not checked if FBlocks of tester 1 or tester 2 are stored. - Note added that manufacturer has to provide the maximum delay, the DUT could register own FBlocks.
1V1.1_088	2.6.2-6	Note added that check of tester 1 and tester 2 could be overlapped. Now, the DUT has to check tester 1 and tester 2 in correct order during system scan.
1V1.1_089	2.6.3-2	Test renamed to "NWM InstID test".
1V1.1_090	2.6.4-4	TM excluded for this test
1V1.1_091	2.7-1	Added note that ring position of DUT will only be changed if DUT is not TM.
1V1.1_092	2.8.1-2	Test deleted
1V1.1_093	2.8.3-1a 2.8.3-1b 2.8.3-7	Added query to check whether notification error cause by error "temporarily not available" (Notification.Error(0x41)). In that case the DUT is not testable.
1V1.1_094	2.8.3-7	Test results: Changed "200ms" to "t_property"
1V1.1_095	2.8.3-10	a) Note added: Test not to be performed with FBlock PM b) If DUT respond with an error unequal "notification error" during notification, it will pass the test as the addressed FBlock does not support notification.
1V1.1_096	2.8.4-1	a) changed t_garbage to t_DeadLockLong b) changed t_timeout to t_property c) Last part of test deleted as t_garbage no longer exists in MOST Specification 2V3
1V1.1_097	2.8.4-3	Changed t_timeout to t_property
1V1.1_098	2.8.4-9	a) Changed "... FBET..." to "...ET..." b) Changed "DUT not ok (1)" to "DUT not ok"
1V1.1_099	Appendix 1	Timer table updated
1V1.1_100	2.8.3-1a 2.8.3-1b	Error received implies also a timeout DUT does not respond at all
1V1.1_101	para 2	Renamed to "General Items" Deleted definition of Core Functions. Moved "Definitions" from para 1 to para 2
1V1.1_102	para 3	Added reference to MOST Specification to any test case
1V1.1_103	2.6.2-3b	Testcase revised (dynamic test interval of NWM considered)
1V1.1_104	Bibliography	Added "General FBlock" to document list
1V1.1_105	Appendix 1	Measurement uncertainty of t_PwrSwitchOffDelay changed from $\pm 100\text{ms}$ to $\pm 500\text{ms}$
1V1.1_106	3.1	Default address of tester is default logical address corresponding to ring position
1V1.1_107	2.1.3	Added definition of change-levels of DUT
1V1.1_108	2.1.4	Added definition of Multi Node Devices
1V1.1_109	all relevant	Removed usage of "should" for mandatory definitions. Exchanged with "must". Deleted "Definition of should" from para 3.1
1V1.1_110	all test cases	Info for Multi Node Devices added
1V1.1_111	2.6.3-2	Test case "NWM InstID test" deleted

Change Ref.	Section	Changes
1V1.1_112	2.2.1-5	Note added: "If DUT is PM, t_restart_max must not be considered"
1V1.1_113	2.3.1-6	FlowChart updated: t_min will be started before sending ShutDown.Result(Suspend)
1V1.1_114	2.6.2-3a	Test description updated: The DUT has to check the tester two more times.
1V1.1_115	2.6.2-3b	Added note to behaviour of Tester for NAK generation resp. no respons to DUT. FlowChart updated: Tester send at least one FBlock to DUT at next check. Then it will wait for any Config.State before checking the CR of the DUT.
1V1.1_116	2.6.4-3	FlowChart updated: Now it will be checked whether DUT behaves inconsistent regarding to requesting the CR. The second note for sending Config.Status has been changed to "Send same Config.Status as above".
1V1.1_117	2.6.4-9	Deleted note " Test limited to 15s. After 15s, DUT potentially uses own address and starts sending.". This is covered by t_DeadLockMid in FlowChart.
1V1.1_118	2.8.1-1	FlowChart updated: The loop over t_test now starts with starting of timer ACK_timer.
1V1.1_119	2.8.3-1a	FlowChart updated: Second last check of notification matrix will only be done if DUT sends Config.Status(ok). Query "DUT supports notification?" changed to "Error(0x20,0x20) or Error(0x03) received?". Note added to test results "DUT ok (3): "To be marked in test report". NetBlock, FBlock ET and FBlock PM will be excluded by the test.
1V1.1_120	2.8.3-1b	FlowChart updated: Query "DUT supports notification?" changed to "Error(0x20,0x20) or Error(0x03) received?". Note added to test results "DUT ok (3): "To be marked in test report". NetBlock, FBlock ET and FBlock PM will be excluded by the test.
1V1.1_121	2.8.3-7	FlowChart updated: Now, second loop of t_property identical to first loop of t_property. Note added to test results "DUT ok (3): "To be marked in test report". NetBlock, FBlock ET and FBlock PM will be excluded by the test. Numbering of DUT ok / DUT not ok changed.
1V1.1_122	2.8.4-1 2.8.4-3 2.8.4-7 2.8.4-8	Prescribed to use "NetBlock.FBlockIDs.Get" as test message.
1V1.1_123	2.8.4-7	Deleted note " Test only applicable for devices without dynamic buffer size (FBlock ET: MessageBufSize.MsgLengthRx <> 0x000)" as already covered by test procedure
1V1.1_124	2.8.4-9	Changed " ET.InstID(0x1).FktID(0x3c8).SetGet " to " ET.InstID(0x0).FktID(0x3c8).SetGet ". Added note "FktID 0x3C8 of FBlock ET is reserved and must not be implemented.
1V1.1_125	Appendix 1	Measure uncertainly changed of: t_slave(2.4.1-1): +-10ms t_master(2.4.1-2): +-10ms t_DiagSlave (2.5-8): +-200ms t_DiagMaster (2.5-8): +-200ms Added t_restart (+-5ms) to 2.4.1-1 and 2.4.1-2 Added t_DelayCfgRequest1 (+-20ms) to 2.6.2-3b Added t_DelayCfgRequest2 (+-200ms) to 2.6.2-3b
1V1.1_126	Appendix 2	minor changes
1V1.1_127	Appendix 3	new (MOST Core Compliance Test Plan List for Early Implementations)
1V1.1_128	Appendix 4	new (MOST Core Compliance Test Plan List for Devices of MOST Specification 2V2)
1V1.1_129	para 1	Reference to MOST Spev 2V3 and FB ET added.
1V1.1_130	all relevant	New template applied.
1V1.1_131	Appendix 4	Appendix is not informative

Change Ref.	Section	Changes
1V1.1_132	2.1.3-1	FlowChart updated: After trigger of wake-up, a shutdown has to be performed.
1V1.1_133	2.4.1-2	FlowChart updated: ">4" changed to ">3"; ">=4" changed to ">=3"
1V1.1_134	2.5-8	FlowChart updated: After trigger of RBD, a shutdown has to be performed. Test description updated: " Tester 1 has to behave like normal MOST device regarding to RBD procedure." " In the part of the test, the DUT performs RBD (between "Perform ShutDown" and "Perform Wake-up"), the tester has to be switched into the same mode as the DUT."
1V1.1_135	2.8.3-7	Last sentence of test description deleted.
1V1.1_136	2.8.3-10	FlowChart updated: Last "Notification.Get" has to be sent for a function that does not support notification".
1V1.1_137	2.1.1-1	Deleted note that the test has to consider all specific wake-up sources of the DUT.
1V1.1_138	2.1.1-5	Test updated: no more consideration of t_wakeup.
1V1.1_139	para 3.1	New definition for unlock generation: "Generate unlocks by destroying preamble"

Changes MOST Core Compliance Test Specification 1V1-01 to MOST Core Compliance Test Specification 1V2

Change Ref.	Section	Changes
1V2_001	Document References	References updated: MOST Dynamic Specification 1V2 MOST Specifcatin 2V4 MOST Function Block "Enhanced Testability" 2V5
1V2_002	2.1.2	Reference to FBlock ET updated (V2.5)
1V2_003	2.1.4	Typo corrected ("Multi Mode Devices" -> "Multi Node Devices")
1V2_004	3.2.2	Added blank page to ensure test cases start at odd page.
1V2_005	2.1.1-1	Added "MOST signal" to value of interest Replaced "optical" with "MOST signal"
1V2_006	2.1.1-1	Replaced t_wakeup with t_wakeup_max. t_test deleted (flowchart reworked)
1V2_007	2.1.1-2	Added t_Config to value of interest Replaced t_Master with t_Config Flowchart: t_master --> t_Config_max
1V2_008	2.1.1-4	Replaced "light" with "MOST signal" in test description Added MOST signal and t_manufacturer_0x00 to value of interest Replaced t_Master with t_Config
1V2_009	all	Replaced t...(max) with t..._max
1V2_010	2.1.1-5	Replaced "optically" with "MOST signal" in Note of test description
1V2_011	2.1.2-4	Added t_manufacturer_0x01 to value of interest Replaced t_Slave with t_Config
1V2_012	2.1.2-5	Replaced t_Slave with t_Config t_slave --> t_Config_max in test description t_waitnodes --> t_waitnodes_max in test description
1V2_013	2.1.2-6	t_shutdown --> t_shutdown_max in test description and some parts of the flowchart t_test deleted (flowchart reworked)

Change Ref.	Section	Changes
1V2_014	2.1.3-1	Added MOST signal and t_manufacturer_0x00 to value of interest Test description: t_shutdown --> t_shutdown_max Test description: t_restart --> t_restart_min
1V2_015	2.1.3-4	Added MOST signal and t_manufacturer_0x00 to value of interest Test description: t_slave --> t_Config_max Replaced t_Slave with t_Config
1V2_016	2.2.1-3	Added MOST signal to value of interest
1V2_017	2.2.1-4	Added MOST signal to value of interest Test description: t_unlock --> t_unlock_max
1V2_018	2.2.1-5	Added MOST signal to value of interest Test description: t_unlock --> t_unlock_max / t_unlock_min Test description: t_restart --> t_restart_min
1V2_019	2.3.1-3	Added MOST signal to value of interest Flowchart: t_suspend --> t_suspend_min Flowchart: Position change of "Start t_shutdownwait(max)" and "send shutdown.result(suspend) to DUT" Flowchart: If DUT fails to perform NO during t_suspend --> DUT not ok (5)
1V2_020	2.3.1-6	Flowchart: added note "NO has to be checked every 500ms" Flowchart: deleted t_min / t_max
1V2_021	2.3.2-2	Test description: t_suspend --> t_suspend_max Note added to meet manufacturer recommendation regarding to cooling. Flowchart: 2x t_suspend --> 2x t_suspend_typical Flowchart: t_suspend --> t_suspend_max Flowchart: t_test deleted (flowchart reworked)
1V2_022	2.3.2-3	Added t_PwrSwitchOffDelay and t_ShutDown to value of interest DUT not ok (2) changed: "DUT switched off MOST signal by itself" to "DUT switched off MOST signal by itself too early" t_PwrSwitchOffDelay --> t_PwrSwitchOffDelay_min 2x t_ShutDownWait --> 1x t_ShutDownWait_max t_shutdown --> t_shutdown_max Flowchart: t_test deleted (flowchart reworked)
1V2_023	2.3.3-1	PM not tested
1V2_024	2.3.3-4	Added NetBlock.ShutdownResult(0x03) to value of interest Deleted last part of test (ignoring of wake-ups during over temperature condition). Flowchart: Added note that communication test has to be performed within 500ms
1V2_025	2.4.1-1	Added t_config to value of interest Flowchart: Replaced t_slave / t_master with t_config_min resp. t_config_max
1V2_026	2.4.1-2	Added t_restart and t_config to value of interest Device type: PM --> "all devices with capability to wake" Flowchart: Replaced t_slave / t_master with t_config_min resp. t_config_max
1V2_027	2.4.1-9	Added ConfigStatus(Invalid) to value of interest. Flowchart: Deleted t_test in procedure "DUT checks system configuration" Test updated: DUT has to send empty FBlock list if no changes of FBlocks occur after NCE
1V2_028	2.4.1-11	Test description: t_bypass --> t_bypass_min
1V2_029	2.4.2-3	Added MOST signal to value of interest Test changed: DUT is allowed to perform wake-up as soon as U_Low is reached (U_Critical related part deleted) Flowchart: Note added to use higher value for U_low in case of hysteresis

Change Ref.	Section	Changes
1V2_030	2.5-2	<p>Added MOST signal to value of interest</p> <p>t_DiagResult deleted.</p> <p>t_DiagSlave / t_DiagMaster --> t_DiagSlave_max / t_DiagMaster_max</p> <p>Flowchart: Note added that DUT is allowed to switch off MOST signal in some situations (DUT not ok(4))</p> <p>Flowchart: Expanded procedure after second "trigger RBD". Now the tester switches off the MOST signal and waits for the DUT.</p> <p>Flowchart: Note added that tester must not start generating MOST signal before DUT does.</p> <p>Flowchart: Note added how to detect NO</p>
1V2_031	2.5-3	<p>Added MOST signal to value of interest</p> <p>Flowchart: Note added that DUT is allowed to switch off MOST signal in some situations (DUT not ok(4))</p> <p>Flowchart: Expanded procedure after second "trigger RBD". Now the tester switches off the MOST signal and waits for the DUT.</p> <p>Flowchart: Note added that tester must not start generating MOST signal before DUT does.</p> <p>Test case now to be performed with all devices (not only TM)</p> <p>t_DiagResult deleted.</p> <p>t_DiagSlave / t_DiagMaster --> t_DiagSlave_max / t_DiagMaster_max</p>
1V2_032	2.5-7	<p>Added MOST signal to value of interest</p> <p>Test description: t_DiagRestart --> t_DiagRestart_min</p>
1V2_033	2.5-8	<p>Added t_DiagSlave, t_DiagMaster and MOST signal to value of interest</p> <p>t_DiagSlave / t_DiagMaster --> t_DiagSlave_max / t_DiagMaster_max</p> <p>Flowchart: Replaced "perform shutdown" with "switch off MOST signal"</p> <p>Flowchart: Note added that tester must not start generating MOST signal before DUT does.</p> <p>Flowchart: Note added that DUT is allowed to switch off MOST signal for a short period of time in case of slave device</p>
1V2_034	2.6.2-1	<p>Added t_WaitForAnswer, FBlockIDs.Get and Configuration.Status(ok) to value of interest</p> <p>Flowchart: Note added that tester1 and tester2 have to be swapped if DUT polls tester1 before tester2</p> <p>DUT not ok (2): Deleted part " or checks devices in wrong order"</p> <p>Flowchart: t_waitForAnswer --> t_WaitForAnswer_min</p>
1V2_035	2.6.2-3a	Added Configuration.Status(NotOk) and Configuration.Status(Ok) to value of interest
1V2_036	2.6.2-3b	<p>Added t_WaitForAnswer to value of interest</p> <p>Flowchart: Loop starts with "1" as the number of retries are counted</p> <p>Flowchart: t_WaitForAnswer --> t_WaitForAnswer_min</p> <p>Flowchart: Inquiry t_DelayCfgRequest1(min) <= t_test <= t_DelayCfgRequest1(max) --> Inquiry t_WaitForAnswer_min + t_DelayCfgRequest1_min <= t_test <= t_WaitForAnswer_max + t_DelayCfgRequest1_max</p> <p>Flowchart: Inquiry t_DelayCfgRequest2(min) <= t_test <= t_DelayCfgRequest2(max) --> Inquiry t_WaitForAnswer_min + t_DelayCfgRequest2_min <= t_test <= t_WaitForAnswer_max + t_DelayCfgRequest2_max</p>
1V2_037	2.6.2-4a	Added FBlockIDs.Get and Configuration.Status(Ok) to value of interest
1V2_038	2.6.2-4b	Added Configuration.Status(ok/new) to value of interest
1V2_039	2.6.2-6	<p>Added FBlockIDs.Get and Configuration.Status(Ok) to value of interest</p> <p>Test changed: Now the tester will have "fixed FBlocks" and "normal FBlocks"</p> <p>Note added about implementation of "fix FBlocks" and "normal FBlocks"</p> <p>Flowchart: Check for "check sequence" deleted</p> <p>Flowchart: Now it will be checked if the "normal FBlocks" are stored into the CR</p> <p>Flowchart: Storage of no "fix FBlocks" --> DUT ok (2)</p>
1V2_040	2.6.3-1	<p>Added Configuration.Status(Invalid) to value of interest</p> <p>Flowchart: Changed loop while waiting for Config.Status(Invalid).</p>

Change Ref.	Section	Changes
1V2_041	2.6.4-3	Added "NWM address, used by DUT" and CentralRegistry.Get to value of interest
1V2_042	2.6.4-4	Added Address of DUT and CentralRegistry.Get to value of interest Exp. Setup: Tester 2 in master mode only as DUT is not allowed to be TM
1V2_043	2.6.4-8	t_answer --> t_answer_max
1V2_044	2.6.4-9	Value of interest: "Reception of control message" replaced with "Control message from DUT"
1V2_045	2.7-1	Value of interest: Node Position Address of DUT, Groupcast Address of DUT and Broadcast Address of DUT added
1V2_046	2.8.3-1a	Value of interest: t_property added Flowchart: t_response replaced by t_property "DUT ok (3)" deleted ("temporary not available" not allowed) Exception of Netblock deleted
1V2_047	2.8.3-1b	Value of interest: t_property added Flowchart: t_response replaced by t_property "DUT ok (3)" deleted ("temporary not available" not allowed) Exception of Netblock deleted
1V2_048	2.8.3-7	Value of interest: t_property added "DUT ok (2)" and "DUT ok (3)" deleted ("temporary not available" not allowed) Exception of Netblock deleted Test description: "The result has to be received within t_property" --> "The DUT has to answer within t_property"
1V2_049	2.8.3-10	Value of interest: Notification.Error added Get.Function.ID --> FktIDs.Get Test improved: Deletion of a not notified Function shall not cause an error message. Exception of Netblock deleted
1V2_050	2.8.4-1	Value of interest: Segmentation Error and t_property added Flowchart: Added note to use arbitrary data
1V2_051	2.8.4-2	Value of interest: Segmented message from DUT added
1V2_052	2.8.4-3	Value of interest: Segmentation Error and t_property added
1V2_053	2.8.4-7	Value of interest: Segmentation Error added Flowchart: "error received from DUT" --> "segmentation error received from DUT"
1V2_054	2.8.4-8	Value of interest: Pool overflow error added
1V2_055	2.8.4-9	Test improved: Not implemented function will be addressed normally and DUT has to respond with error.
1V2_056	Appendix 1	t_property added for 2.8.3-1a and 2.8.3-1b
1V2_057	Appendix 2	FktID 0x3C8: reserved --> void
1V2_058	para 3.1	DUT manufacturer information list added
1V2_059	para 3.1	Definition for "generate unlock" changed
1V2_060	para 3.1	Definition of "Perform wake-up" changed
1V2_061	para 3.1	DUT: NetInterfacePowerOff: Trigger AutoWakeUp via FB ET --> Trigger AutoWakeUp if required and applicable
1V2_062	para 3.1	"DUT in SleepMode": Note about to treat with devices, not supporting AbilityToWake
1V2_063	all	Names to timer matched to MOST Specification in terms of upper and lower case
1V2_064	2.6.4-8	Name of test changed: "FBlock Respond Time test" --> "FBlock Response Time test"
1V2_065	para 2.1.3	Definition of Level 1 changes improved: Only functional application changes within the same application (e.g., no influence on timing)
1V2_066	para 3.1	Added note about timer names, based on MOST Specification
1V2_067	2.3.2-2 para 3.2	Moved note about following manufacturer recommendation regarding to cooling from test 2.3.2-2 to para 3.2

Change Ref.	Section	Changes
1V2_068	all relevant	Deleted "MOST signal" from section "Value of interest"
1V2_069	2.3.3-4	Temperature management is optional. DUT ok (2) introduced for DUT that does not support temperature managemet.
1V2_070	2.4.1-2	Test applicable for PM only
1V2_071	2.5-2	Flowchart: In-between closing the ring and performing wakeup, the tester has to wait t_Diag_Restart_max More precise definition why DUT is allowed to switch off MOST signal for a short period of time during testing
1V2_072	2.5-3	Flowchart: In-between closing the ring and performing wakeup, the tester has to wait t_Diag_Restart_max More precise definition why DUT is allowed to switch off MOST signal for a short period of time during testing
1V2_073	2.5-7	Flowchart: In-between closing the ring and performing wakeup, the tester has to wait t_Diag_Restart_max
1V2_074	2.5-8	More precise definition why DUT is allowed to switch off MOST signal for a short period of time during testing
1V2_075	2.1.1-4	Flowchart: Use feedback from DUT when trigger AutoWakeup via FB ET. If DUT returns error 0x07, the DUT has no capability to wake.
1V2_076	2.1.3-1	Flowchart: Use feedback from DUT when trigger AutoWakeup via FB ET. If DUT returns error 0x07, the DUT has no capability to wake.
1V2_077	2.1.3-4	Flowchart: Use feedback from DUT when trigger AutoWakeup via FB ET. If DUT returns error 0x07, the DUT has no capability to wake.
1V2_078	2.4.1-1	Flowchart: Use feedback from DUT when trigger AutoWakeup via FB ET. If DUT returns error 0x07, the DUT has no capability to wake.
1V2_079	2.4.1-2	Flowchart: Use feedback from DUT when trigger AutoWakeup via FB ET. If DUT returns error 0x07, the DUT has no capability to wake.
1V2_080	2.6.4-1	FlowChart: Wait t_DeadLockMid before disconnecting the DUT from power.
1V2_081	2.1.0-1	New test case (Generic FBlock Property test)
1V2_082	2.1.0-2	New test case (Generic FBlock Method test)
1V2_083	2.3.3-5	New test case (Temperature ShutDown Reahtion test)
1V2_084	2.6.2-5	New test case (Config(new) Order test)
1V2_085	2.6.2-7	New test case (InstID Wildcard test)
1V2_086	2.6.4-10	New test case (NotificationCheck test)
1V2_087	2.8.3-2	New test case (Config(ok) Delay test)
1V2_088	para 3.12	Extended Core (Source/Sink) test cases included
1V2_089	para 3.1	New procedure: "Trigger RBD"
1V2_090	para 2.1.4	Definition of "family" in context with core compliance
1V2_091	para 3.1	Reference to t_manufacturer_0x01 deleted (obsolete; replaced by t_Boundary)
1V2_092	para 3.1	Deleted "value of SBC register for NO" from Manufacturer Information List
1V2_093	para 3.1	Procedure "Trigger RBD": parameter "Diagnosis" added
1V2_094	2.1.0-1 2.1.0-2	Added reference to MOST Specification para 2.3.2.5 OPTYPE
1V2_095	2.1.2-4	t_manufacturer_0x01 substituted by t_Boundary
1V2_096	2.3.2-2	Test updated. Now a DUT that does not support ShutDown.Suspend mechanism will pass the test with DUT ok (2).
1V2_097	2.3.3-5	Test updated. Now only t_WaitAfterOvertempShutDown will be tested
1V2_098	2.5-2 2.5-3 2.5-8	Reference to t_off deleted as timer no longer exists in MOST Specification
1V2_099	2.5-8	RBD-Results not consider Multinode devices containing Timing Master and Timing Slave.
1V2_100	2.6.4-1	Test updated: After disconnecting from power and wake-up, the DUT is allowed to use "uninitialized_node_address" or any other node address from static or dynamic range.

Change Ref.	Section	Changes
1V2_101	2.6.4-3	Note added to send Config.Status(New) and Config.Status(Invalid) with empty FBlock list. Note added to switch off MOST signal between test loops without sending ShutDown.Start(Execute)
1V2_102	2.6.4-10	Typo in test description: "mut" --> "must"
1V2_103	2.8.3-1a	Typo in flowchart: "t_DeadockMid" --> "t_DeadLockMid"
1V2_104	2.8.4-7	Flowchart: Added note to last inquiry. Segmentation error (0x02) is allowed if DUT has dynamic buffer size.
1V2_105	Appendix 1	New timer measurement uncertainty: t_ProcessingDefault1= +-20ms t_WaitAfterOvertemShutdown= +-500ms t_WaitBeforeScan= +-20ms t_WaitAfterNCE= +-30ms
1V2_106	Appendix 3	Test cases 2.8.4-1 and 2.8.4-3 not relevant for early implementors
1V2_107	Appendix 4	Test cases 2.8.4-1 and 2.8.4-3 not relevant for MOST Specification 2V2. Test 2.1.2-4: t:manufacturer --> t_Boundary
1V2_108	Introduction	References to MOST Specification updated
1V2_109	2.6.4-1	Now, the case that the address cannot be set will be tolerated (note added)
1V2_110	2.6.4-3	Test updated: Now, FBlock.InstID 0xC8.0x01 has to be used for loop with Configuration.Status(Invalid). DUT is allowed to behave different in the test loops regarding CR request. If DUT does not request CR within t_DeadLockShort --> DUT ok.
1V2_111	2.8.3-1a 2.8.3-1b	More precise definition of observed error message from DUT (Notification.Error)
1V2_112	2.1.0-1 2.1.0-2	Added reference to test TEST_GSI_GSO_Identification for Sink/Source identification. More precise definition, which FktIDs have to be tested
1V2_113	2.1.0-2	Moved note about handling of "processing" no note section of test description.
1V2_114	2.2.1-3 2.2.1-4 2.2.1-5	Replaced description for unlock generation with reference to para 3.1 (General Notes)
1V2_115	all relevant	Replaced FCat by FBlock Library
1V2_116	2.3.2-2	FlowChart: More precise definition of observed error message from DUT ("ET.ShutDownSuspendMode.Error") at first inquiry.
1V2_117	2.1.1-5	Device Typ: All devices that are wakeable via MOST signal
1V2_118	para 3.1	DUT:Normal Operation; DUT is NWM: Additionally, the tester has to wait for relevant node opens its bypass (MPR = MPR_nominal) in case the DUT is a Multi Node Device.
1V2_119	2.1.1-5	Replaced "MPR>1" with "MPR = MPR_Nominal"

Changes MOST Core Compliance Test Specification 1V2 to MOST Core Compliance Test Specification 1V3

Change Ref.	Section	Changes
1V3_001	all tests	Add indication for every test to be applicable for: [x] MOST25 [x] MOST50 [x] oPHY [x] ePHY
1V3_002	Document reference; para 1	Reference to MOST Specification Documents updated

Change Ref.	Section	Changes
1V3_003	all relevant	"All-bypass" --> "bypass"
1V3_004	2.1.1-5	Name of test changed: "Bypass test (all-bypass)" --> "Bypass test"
1V3_005	para 2.1.5	Secondary nodes only supported by MOST25
1V3_006	3.2-3 3.2-4 3.2-5 3.2-6 3.2-7 3.2-8 3.2-9	Resource.Allocate not supported by MOST50
1V3_007	Document References	Reference to Dynamic Specification deleted
1V3_008	para 3.1	Replaced "AbilityToWake" with "PermissionToWake" in sub chapter "wake-up"
1V3_009	para 3.1	Deleted " Device supports ability to wake (in sense of capability):" from manufacturer information table. This information now is provided by "CapabilityToWake"
1V3_010	para 3.1	Deleted " DUT has implemented the "AbilityToWake" from manufacturer information table. PermissionToWake is mandatory in MOST Specification 2V5
1V3_011	para 3.1	Replaced "AbilityToWake" with "PermissionToWake" in "States of DUT: SleepMode". Also deleted workaround for devices, not supporting AbilityToWake.
1V3_012	2.1.1-4	Replaced "AbilityToWake" with "PermissionToWake". Introduced "CapabilityToWake"
1V3_013	2.1.3-1	Replaced "AbilityToWake" with "PermissionToWake". Introduced "CapabilityToWake"
1V3_014	2.1.3-4	Replaced "AbilityToWake" with "PermissionToWake". Introduced "CapabilityToWake"
1V3_015	2.4.1-1	Replaced "AbilityToWake" with "PermissionToWake". Introduced "CapabilityToWake"
1V3_016	2.4.1-2	Replaced "AbilityToWake" with "PermissionToWake". Introduced "CapabilityToWake"
1V3_017	3.2-3 3.2-4 3.2-5 3.2-6 3.2-7 3.2-8 3.2-9	Added special MCSs for MOST50 devices without Resource.Allocate messages
1V3_018	all	New structure of chapters and test cases
1V3_019	2.8.3-7	Test to be performed with all devices (flow chart updated accordingly; based on 2.8.3-1a) Test result "DUT ok (1)" added for DUT that does not support notification. (1V2_121)
1V3_020	2.8.3-2	At the beginnig of the test, it will be checked whether the DUT supports NotificationCheck by requesting the FktID list. (1V2_123)
1V3_021	2.1.3-1	Test description updated: The test has to be performed even if the DUT is not able to perform active wake-ups. (1V2_124)
1V3_022	2.1.3-4	Test description updated: The test has to be performed even if the DUT is not able to perform active wake-ups. (1V2_125)

Change Ref.	Section	Changes
1V3_023	2.1.1-4	Test description updated: The test has to be performed even if the DUT is not able to perform active wake-ups. (1V2_126)
1V3_024	2.4.1-1	Test description updated: The test has to be performed even if the DUT is not able to perform active wake-ups. (1V2_127)
1V3_025	2.4.1-2	Test description updated: The test has to be performed even if the DUT is not able to perform active wake-ups. (1V2_128)
1V3_026	Appendix 3	Deleted (1V2_129)
1V3_027	Appendix 4	Deleted (1V2_130)
1V3_028	Document History	Older document history moved to the end of the document (Appendix 3) (1V2_131)
1V3_029	3.2-3	Note added "Bandwidth constraints must be considered when beginning with next source number." (1V2_132)
1V3_030	para 3.1	Manufacturer information list updated: <ul style="list-style-type: none"> - Item deleted "DUT supports notification" - Item added "List of FBlocks, containing sink and/or source functionality" - Item added "Sink devices: List of all supported sink numbers with DataType and DataDescription " - Item added "Source devices: List of all supported source numbers with DataType and DataDescription " - Item added "Source devices: ChannelList" - Item added "Source devices: DUT supports allocate mechanism" - Item added "Source device: DUT supports source connect mechanism" - Item added "DUT supports SourceActivity" (1V2_133)
1V3_031	3.2-1	Second comment deleted in MSC (1V2_134)
1V3_032	3.1-1 3.1-3 3.1-4 3.1-5 3.1-6 3.1-8	Replaced "any SinkNumber" by "every single SinkNumber" (1V2_135)
1V3_033	3.2-1 3.2-3 3.2-4 3.2-5 3.2-6 3.2-10 3.2-11 3.2-14	Replaced "any SourceNumber" by "every single SourceNumber" (1V2_136)
1V3_034	3.0-1	Replaced "any FBlock" by "every single FBlock" in note (1V2_137)
1V3_035	2.6.4-10 2.8.3-2	Replaced "any registered FBlock" with "every single registered FBlock" in note (1V2_138)
1V3_036	para 3.12	Statement " DUT, containing CM not to be tested as test results potentially not representative " deleted. Excluding CM devices will be done for every relevant test case individually. (1V2_139)

Change Ref.	Section	Changes
1V3_037	para 3.11.4	New para inserted "Source / Sink Identification" (1V2_140)
1V3_038	3.0-1	Test moved to para 3.11.4 "Source / Sink Identification" (1V2_141)
1V3_039	para 3.11.4 para 3.12	Rows "Device type" and "Multi Node Device" added to every test description (1V2_142)
1V3_040	3.1-3 3.1-4 3.1-5 3.1-6 3.1-8 3.2-3 3.2-4 3.2-5 3.2-6 3.2-7 3.2-8 3.2-9 3.2-10 3.2-11 3.2-12 3.2-13 3.2-14	Test only applicable if DUT does not contain CM (1V2_143)
1V3_041	2.8.3-1a 2.8.3-1b 2.8.3-7 2.8.3-10	The test has to be performed with every single registered FBlock and function of the DUT (1V2_144)
1V3_042	2.8.3-10	Note added in flow chart to "Send Notification.Set(Clear) for an existing FktID to DUT": "Use a function that supports notification."
1V3_043	2.4.1-11	Note added in flow chart: "The DUT must not switch off its MOST signal during the test. Otherwise the MPR-register would not be valid anymore."
1V3_044	3.1-3	Note added: "The tester has to ensure that the channels to which a sink has to connect to are in use by a source. (Reason: In some test cases the tester sends the message "Connect.StartResult" with a ChannelList to the DUT (sink device) to trigger the DUT to connect to the channel. Practice has shown that some sink devices check the usage of the channels they have to connect to before performing the connection. Only if the TM indicates the channels as allocated by a source, the sink device will connect. This behaviour is neither prescribed nor forbidden by the MOST Specification and has to be tolerated."
1V3_045	2.1.0-1 2.1.0-2	Replaced " FktID: All FktIDs reported by DUT (FktID.Status) and also listed in relevant FBlock Library (NWM, NetBlock or GeneralFBlock) of MOSTCo as well." by " FktID: All FktIDs reported by DUT (FktID.Status) and also listed in relevant FBlock Library (NWM, GeneralFBlock) of MOSTCo as well. Additionally, All FCat Fkts. will be checked of NetBlock." Note added: "For NetBlock, Error 0x03 (Fkt. not available) is also permitted as answer"
1V3_046	all sink/source test cases	Note added: "Timeout "t_CM_DeadLockPrev" is valid for whole MSC."
1V3_047	2.8.3-10	Added note in description: "All FBlocks have to be tested within one test loop (no separate test loop for every FBlock) to increase stress of DUT." Add

Change Ref.	Section	Changes
1V3_048	3.2-3 3.2-4 3.2-5 3.2-6 3.2-7 3.2-8 3.2-9	Added Note in description to "Spy/Resource(De)Allocate with Grantinfo + ChannelList for DUT = TM": In some cases the Spy device is not able to evaluate ChannelList, Status and Label from the ResourceAllocate message of DUT (e.g. DUT = TM). In that case the Spy and the Tester has to assume correct values to ensure the DUT does not fail the test due to technical restrictions of test setup. The test result has to be marked accordingly. Nevertheless, the DUT will fail the test if it fails to send any Resource(De)Allocate message to TM.
1V3_049	3.2-7	MSC updated: Message "Allocate.Error": - ErrorCode 0x06 allowed, too - ErrorInfo: don't care
1V3_050	para 2.1.2	Added hint how to handle wrong figures returned by FB ET.
1V3_051	2.2.1-4	Test procedure (timings and counter) updated. Timing diagram added for clarification.
1V3_052	2.4.1-11	Check for t_bypass_max deleted
1V3_053	2.5-2 2.5-3	Flowchart updated: Exchanged "Close the ring, wait for t_DiagRestart_max and perform wake-up" with "Close the ring, perform wake-up and wait until DUT switches on its MOST signal within t_diag_restart(max)".
1V3_054	2.5-2 2.5-3	Flowchart updated: Before 2. trigger of RBD, the power has to be switched off and the DUT has to enter NormalOperation.
1V3_055	2.5-7	Flowchart updated: Block "Wait for t_DiagRestart_max" deleted.
1V3_056	2.6.4-4	Flowchart updated: Reference to test case 2.6.4-3 deleted. Note added how to check whether the DUT supports DR.
1V3_057	2.8.1-1	Test case deleted
1V3_058	2.8.3-1a 2.8.3-1b 2.8.3-2 2.8.3-7 2.8.3-10	Flowchart updated: Added note at the beginning of the test to wait for manufacturer dependent time until the property is available.
1V3_059	para 3.1	Added " Delay until properties are available after Configuratin.Status(Ok)" into manufacturer information list.
1V3_060	2.8.4-2	Improved test procedure to consider DUT with a small buffer size.
1V3_061	2.5-2 2.5-3 2.5-7 2.5-8	The reported node position of the RBD result has to be ignored.
1V3_062	2.2.1-4	Test updated. Now, an lock/unlock sequence will be generated permanently. Counter obsolete; timer used instead.
1V3_063	2.8.1-1	New ACK/NACH test introduced
1V3_064	2.8.3-10	Added note in FlowChart: "In case all functions support notification, skip this part and go directly to "Send Notification.Set(ClearAll) to DUT"
1V3_065	2.5-8	Deleted note in FlowChart about Diag_M2 Test not applicable for DUT=TM Test renamed to "RBD missing TM test" FlowChart updated "perform wakeup" --> "perform wakeup after t_Diag_Slave_max"
1V3_066	2.8.4-7	Test updated: Use message "ET.EchoMessage(0x208).Data" instead of NetBlock.FBlockIDs.Get()
1V3_067	para 3.1	added procedure description how to check whether DUT supports CapabilityToWake

