

MOST

Media Oriented Systems Transport

Multimedia and Control
Networking Technology

Core Compliance Test Specification

ERRATA SHEET

Rev. 1.3.8

11/2014

MOSTCO CONFIDENTIAL

See page 3 for the terms of disclosure



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Bibliography

Number	Document
[1]	MOST Specification Framework
[2]	MOST Specification
[3]	MOST High Protocol Specification
[8]	MOST FunctionCatalog
[9]	MOST Specification Of Physical Layer
[10]	MOST Compliance Test of Physical Layer
[11]	MOST Compliance Requirements
[12]	MOST Core Compliance Test Specification

Document History

Changes from 1V3-07 to 1V3-08

Change Ref.	Section	Changes
1V3-08_01	2.1 / 2.2	2.6.2-3a Device Ignore Test (a): (1) added
1V3-08_02	2.1 / 2.2	2.1.1-5 Bypass Test: (1) added

Note: All changes are marked with blue text color.

Changes from 1V3-06 to 1V3-07

Change Ref.	Section	Changes
1V3-07_01	2	Introduction of separate chapters for Core Compliance 1.1.1 and one for Core Compliance 1.3.1
1V3-07_02	2.2	2.6.2-7 Config(New) Order Test: (1) added
1V3-07_03	2.2	2.6.4-9 Control Message Suppression Test: (1) added
1V3-07_04	2.2	3.2-14 TEST_GSO_SourceActivity: (1) added
1V3-07_05	2.2	Appendix 2: (1) added
1V3-07_06	2.2	GEN 1 added: Guideline „Improved Product ID Handling“
1V3-07_07	2.2	GEN 2 added: Section “Multi Node Devices”
1V3-07_08	2.2	GEN 3 added: not reaching preconditions
1V3-07_09	2.2	2.1.0-2 Generic FBlock Method test: (1) added
1V3-07_10	2.2	2.6.2-6 Double FBlock Test: (1) added

Changes from 1V3-05 to 1V3-06

Change Ref.	Section	Changes
1V3-06_01	2	2.6.1-1 NWM address initialization test: (1) added
1V3-06_02	2	2.8.3-7 Notification Double Entry Test: (2) and (3) added
1V3-06_03	2	2.8.3-w NotificationMatrix Tests: (1) added
1V3-06_04	2	2.3.3-1 Over-/Undervoltage test: (2) added
1V3-06_05	2	3.x GSI / GSO tests: (1) added
1V3-06_06	2	3.2-w GSO tests: (1) added
1V3-06_07	2	2.6.2-4b Device Integration test (b): (3) added
1V3-06_08	2	2.6.2-4c Device Integration test (c): (1) added
1V3-06_09	2	3.2-3 TEST_GSO_Allocate: (1) added
1V3-06_10	2	Column "1V2-00" deleted; Table splitted in two tables, one for Core Compliance 1V3-01 and one for Core Compliance 1V1-01; all topics for 1V2-00 and 1V3-00 that are covered by 1V3-01 have been deleted

Changes from 1V3-04 to 1V3-05

Change Ref.	Section	Changes
1V3-05_01	2	Device Integration test (b) 2.6.2-4b: 2.6.2-4b (2) added.
1V3-05_02	2	TEST_GSI_DisConnect Device Integration test 3.1-5: 3.1-5 (1) added.
1V3-05_03	2	Node Addressing test 2.7-1: 2.7-1 (1) added.
1V3-05_04	2	Temperature ShutDown Reaction test 2.3.3-5: 2.3.3-5 (1) added.
1V3-05_05	2	Gen 9 added: Revise Detect "sleep mode"
1V3-05_06	2	Critical Voltage test 2.4.2-1: 2.4.2-1(1) added.
1V3-05_07	2	Low Voltage test 2.4.2-3: 2.4.2-3 (5) added.

Changes from 1V3-03 to 1V3-04

Change Ref.	Section	Changes
1V3-04_01	2	Generic FBlock Property resp. Method tests: 2.1.0-x (3) added.
1V3-04_02	2	Message Segmentation Buffer test 2.8.4-7 (3), (4) added.
1V3-04_03	2	Shutdown Start (Execute) test: 2.3.2-3 (2) added.
1V3-04_04	2	NotificationCheck test: 2.8.3-2 (3) added.

Changes from 1V3-02 to 1V3-03

Change Ref.	Section	Changes
1V3-03_01	2	Device Ignore Test: 2.6.2-3b (9),(10) added, superseding (8)
1V3-03_02	2	TEST_GSI_GSO_Identification: 3.0-1 (1) added.
1V3-03_03	2	Notification Matrix Storage test (Slave): 2.8.3-1b (5) added
1V3-03_04	2	TMRBDProcedure Test: 2.5-3 (2) added
1V3-03_05	2	Control message suppression test: 2.6.4-9 (1), (2) added.
1V3-03_06	2	Restart stop test: 2.4.1-1 (1) added.

Changes from 1V3-00 to 1V3-01

Change Ref.	Section	Changes
1V3-01_01	2	Restart continue test: 2.4.1-2 (1) added.
1V3-01_02	2	TEST_GSO_Allocate_NoChAvail: ResourceAllocate: 3.2-5 (1) added.
1V3-01_03	2	TEST_GSO tests: 3.2-y (1) added.
1V3-01_04	2	TEST_GSO tests: 3.2-z (1) added.
1V3-01_05	2	TEST_GSO_Allocate_ReqChAvail: 3.2-6 (1) added.
1V3-01_06	2	TEST_GSI tests: 3.1-x (1) added.
1V3-01_07	2	Generic FBlock Property resp. Method tests: 2.1.0-x (1), (2) added.
1V3-01_08	2	Low Voltage test: 2.4.2-3 (4) added.
1V3-01_09	2	TEST_GSO_SourceDisconnect_Repeat: 3.2-13 (1) added.
1V3-01_10	2	NotificationCheck Test: 2.8.3-2 (1), (2) added.
1V3-01_11	2	Notification Matrix tests: 2.8.3-z (1) added.
1V3-01_12	2	Device Ignore Test (b) 2.6.2-3b (5),(6),(7) added. (3) resp. (4) marked as obsolete and referenced to (5) resp. (6).
1V3-01_13	2	TEST_GSI tests: 3.1-y (1) added.
1V3-01_14	2	LowLevel retries and MidLevel retries and timing: GEN 8 added. Use these values in general for the tester.

Changes from 1V2-03 to 1V3-00

Change Ref.	Section	Changes
1V3-00_01	2	Consolidation of Errata Sheets.
1V3-00_02	2	Slave wake-up 2.1.3-1, Wake-up / Shutdown test 2.1.1-4, Waking slave 2.1.3-4: timeout 2.1.y (1) added.
1V3-00_03	2	Device Integration test (b) 2.6.2-4b: 2.6.2-4b (1) added.
1V3-00_04	2	Reaction of NCE test: 2.4.1-9 (1) added.

Changes from 1V2-02 to 1V2-03

Change Ref.	Section	Changes
1V2-03_01	2	Notification Error Test 2.8.3-10 (2) added.
1V2-03_02	2	RBD Missing TM / Multi-TM 2.5.8 (1)...(4) added.
1V2-03_03	2	Accumulated Short Unlock Test 2.2.1-4 (4) added. Obsolete (2), (3)
1V2-03_04	2	Message Segmentation Buffer test 2.8.4-7 (1) added.

Changes from 1V2-01 to 1V2-02

Change Ref.	Section	Changes
1V2-02_01	2	RBD tests 2.5-x (1) added
1V2-02_02	2	Wakeup after RBD test: 2.5-7 (1) added.
1V2-02_03	2	RBD tests: 2.5-y (1) added.

Change Ref.	Section	Changes
1V2-02_04	2	FB ET wrong figures: GEN 1 added.
1V2-02_05	2	Re-enter Delay test: 2.4.1-11 (2), (3) added.
1V2-02_06	2	Ack/Nack-Test: 2.8.1-1 (1) test case waiver added.
1V2-02_07	2	Segmented Message Sending Test: 2.8.4-2 (1) added.
1V2-02_08	2	Notification tests: 2.8.3-y added.
1V2-02_09	2	Address Reinitialization Test: 2.6.4-4 (1), (2) added.
1V2-02_10	2	Accumulated Short Unlock Test: 2.2.1-4 (1), (2), (3) added.

Changes to 1V2-01

Change Ref.	Section	Changes
1V2-01_00	1, 2	First Issue corresponding to MOST CORE COMPLIANCE TEST SPECIFICATION 1V2 (MOST Spec 2V4).

1 Introduction

This document is a supplement to the MOST CORE COMPLIANCE TEST SPECIFICATION, Rev. 1.1.1 and 1.3.1 [12].

2 Errata

Legend: → means "will be substituted by"

2.1 Core Compliance Test Specification 1.1.1

Chapter / Topic / Testcase	Errata				
GEN 1	<p>Description of „perform wakeup“:</p> <p>a) Tester is TM 1) Generate wake-up event → Generate MOST signal 2) obsolete.</p> <p>b) Tester is not TM 1) Generate MOST signal → Generate wake-up event (e.g. optical slave wakeup including t_restart) 2) obsolete.</p>				
GEN 2	<p>Change Chapter 3.1 General Notes</p> <p>Definition of Procedure Generate Unlock</p> <p>Generate unlocks by destroying the preamble. → To generate an unlock event of predictable duration two requirements must be met. First the preamble at the beginning of at least every third MOST frame has to be made invalid or delayed during the period of unlock. Second a PLL unlock must be avoided.</p>				
GEN 3	<p>Procedure how to handle timer min/max values in test cases with MOST Spec 2V2 devices where those values are not specified:</p> <p>1. If the typical value of MOST Spec 2V3 matches the timer in MOST Spec 2V2 then the test case uses the corresponding min /max or typical value of MOST Spec 2V3.</p> <p>2.</p> <table border="0"> <tr> <td>T_waitnodes</td><td>in 2V2: tests use value of 2V3 :Constraint 100ms) As T_waitnodes (max)</td></tr> <tr> <td>T_shutdown</td><td>in 2V2: tests use value of 2V3:Constraint 15 ms as T_shutdown (max)</td></tr> </table>	T_waitnodes	in 2V2: tests use value of 2V3 :Constraint 100ms) As T_waitnodes (max)	T_shutdown	in 2V2: tests use value of 2V3:Constraint 15 ms as T_shutdown (max)
T_waitnodes	in 2V2: tests use value of 2V3 :Constraint 100ms) As T_waitnodes (max)				
T_shutdown	in 2V2: tests use value of 2V3:Constraint 15 ms as T_shutdown (max)				

Chapter / Topic / Testcase	Errata
	<p>T_shutdownwait in 2V2: 1 sec, tests use values of 2V3 (1/ 2/15 sec) but note this timer is not used in tests for 2V2.</p> <p>T_restart in 2V2: min. 300 ms, tests use values of 2V3 (275/ 300 / 350 ms)</p> <p>T_bypass: this timer is not used in tests for 2V2</p> <p>T_answer in 2V2: tests use value of 2V3: Constraint 50ms) as t_answer (max)</p> <p>T_property : in 2V2, ch. 2.3.2.5.5: tests use value of 2V3:Constraint 200ms as T_ property (max)</p> <p>3. In case DUT (implemented according MOST Spec 2V2) violates a timer the manufacturer has to give more specific information, the test case will be redone with adapted values. This will be remarked in the test report.</p>
GEN 4	<p>MOST Core Compliance Test Spec 1V1-1,p. 25: DUT: NetInterface PowerOff": Effectuate State:</p> <p>Trigger AutoWakeUp via FB ET Switch off tester MOST signal</p> <p>→</p> <p>Switch off tester MOST signal Note: Trigger AutoWakeUp if required and applicable</p>
GEN 5	<p>Over-temperature behaviour seems to be only optional. So for Compliance Testing it shall be treated like "Extended Core", i.e. it must be tested if implemented. The check whether Over-temperature behaviour is supported will be checked by FB.ET which gives an error message in case it is not implemented.</p>
GEN 6	<p>Definition of "Family" in context Core Compliance:</p> <p>Definition of "family" to minimize test effort for product variants with many similarities. A family member is a variation of parameters invariant to MOST Compliance Verification.</p> <p>Such variations can be considered as equivalent level 1 or level 2 changes (MOST Core Compliance Test Spec, chapter "Changes of DUT")</p> <p>In case a change supersedes the already tested range it has to be considered as "level 3" change and consequently cannot be considered as a family member.</p>
GEN 7	<p>In case FB ET returns wrong figures, e.g. wrong voltage levels, the corresponding test result will be indicated as NOT ok.</p> <p>Reason: Due to effort reasons compliance testing relies on FB ET,</p>

Chapter / Topic / Testcase	Errata
	the test house has to inform the supplier correspondingly.
GEN 8	<p>Manufacturer information list already contains LowLevel retries and MidLevel retries and timing. Use these values in general for the tester.</p> <p>Reason: In case of retries of broadcast messages the values have to be accurate.</p>
GEN 9	<p>(1) (1V3-05_05) Revise Detect "sleep mode"</p> <p>i) Delete: If DUT starts generating MOST signal, before the timeout (specified by manufacturer) has been expired, the test will be stopped ("DUT not ok: No SleepMode possible").</p> <p>ii) Add at a) DUT is power master: If DUT supports SleepMode: If DUT generates MOST signal at Tx although timeout expires, the test will be stopped ("DUT not ok: No SleepMode possible").</p> <p>iii) Add For MOST50 ePhy no monitoring of activity</p>
2.1.1-1 <i>2.1.1-1 Signal-on test</i>	<p>(1) Add block after "DUT in SleepMode": Wait 10 sec</p> <p>Reason: Depending on the time after detection of sleep mode the wakeup time may vary due to existing energy in the system.</p>
2.1.1-5 <i>2.1.1-5 Bypass Test</i>	<p>(1) Adapt Device type: All devices that are wakeable via MOST signal → All devices that are wakeable via MOST signal except PM</p>
2.1.1-x <i>2.1.1-1 Signal-on test</i> <i>2.1.1-5 Bypass test</i>	<p>(1) These tests have the pre-condition "DUT in SleepMode".</p> <p>For devices that do not implement AbilityToWake-Function (despite Coordination Area), the following holds</p> <p>I. The answer to AbilityToWake.SetGet need not be evaluated.</p> <p>II. a) The supplier must specify how to suppress a wake-up condition for shutdown procedure.</p> <p>b) If such a suppression is not applicable in the scope of compliance testing, the precondition SleepMode cannot be established, and the test case will be passed as OK. This has to be remarked in the test report.</p>

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2.1.2-4 <i>2.1.2-4 SBC register check test</i>	<p>(1) Adapt value of interest: t_boundary, SBC register → change of NetInterface state</p> <p>Reason: Neither the t_boundary itself nor the SBC register value is in the focus of this test, rather it is the change of NetInterface state.</p> <p>(2) Adapt test description: The DUT has to change to NormalOperation within t_Boundary. → The DUT has to change to NormalOperation</p>
2.1.y <i>2.1.3-1 Slave wake-up 2.1.1-4 Wake-up / Shutdown test 2.1.3-4 Waking slave timeout</i>	<p>(1) Replace t_manufacturer_0x00 by t_deadlock_mid</p>
2.2.1-4 <i>2.2.1-4 Accumulated Short Unlock Test</i>	<p>(1) Generate single unlock (t=0.2 x t_Unlock_typical) → Generate single unlock (t= 0.5 x t_Unlock_min)</p> <p>(2) n >= 5? → n >= 2? (<i>obsolete</i>)</p> <p>(3) n = 10? → n = 7? (<i>obsolete</i>)</p> <p>(4) The tester generates an unlock sequence (consists of a series of unlocks (0.5 x t_Unlock_min) and locks (0.8 x t_Lock_min). The DUT must not switch off MOST signal too early (< 110ms) or too late (> 300ms).</p>

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	<p>2.2.1-4</p> <pre> graph TD Start([DUT in NormalOperation]) --> GenSeq[Start generating unlock sequence] GenSeq --> Init[Start t_test_min := 110ms; Start t_test_max := 300ms] Init --> Join(()) Join --> MOST{MOST signal off?} MOST -- yes --> T_min{t_test_min expired?} MOST -- no --> T_max{t_test_max expired?} T_min -- yes --> DUT_ok([DUT ok]) T_min -- no --> DUT_not_ok_2([DUT not ok (2)]) T_max -- yes --> DUT_not_ok_1([DUT not ok (1)]) T_max -- no --> Join </pre> <p>Note: Unlock sequence consists of periodic sequence of 30ms unlock (0.5 x t_{Unlock_min}) and 60ms lock (0.8 x t_{Lock_min}).</p> <p>1. Unlock 1. Pause 2. Unlock 2. Pause 3. Unlock 3. Pause 4. Unlock 4. Pause ...</p> <p>0 30 60 90 120 150 180 210 240 270 300 330 360 ...</p> <p>← DUT not ok → DUT ok → DUT not ok → ...</p>
<p>2.3.1-3</p> <p>2.3.1-3 Timeout execute / Timeout suspend</p>	<p>(1) Describe behaviour if "DUT has to perform NO (check every 500 ms) fails. This leads to: DUT not ok (5): The DUT fails to perform NO during t_RetryShutDown.</p> <p>(2) change in flow chart</p> <p>Timeout t_suspend → Timeout t_suspend_min</p>
<p>2.3.2-2</p> <p>2.3.2-2 Shutdown.Start.Query</p>	<p>(1) The check whether "DUT supports shutdown suspend behaviour" will be checked by FB.ET which gives an error message in case it is not implemented.</p>
<p>2.3.2-3</p>	<p>(1) Start (2 x t_ShutDownWait) → Start (t_ShutDownWait_max)</p>

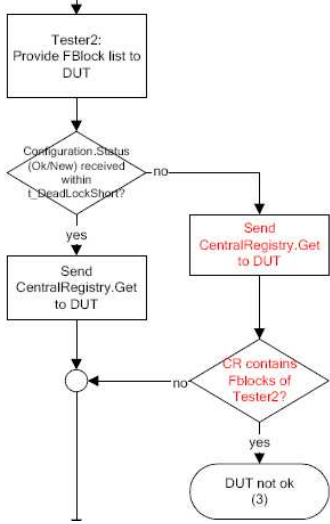
Chapter / Topic / Testcase	Errata
2.3.2-3 Shutdown.Start(Execute) test	
2.3.3-1 2.3.3-1 Over-/Undervoltage test	(1) This test case is not applicable for PowerMaster (because of restart after t_restart)
2.3.3-4 2.3.3-4 Over temperature signal off test	(1) Note that there might be DUT applicative cases (e.g. telephone emergency call) where the DUT might not ignore any wake-up event. Delete in test description last part of last sentence "and ignore any wake-up event." (2) Delete last part of test (beginning with generate Wake-up event) ; consequently DUT not ok(4) is obsolete. (3) The check whether communication between Tester 1 and Tester 2 is possible has to be performed within 500 ms after Netblock.Shutdown.Result (0x03) in order to avoid difficulties with a DUT that has identical temperature levels for shutdown level and dead level.
2.4.1-2 2.4.1-2 Restart continue test:	(1) Add in note: In order to assure that wakeup duration has expired the tester is allowed to wait additionally for t_deadlock_mid. Reason: Test case will be held independent from manufacturer timing through waiting for stop event longer, i.e. additionally for t_deadlock_mid.
2.4.1-9 2.4.1-9 Reaction on NCE (resp. Appendix 1: Requirements and measurement uncertainty for test equipment and setup)	(1) Tolerance T_WaitAfterNCE to 5ms → 30 ms Reason: System Control Message (2) t_WaitAfterNCE(max) will not be checked, use t_DeadlockShort instead. Reason: No system-relevant constraint.
2.4.1-11 2.4.1-11 Re-enter Delay test	(1) Add in note: "The DUT must not switch off its light during the test". Reason: Otherwise the MPR –register would not be valid anymore. (2) Delete check for t_bypass_max, Reason : Only t_bypass_min can be checked within this test focus. (3) Meaning of "DUT not ok (1): The DUT enters the ring too late" is obsolete.
2.4.2-1 2.4.2-1 Critical Voltage test	(1) (1V3-05_06) Adapt note: A test message has to be sent at every 0.5V step. → A test message has to be sent at every step.
2.4.2-3	(1) generate (external)wake-up event obsolete

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2.4.2-3 Low Voltage test	<p>(2) Inquiry "MOST signal off" obsolete</p> <p>(3) DUT not ok (3) obsolete</p> <p>(4) „DUT not ok (1): DUT disturbs communication before U_{Low} is reached“ → DUT disturbs communication before U_{Low} is reached or before MOST signal is switched off</p> <p>Reason: The error message „DUT not ok (1): DUT disturbs communication before U_{Low} is reached“ is mistakable if voltage is below U_{Low}, but MOST signal is still on and communication is disturbed.</p> <p>(5) (1V3-05_07) Delete note: "Tolerances of manufacturer have to be considered. If U_{Low} has hysteresis, the higher value has to be used!"</p>
2.5-2 2.5-2 RBD procedure test	<p>(1) Change in flow chart: In second part after "trigger RBD", insert switch off MOST signal and wait until MOST signal on from DUT. Add note correspondingly "MOST signal must be received until t_{DeadlockShort}"</p> <p>(2) T_DiagResult does not exist.</p> <p>(3) Add comment "Tester must switch on light only after DUT" at statement "Trigger RBD" for part "closed ring"</p> <p>(4) Add note: „RBD procedure test: In case of a Slave-DUT, "DUT not OK(4): DUT stops generating MOST signal" will only be obtained after a second light off “.</p>
2.5-3 2.5-3 TM RBD procedure test	<p>(1) Tester1 has to behave like normal MOST device regarding RBD. t_{diag_master} of MOST tester = t_{diag_master_min}</p>
2.5-8 2.5.8 RBD Missing TM / Multi-TM	<p>(1) Change in flow chart: Perform shutdown → Switch off MOST signal.</p> <p>(2) Add comment "Tester must switch on light only after DUT" at statement "Trigger RBD"</p> <p>(3) Add note „In case of a Slave-DUT, the loop "MOST signal off" will only be left after a second MOST signal off, as with the first MOST signal off still the time t_{off}- (because of reconfiguration) elapses and RBD is not finished yet.“</p> <p>(4) Adapt note: Slave: Result = "No TM" TM: Result = "Multimaster"</p>

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	<p>→ DUT contains TM: Result = "Multimaster" Otherwise Result = "No TM" or "NoLock".</p> <p>(5) Delete note "If DUT is slave device, it will switch off MOST signal once for a short period of time (t_Restart) because of reconfiguration during "Diag_M2". In that case, the loop has to continue until MOST signal is switched off the second time." Reason: Tester switches off its light when DUT switches off its light so there is no "second time".</p> <p>(6) "Perform wake-up" → "perform wake-up" after t_diag_slave(max) Reason: Otherwise too early and RBD might not be finished yet.</p> <p>(7) 1. Test case waiver for DUT = TM. Reason: The flow chart does not hold for DUT=TM. The described test setup will lead to "diagnosis ready" and MOST signal remains on.</p> <p>(8) Rename test "RBD Missing TM"</p>
<p>2.5-x</p> <p><i>2.5.2 RBD procedure test 2.5-3 TM RBD procedure test, 2.5-8 RBD missing TM / MultiMaster test</i></p>	<p>(1) Close the ring and perform wakeup → Close the ring and perform wakeup and wait t_Diag_Restart_Max in between.</p>
<p>2.5-y</p> <p><i>2.5.2 RBD procedure test, 2.5-3 TM RBD procedure test</i></p>	<p>(1) Add block before second "trigger RBD" : "switch off power, DUT in normal operation" Reason: better decoupling as each test actually covers 2 test cases</p>
<p>2.6.1-1</p> <p><i>2.6.1-1 NWM address initialization test</i></p>	<p>(1) Adapt note at block "Get address from DUT": FBlockIDs.Get could be used to get the address of the DUT. → FBlockIDs.Get (received from DUT) could be used to get the address of the DUT.</p>
<p>2.6.2-1</p> <p><i>2.6.2-1 FBlock polling test</i></p>	<p>(1) 1. Substitute note: "The DUT has to check the devices in their ring position" → "If the DUT polls Tester 1 before Tester2, the roles of tester 1 and tester 2 have to be swapped."</p>

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2.6.2-3a <i>2.6.2-3a Device Ignore Test (a)</i>	<p>(1) Add note at inquiry "Receiving Configuration.Status(Ok) within t_DeadLockShort?": DUT is allowed to send Configuration.Status(NotOk) or start a rescan before broadcasting Configuration.Status(Ok).</p>
2.6.2-3b <i>2.6.2-3b Device Ignore Test (b)</i>	<p>(1) In flow Chart Loop = 2 → Loop=1 Note: Loop starts with 1 counting retries.</p> <p>(2) This Test case is not applicable for Early Implementations and MOST Spec 2V2 devices (because existent implementations do not consider the corresponding counter of MOST Spec but are based on a timer).</p> <p>(3) Inquiry t_DelayCfgRequest1(min) <= t_test <= t_DelayCfgRequest1(max) → Inquiry t_WaitForAnswer (min) + T_DelayCfgRequest1(min) <= t_test <= t_WaitForAnswer (max) + T_DelayCfgRequest1(max) See (5)</p> <p>(4) Inquiry t_DelayCfgRequest2(min) <= t_test <= t_DelayCfgRequest2(max) → Inquiry t_WaitForAnswer (min) + T_DelayCfgRequest2(min) <= t_test <= t_WaitForAnswer (max) + T_DelayCfgRequest2(max) See (6)</p> <p>(5) 1. Inquiry t_WaitForAnswer (min) + T_DelayCfgRequest1(min) <= t_test <= t_WaitForAnswer (max) + T_DelayCfgRequest1(max) → Inquiry T_DelayCfgRequest1(min) <= t_test <= t_WaitForAnswer (max) + T_DelayCfgRequest1(max)</p> <p>(6) Inquiry t_WaitForAnswer (min) + T_DelayCfgRequest2(min) <= t_test <= t_WaitForAnswer (max) + T_DelayCfgRequest2(max) → Inquiry t_DelayCfgRequest2(min) <= t_test <= t_WaitForAnswer (max) + T_DelayCfgRequest2(max)</p>

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	<p>(7) Admit 20 resp. 19 loops (retries). Reason: MOST Spec not completely accurate in this definition (MOST Spec Rev. 2.5, section 3.8).</p> <p>(8) (1V3-03_01) 2.6.2-3b (Device Ignore Test) Perform Wakeup -> DUT checks Tester1 within t_DeadLockMid? -></p> <p>→ Perform Wakeup -> Wait t_DeadLockMid -> Tester 1: Open bypass -> DUT checks Tester1 within t_DeadLockMid? -></p> <p><u>Reason:</u> In multi-node devices, at the startup there may happen much traffic with many FBlocks. This may lead to infringement of time intervals due to OEM Low level retry requirements.</p> <p>(9) (1V3-03_01) Procedure (superseding (8)): Perform Wakeup -> DUT checks Tester1 within t_DeadLockMid? -> → Perform Wakeup -> Wait manufacturer dependent t_WaitForApplication -> Tester 1: Open bypass -> DUT checks Tester1 within t_DeadLockMid? -></p> <p><u>Reason:</u> In multi-node devices, at the startup there may happen very much traffic with many FBlocks. This may lead to infringement of time intervals due to OEM Low level retry requirements.</p> <p>(10) (1V3-03_01) Experimental Setup: a) tester setup #1 → tester setup #2 b) tester #2 behaves like normal MOST device.</p> <p><u>Reason:</u> In case of MOST signal wake-up, tester #2 is needed to perform the wake-up.</p>
2.6.2-4b 2.6.2-4b Device Integration test (b)	<p>(1) Add note at block "Tester1: Send CentralRegistry.Get to DUT":</p> <p>- Wait for manufacturer dependent time until all DUT FBlocks have registered.</p>

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	<p>(2) (1V3-05_01)</p> <p>Add after 2nd inquiry « Configuration.Status (Ok/New) received Within t_DeadLockShort?» in path “no” block “Send CentralRegistry.Get to DUT” and check for consistency and continue the subsequent checks.</p>  <pre> graph TD Start(()) --> Tester2[Tester2: Provide FBlock list to DUT] Tester2 --> Decision1{Configuration.Status (Ok/New) received within t_DeadLockShort?} Decision1 -- yes --> Send1[Send CentralRegistry.Get to DUT] Decision1 -- no --> Send2[Send CentralRegistry.Get to DUT] Send1 --> Decision2{CR contains Fblocks of Tester2?} Send2 --> Decision2 Decision2 -- yes --> End([DUT not ok (3)]) Decision2 -- no --> Decision1 </pre>
<p>2.6.2-6</p> <p>2.6.2-6 Double FBlock Test</p>	<p>(1) Inquiry “DUT has checked Tester 2 before Tester 1”, DUT not ok (6) are obsolete.</p> <p>(2) Change: DUT not ok (4) → DUT ok (2): The DUT has passed the test, no tester stored in CR.</p>
<p>2.6.4-1</p> <p>2.6.4-1 Address Initialization Test</p>	<p>(1) Before disconnecting the DUT from Power a block is missing “Wait a period t_DeadlockMid” Reason: Wait the manufacturer dependent time T_PwrSwitchOff_Delay.</p> <p>(2) Change 3rd inquiry “Address of DUT = test” → “Address of DUT valid, i.e. address in static or dynamic address range” Reason: The DUT might not store a previously given address persistently and then may have any “valid” address.</p> <p>(3) Add in first note: “The case that the address cannot be set will be tolerated.”</p>
<p>2.6.4-3</p> <p>2.6.4-3 NWM Address Storage Test</p>	<p>(1) Add Note: The test has to be performed three times: 1. with sending Config.Status (OK) 2. with sending Config.Status (New) with empty FBlock List</p>

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	<p>3. with sending Config.Status (Invalid) with an FBlock not implemented in the DUT (Use FBlockID.InstID= 0xC8.0x01)</p> <p>(2) Add note for testing condition:</p> <p>Between test loops, switch off light without shutdown.start (execute), i.e. irregular shutdown.</p> <p>Reason: MOST Spec 2V4, ch. 3.3.4.1.4 Persistence of the Dec. Reg.</p> <p>(3) Last inquiry in the right branch is obsolete and yield DUT (ok).</p> <p>(4) Last inquiry in the left branch: a) Wait t_DeadLockShort b) Tester 1 received request from DUT → Tester 1 received request with correct logical address of the tester?</p> <p>Reason: In MOST Spec it is not specified when a device is able to receive a message after entering the network. Through a broadcast it cannot be detected whether the message is received.</p>
2.6.4-4 <i>2.6.4-4 Address Reinitialization Test</i>	<p>(1) Delete note "refer to test case 2.6.4-3".</p> <p>(2) Inquiry "DUT supports DR" will be done already during system start-up (If the DUT does not request the CR after Config (OK) within t_DeadLockShort, DR is not supported by the DUT.)</p>
2.6.4-8 <i>2.6.4-8 FBlock Response Time test</i>	<p>(1) T_Answer → T_Answer_max Reason: Due to "mental consistency" as T_Answer is a constraint given only its max value.</p>
2.6.4-9 <i>2.6.4-9 Control message suppression test</i>	<p>(2) (1V3-03_05) Note: Tester (NWM) must not send FBlockIDs.Get to DUT. → Note: Tester (NWM) must not send any message to the DUT.</p>
2.7-1 <i>2.7-1 Node Addressing test</i>	<p>(1) (1V3-05_03) Send message → Send GroupAddress.Get</p>
2.8.1-1 <i>2.8.1-1 Ack/Nack-Test</i>	<p>(1) <i>Waiver of Test case for Ack/Nack test.</i></p> <p>Reason: timer t_MsgResponse not existent in MOST Spec 2V3, 2V4;</p>

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	Almost every device fails this test. Especially devices, which are designed for high message performance, do not constantly keep with the pace for 5 sec. This is mainly because the test flow is sending messages with a higher frequency than requested by the MOST Spec and for a time which is longer than the buffers of most devices are designed for.
2.8.3-10 <i>2.8.3-10 Notification Error Test</i>	(1) Add note: In case all functions support notification go directly to "Send Notification.Set(ClearAll) to DUT".
2.8.3-1a <i>2.8.3-1a Notification Matrix Storage test (NWM)</i>	(1) 1. Timer t_Response is t_property. (also to be included in list of timers in Appendix 1) (2) Error received or timeout response? → Notification.Error received within t_Property (3) Error (0x20,0x20) or Error(0x03) → Notification.Error (0x20,0x20) or Notification.Error(0x03) (4) Inquiry "Notification.Error(0x41) is obsolete and leads directly to DUT(NotOk)
2.8.3-1b <i>2.8.3-1b Notification Matrix Storage test (Slave)</i>	(1) Timer t_Response is t_property. (also to be included in list of timers in Appendix 1) (2) Error received or timeout response? → Notification.Error received within t_Property (3) Error (0x20,0x20) or Error(0x03) → Notification.Error (0x20,0x20) or Notification.Error(0x03) (4) Inquiry "Notification.Error(0x41) is obsolete and leads directly to DUT(NotOk) (5) (1V3-03_03) Refer to left branch. There is the following procedure: 1 send Configuration.Status(NotOk) to DUT 2 Request Notification Matrix from DUT → 1 send Configuration.Status(NotOk) to DUT 2 wait for t_DeadLockShort 3 send Configuration.Status(OK) to DUT 4 Request Notification Matrix from DUT" Reason: The previous procedure is not possible due to the fact that the DUT is

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	not allowed to communicate within Configuration Status NotOk.
2.8.3-2 2.8.3-2 NotificationCheck Test	<p>(1) DUT responds with empty list? → DUT responds with empty list or any NotificationCheck error? Reason: The device need not respond with an empty list to NotificationCheck.Get for a DeviceID that is not notified but may respond with the error message e.g. 0x20.0x08.</p> <p>(2) Delete note in flowchart: "Note: Test has to be performed with every single FBlock of the DUT." Reason: Test case not applied for Netblock.</p>
2.8.3-7 2.8.3-7 Notification Double Entry Test	<p>(1) Substituted by 2.8.3-7 Notification Double Entry Test of MOST Core Compliance Test Spec 1V2.</p>
2.8.3-x 2.8.3-1a Notification Matrix Storage test (NWM), 2.8.3-1b Notification Matrix Storage test (Slave), 2.8.3-7 Notification Matrix Double Entry test, 2.8.3-10 Notification Error	<p>(1) The exception not to test Netblock holds only for Early Implementors.</p> <p>(2) Add note in description: "All FBlocks have to be tested within one Test loop (no separate test loop for every FBlock) to increase stress of DUT."</p>
2.8.3-y 2.8.3-1a Notification Matrix Storage test (NWM), 2.8.3-1b Notification Matrix Storage test (Slave), 2.8.3-2 NotificationCheck test, 2.8.3-7 Notification Matrix Double Entry test, 2.8.3-10 Notification Error	<p>(1) Add note at beginning (DUT performs Normal operation) "Wait for manufacturer dependent time" Reason: Property not available before.</p>
2.8.3-z 2.8.3-1a Notification Matrix Storage test (NWM), 2.8.3-1b Notification Matrix Storage test (Slave), 2.8.3-7 Notification Matrix Double Entry test, 2.8.3-10 Notification Error	<p>(1) This test case is not performed for Netblock. Reason: The exception not to test Netblock is valid generally (and not only for Early Implementations).</p>
2.8.4-1 2.8.4-1 Segmented Message Rejection Test	<p>(1) Waiver of test case for Early Impl. Reason: NetService Implementation prior 1.10.x does not contain segmentation error.</p> <p>(2) Waiver of test case for MOST 2V2 devices Reason: NetService Implementation prior 1.10.x does not contain</p>

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	segmentation error.
2.8.4-2 <i>2.8.4-2 Segmented Message Sending Test</i>	<p>(1) Trigger the segmented message via FBlock ET (EchoMessage). The message must consist of at least four segments. → Trigger the segmented message via FBlock ET (EchoMessage). Depending on the buffer size given by FB ET the message consists of 2, 3 or 4 segments, 2 segments (size=13..22), 3 segments (23..33), 4 segments (> 33).</p> <p>Reason: Improvement so that test case is also able to consider a small buffer size.</p> <p>(2) Add to note: In case the MessageBufSize is dynamic (indicated by value 0) the length to be sent by the tester is 44 bytes (4 segments).</p>
2.8.4-3 <i>2.8.4-3 Message Segmentation Error Test</i>	<p>(1) Waiver of test case for Early Impl. Reason: NetService Implementation prior 1.10.x does not contain segmentation error.</p> <p>(2) Waiver of test case for MOST 2V2 devices Reason: NetService Implementation prior 1.10.x does not contain segmentation error.</p>
2.8.4-7 <i>2.8.4-7 Message Segmentation Buffer test</i>	<p>(1) Use ET.EchoMessage(0x208).Data with correct InstId for the DUT (instead of NetBlock.FBlockIDs.Get())</p>
2.8.4-7 <i>2.8.4-7 Message Segmentation Buffer test</i>	<p>(1) Last Inquiry: Error received from DUT → Segmentation error received from DUT</p> <p>(2) Add note at last inquiry: In case of dynamic buffer size, Segmentation Error (0x02) is also allowed. In this case the test must be aborted.</p>

2.2 Core Compliance Test Specification 1.3.1

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GEN 1	<p>Guideline „Improved Product ID Handling“ The MOST Compliance process requires a unique ProductID in order to enable a distinction of MOST products. The following guideline gives some advice how to handle it.</p> <p>I) Use cases A) A device will get a new SW ==> The Product ID may stay the same. B) Different Coding of the device (parts of the SW will be set to "passive") ==> The Product ID may stay the same. C) A device will be used in several countries (The same (MOST-)HW with different "Non MOST HW") ==> The Product ID should reflect the different variants. D) The branding of the device has changed (e.g. new surface on the screen) ==> The Product ID should reflect the different variants.</p> <p>IIa) Example for appropriate ProductIDs: 1) A unique ProductID should consist of at least 4 characters e.g.: 1234, ABC1, 0816, ABCD</p> <p>IIb) Examples for inappropriate ProductIDs: 1) n.a. 2) STD_MIB 3) Gateway</p>
GEN 2	<p>Adapt section "Multi Node Devices": A DUT with more than one MOST node is defined as Multi Node Device → A multi node device is a device with an external MOST interface which is connected to more than one MOST node. Remark: When dealing with devices with several external MOST interfaces each MOST interface shall be treated as a separate DUT. Realize, one of those interfaces may be a Multi Node Device.</p>
GEN 3	<p>General Notes: If it is proven that the DUT is not possible to reach preconditions (such as modulated signal, lock, scan performed, Config ok), the test case will be marked as "DUT not Ok".</p>
2.1.0-2 <i>2.1.0-2 Generic FBlock Method test</i>	<p>(1) Add note: During this test case it must be considered that sending of an error is optional in function DTCP_Control.</p>
2.1.1-5 <i>2.1.1-5 Bypass Test</i>	<p>(1) Adapt Device type: All devices → All devices that are wakeable via MOST signal except PM</p>

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2.3.3-1 <i>2.3.3-1 Over-/Undervoltage test</i>	(2) Add note: The Trigger AutoWakeUp must not be applied.
2.6.1-1 <i>2.6.1-1 NWM address initialization test</i>	(1) Adapt note at block "Get address from DUT": FBlockIDs.Get could be used to get the address of the DUT. → FBlockIDs.Get (received from DUT) could be used to get the address of the DUT.
2.6.2-3a <i>2.6.2-3a Device Ignore Test (a)</i>	(1) Add note at inquiry "Receiving Configuration.Status(Ok) within t_DeadLockShort?": DUT is allowed to send Configuration.Status(NotOk) or start a rescan before broadcasting Configuration.Status(Ok).

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<p>2.6.2-4b</p> <p>2.6.2-4b Device Integration test (b)</p>	<p>(3)</p> <p>2.6.2-4b</p> <p>Specification Document</p>

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	<p style="text-align: right;">2.6.2-4c</p> <div style="display: flex; justify-content: space-between;"> <div style="width: 45%;"> <p>Note: Tester2 has to send FBlockIDs.Status (FBlockIDList). The FBlockIDList has to contain all FBlocks provided before and one new FBlock.</p> </div> <div style="width: 45%;"> <p>Note: Tester2 has to be able to provide more FBlocks than storable into the CR of the DUT (detectable via FBlock ET.CentralRegistrySize.Get). Potentially, the DUT stores own FBlocks into the CR which will reduce available size of CR.</p> </div> </div> <pre> graph TD Start([continued from 2.6.2-4b]) --> Join1(()) Join1 --> Tester2[Tester2: Provide one new FBlock to DUT] Tester2 --> StartTimer[Start t_DeadLockShort] StartTimer --> Join2(()) Join2 --> D1{Configuration.Status (New, DeltaFBlockList) received?} D1 -- yes --> D2{DeltaFBlockList contains new FBlock of Tester2?} D2 -- yes --> Join1 D2 -- no --> Join2 D1 -- no --> D3{Configuration.Status (Invalid, DeltaFBlockList) received?} D3 -- yes --> D4{DeltaFBlockList contains any FBlock of Tester1 or Tester2?} D4 -- yes --> Mark[Mark unregistered FBlock(s) of Tester1 and Tester2] Mark --> Join2 D4 -- no --> Join2 D3 -- no --> D5{Timeout t_DeadLockShort?} D5 -- yes --> Send[Send CentralRegistry.Get to DUT] Send --> D6{CR contains all successfully registered FBlocks of both Tester?} D6 -- yes --> DUTok([DUT ok]) D6 -- no --> DUTnotok([DUT not ok]) D5 -- no --> DUTnotok </pre> <p>Note: In case DUT sends FBlockIDs.Get to Tester1 or Tester2 during test, the Tester has to respond with FBlock list that contains all FBlocks the tester has indicated to the DUT at this time. This has to be done to prevent the DUT from deleting FBlocks of Tester1 or Tester 2 from CR.</p> <p>Note: In case DUT tries to change the InstID of an FBlock of Tester1 or Tester2, it has to be accepted and performed by the Tester according to MOST Specification.</p> <p>Note: The CR has to contain all FBlocks of Tester1 and Tester2 that have been registered successfully. It must not contain FBlocks of Tester1 or Tester2 that are not indicated by DUT with Configuration.Status(New, DeltaFBlockList) or which have been unregistered before by the DUT with Configuration.Status(Invalid,...). Exception: First FBlock provided by Tester1 at initial scan will not be indicated with Configuration.Status(New, DeltaFBlockList) but has to be stored in CR.</p> <p>Note: Use this information for action box „CR contains all successfully registered FBlocks of both Tester?“. </p>

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2.6.2-6 <i>2.6.2-6 Double FBlock Test</i>	<p>(1) Adapt note: Tester must not process FBlockIDs.SetGet from DUT. It has to continue using the same (old) InstID. → Both testers answer regularly to requests to their NetBlocks without changing their FBlock configuration.</p> <p><i>Reason:</i> <i>To avoid change of test focus which does not cover a check for “discard device” during compliance testing.</i></p>
2.6.2-7 <i>2.6.2-7 Config(New) Order Test</i>	<p>(1) Adapt note: Before broadcasting Configuration.Status(Ok), the DUT must not broadcast any other Configuration.Status message. → Before broadcasting Configuration.Status(Ok), the DUT must not broadcast Configuration.Status(NewExt / New / Invalid).</p> <p><i>Reason:</i> <i>Before broadcasting Configuration.Status(Ok), a broadcast of Configuration.Status(NotOk) may occur, it is only forbidden to broadcast Configuration.Status(NewExt / New / Invalid).</i></p>
2.6.4-9 <i>2.6.4-9 Control Message Suppression Test</i>	<p>(1) a) Delete: Exception: DUT is allowed to send debug messages (to 0x0FF0).</p> <p>b) Adapt Inquiry: Received any control message from DUT? → Received NetBlock.FBlockIDs.Status(FBlockIDList) from DUT?</p> <p><i>Remark:</i> <i>DUT manufacturer list with list of FBlocks FBlocks (=“special applications”) seems to be not very practical.</i></p>
2.8.3-7 <i>2.8.3-7 Notification Double Entry Test</i>	<p>(2) Add note at inquiry “Double entries detected?”: No entry of tester 1 is tolerated and does not lead to NotOK (3). Reason: Clarification necessary to avoid ambiguous understanding</p> <p>(3) 1.) Adapt command box: Start t_Property → Start t_WaitforProperty</p> <p>2) Adapt Inquiry t_Property expired? → t_WaitforProperty expired?</p> <p><i>Reason: This test case covers all FBlocks in one loop. A DUT with many notifiable FBlocks and many FktIDs is not able to react within t_Property.</i></p>
2.8.3-w <i>2.8.3-1a Notification Matrix Storage test (NWM), 2.8.3-1b Notification Matrix Storage</i>	<p>(1) Add note at inquiry “Notification Matrix empty?”: Each notified function of each FBlock of DUT must be checked.</p>

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test (Slave), 2.8.3-7 Notification Matrix Double Entry test,	Reason: Avoid ambiguity.
3.2-3 3.2-3 TEST_GSO_Allocate	<p>(1)</p> <p>Add note to test description: Already allocated sources should be deallocated before sending Allocate.StartResultAck to a new SourceNr.</p> <p><i>Reason:</i> <i>Test case is performed in loops if every source of the DUT. If the DUT has many FBlocks with many sources, it is possible, that a source responds with an error message to Allocate.StartResultAck, because there is still another source allocated.</i></p>
3.2-14 3.2-14 TEST_GSO_SourceActivity	<p>(1)</p> <p>Adapt: SourceActivity.ErrorAck (SenderHandle='0x01', ErrorCode='0x03', ErrorInfo=_) → SourceActivity.ErrorAck (SenderHandle='0x01', ErrorCode='_', ErrorInfo=_)</p> <p><i>Reason: Improvement; there is no distinction of ErrorCode necessary.</i></p>
3.2-w 3.2-8 TEST_GSO_DeAllocate 3.2-9 TEST_GSO_DeAllocate_Repeate 3.2-14 TEST_GSO_SourceActivity	<p>(1)</p> <p>Delete note: "If DUT responds with xxx.ProcessingAck, the tester has to repeat xxx.StartResultAck after t_CM_DeadLockPrev (max 10 retries)"</p> <p><i>Reason:</i> Not adequate any more as t_CM_DeadLockPrev has been substituted by t_deadlock_short. (refer to change 3.x)</p>
3.x 3.1-1 to 3.1-6 TEST_GSI_... 3.2-3 to 3.2-14 TEST_GSO_...:	<p>(1)</p> <p>t_CM_DeadLockPrev → t_deadlock_short</p> <p><i>Reason:</i> Manufacturer is allowed to adapt t_deadlock_short in case the method replies with xxx.ProcessingAck, as it may happen that the DUT responds with ProcessingAck to StartResultAck before sending ResultAck. Because of this reason the delay to ResultAck can exceed the timer t_CM_DeadlockPrev (max 1000ms)</p>
Appendix 2	<p>(1)</p> <p>Delete reference to FB ET 0x214 ManufacturerTimings.</p> <p><i>Reason: Function ManufacturerTimings is obsolete.</i></p>

Notes: